RX Family

Firmware Update Module Using Firmware Integration Technology

Introduction

This application note describes the firmware update module using Firmware Integration Technology (FIT). The module is referred to below as the firmware update FIT module.

By using the FIT module, users can easily incorporate firmware update functionality and secure boot functionality into their applications. This application note explains how to use the firmware update FIT module and how to incorporate its API functions into user applications.

The release package associated with this application note includes a demo project. You can confirm the basic operation of the firmware update functionality by following the steps described in section 5, Demo Project, to build an environment to run the demo.

Operation Confirmation Devices

RX24T Group

RX26T Group

RX65N, RX651 Group

RX66N Group

RX66T Group

RX660 Group

RX671 Group

RX72M Group

RX72N Group

When using this application note with other Renesas MCUs, careful evaluation is recommended after making modifications to comply with the alternate MCU.

Related Application Notes

Application notes related to this application note are listed below. Refer to them in conjunction with this document.

- Firmware Integration Technology User's Manual (R01AN1833)
- RX Family Adding Firmware Integration Technology Modules to Projects (R01AN1723)
- RX Family Board Support Package Module Using Firmware Integration Technology (R01AN1685)
- RX Family Flash Module Using Firmware Integration Technology (R01AN2184)
- RX Family SCI Module Using Firmware Integration Technology (R01AN1815)
- RX Family BYTEQ Module Using Firmware Integration Technology (R01AN1683)

Target Compliers

- C/C++ Compiler Package for RX Family from Renesas Electronics
- GCC for Renesas RX
- IAR C/C++ Compiler for RX

For details of the environments on which operation has been confirmed, refer to 6.1, Confirmed Operation Environments.

Contents

1. Overview

1.1 About the Firmware Update Module

A firmware update is a process in which a device overwrites its own firmware, the software that controls the device's hardware, with a new version of the firmware (called the "update image" in this document) obtained through unspecified means. Firmware updates may be applied to fix bugs, add new functions, or improve performance.

The firmware update module is middleware that, when firmware update functionality is added to the user's system, provides the following functionality as its components:

- Functionality for importing the update image to the MCU via a communication interface
- Functionality for validating the update image (ECDSA NIST P-256 and SHA256 are used for validation.)
- Functionality for programming the update image to the on-chip flash memory (self-programming)
- Functionality for activating the update image

Generally, a firmware update system comprises two programs: an application program providing firmware update functionality and a bootloader providing secure boot functionality used to validate the first program.

The bootloader functionality is essential to the proper functioning of the firmware update. It guarantees that the sequence of processing that composes the firmware update, including validation of the update image, is legitimate.

The firmware update module for the RX Family provides functionality for the following three firmware update methods.

- Dual-bank method
- Linear mode partial update method
- Linear mode full update method

A tool (Renesas Image Generator) for creating firmware images is provided as a utility. Renesas Image Generator can generate the following types of images for use by the firmware update module.

- Initial image: An image file containing the bootloader and application program that is programmed using Flash Writer at the time of initial system configuration (extension: mot).
- Update image: An image file containing the firmware update (extension: rsu).

1.2 Configuration of Firmware Update Module

Figure 1.1 shows the configuration of the modules in the bootloader and application program incorporating the firmware update module, and Table 1.1 lists the modules used in the bootloader and application program.

The update image received by the communication interface is self-programmed to the on-chip flash memory of the target device via the firmware update module and the flash memory driver.

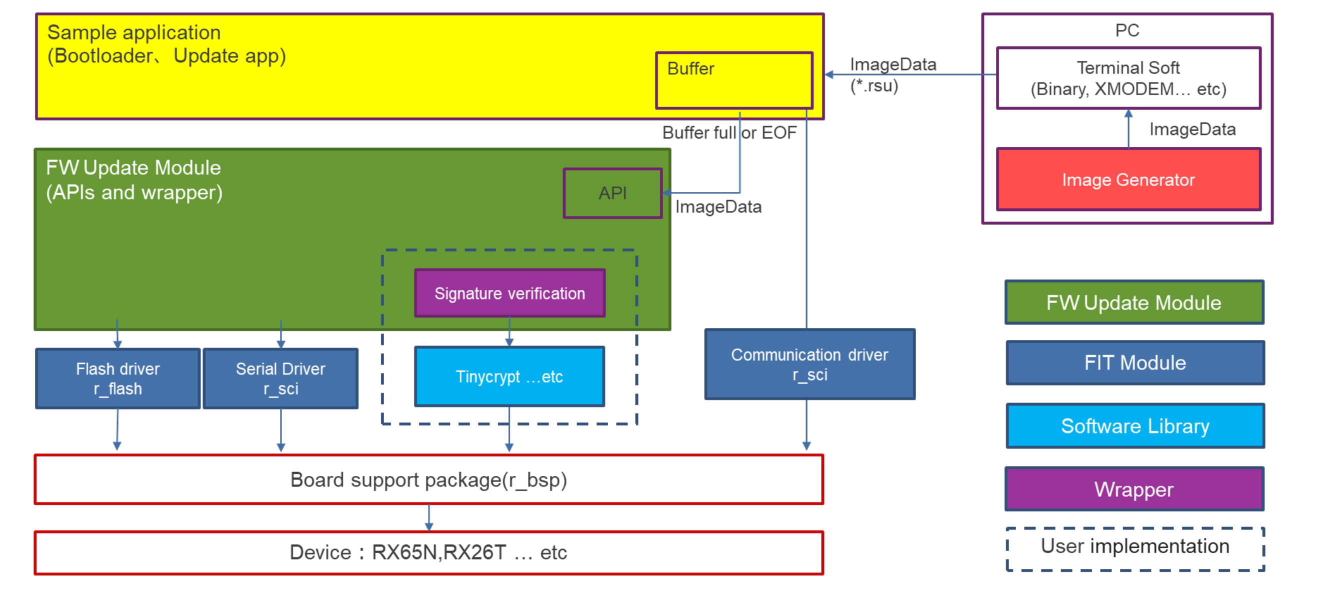


Figure 1.1 Configuration of Modules in Sample Bootloader and Application Program

Table 1.1 List of External Modules Used in Sample Bootloader and Application Program

Type	Application Note (Document No.)	FIT Module
BSP	RX Family Board Support Package Module Using Firmware Integration Technology (R01AN1685)	r_bsp
Device driver	RX Family Flash Module Using Firmware Integration Technology (R01AN2184)	r_flash_rx
Device driver	RX Family SCI Module Using Firmware Integration Technology (R01AN1815)	r_sci_rx
Middleware	RX Family BYTEQ Module Using Firmware Integration Technology (R01AN1683)	r_byteq

1.3 Firmware Update Operation

The firmware update module for the RX Family supports both the dual mode and the linear mode of the MCU's on-chip flash memory.

Dual mode uses the hardware's dual bank function to store the firmware to be updated (update image) on the buffer plane and then swap banks with the main plane to provide a dual bank method of updating..

For linear mode, two methods are provided: one in which the firmware update (update image) is stored temporarily on the buffer plane and another in which it is programmed directly to the main plane.

- Main plane: Area for storing the image used for booting
- Buffer plane: Area for storing the image to be applied as an update

The method of writing the update image directly to the main plane allows all of the internal flash memory to be used as the main plane, but since there is no buffer plane, it is not possible to restore the firmware to its pre-update state in the event of an update failure.

The update method support status varies by device and flash memory capacity, as detailed below.

Table 1.2 Supported Update Methods for Each Product

Flash / Product	4MB	2MB	1,5MB	1MB	756KB	512KB	384KB	256KB	128KB
RX72N	a/b	b	-	-	-	-	-	-	-
RX72M	a/b	b	-	-	-	-	-	-	-
RX671	-	a/b	a/b	b	-	-	-	-	-
RX660	-	-	-	b	-	b	-	-	-
RX66T	-	-	-	b	-	b	-	b	-
RX66N	a/b	b	-	-	-	-	-	-	-
RX65N/651	-	a/b	a/b	b	b	b	-	-	-
RX26T	-	-	-	-	-	a/b	-	b	b
RX24T	-	-	-	-	-	b	b	b	b
a: Dual-Bank Method b: Linear Mode Partial Update Method / Full Update Method -: Not supported									

1.3.1 Dual-Bank Method

The update image is stored in the buffer plane in the on-chip flash memory, and, after it is validated, the banks are swapped, exchanging the main plane and buffer plane.
his method allows the application program to contain the firmware update functionality.
This means that if the firmware update fails before bank swapping occurs, the pre-update image in the main plane can be launched to retry the firmware update.
Since the on-chip flash memory is divided into two portions by the dual-bank functionality, the size of the on-chip flash memory available to store the application program is equal to the size of one of the two portions into which the on-chip flash memory has been divided minus the size of the bootloader.

1.3.1.1 Operation of Dual-Bank Method

The update image is stored in the buffer plane using the dual-bank functionality of the on-chip flash memory, and the firmware update is accomplished by using the bank-swapping functionality to exchange the banks.

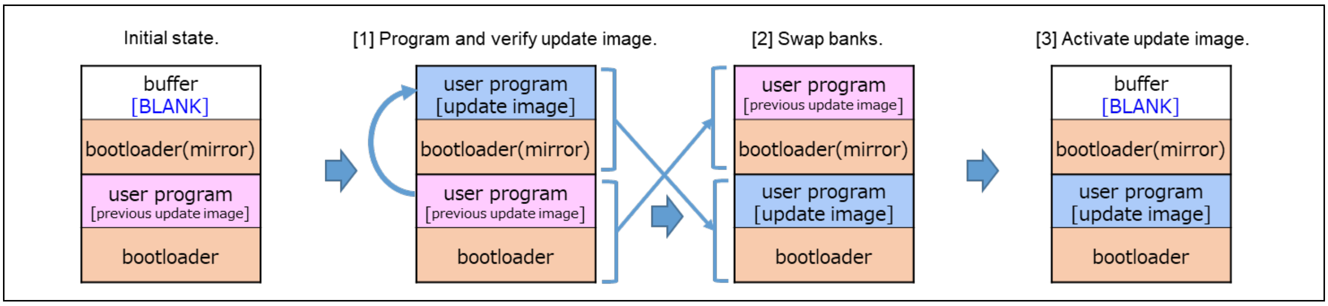


Figure 1.2 Operation of Dual-Bank Method

[1] Program and verify update image.
The previous update image (application program) stored in the main plane is used to program the update image to the buffer plane and verify it.

[2] Swap banks.
If verification is successful, the banks are swapped.

[3] Activate update image.
The buffer plane is erased by the bootloader.
(The demo program does not erase the buffer side. If you need to erase the image before updating for rollback measures, please add a process to erase the buffer side image.)

1.3.2 Linear Mode Partial Update Method

The update image is stored temporarily in the buffer plane in the on-chip flash memory, and, after it is validated, it is self-programmed to the main plane. This method allows the application program to contain the firmware update functionality. This means that if the firmware update fails before self-programming to the main plane occurs, the pre-update image in the main plane can be launched to retry the firmware update. The size that can store the application program is half the size of the remaining internal flash memory minus the bootloader.

1.3.2.1 Operation of Linear Mode Partial Update Method

This method divides the on-chip flash memory into a main plane and a buffer plane and then temporarily stores the update image in the buffer plane. Firmware is updated by storing the update image on the buffer plane and copying it from the buffer plane to the main plane.

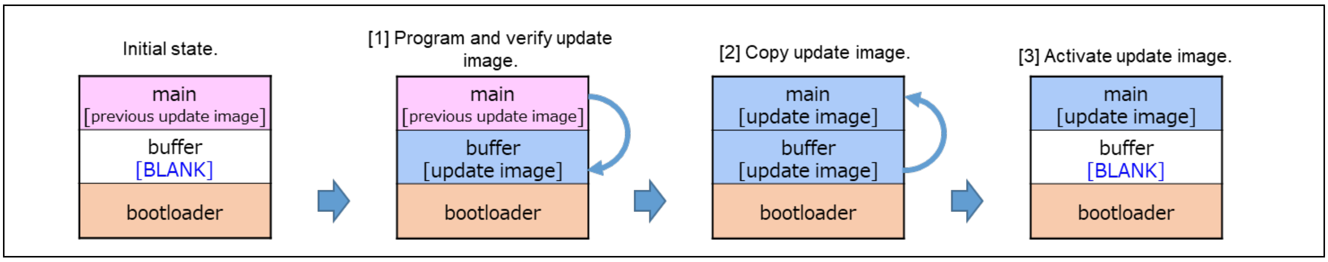


Figure 1.3 Operation of Partial Update Method

[1] Program and verify update image.
The previous update image (application program) stored in the main plane is used to program the update image to the buffer plane and verify it.

[2] Copy update image.
If verification is successful, the system is reset, the main plane is erased by the bootloader, and the updated image is copied from the buffer plane to the main plane.

[3] Activate update image.
The buffer plane is erased by the bootloader.
(The demo program does not erase the buffer side. If you need to erase the image before updating for rollback measures, please add a process to erase the buffer side image.)

1.3.3 Linear Mode Full Update Method

The update image is self-programmed to the main plane, after which it is validated. This method requires the bootloader to contain the firmware update functionality. This means that if the firmware update fails, the bootloader functionality can be used to retry the firmware update. The functionality of the application program cannot be used until the firmware update succeeds.
The size that can store the application program is the remaining size of the internal flash memory minus the bootloader.

1.3.3.1 Operation of Linear Mode Full Update Method

This method of writing the update image directly to the main plane allows all of the internal flash memory to be used as the main plane, but since there is no buffer plane, it is not possible to restore the firmware to its pre-update state in the event of an update failure.

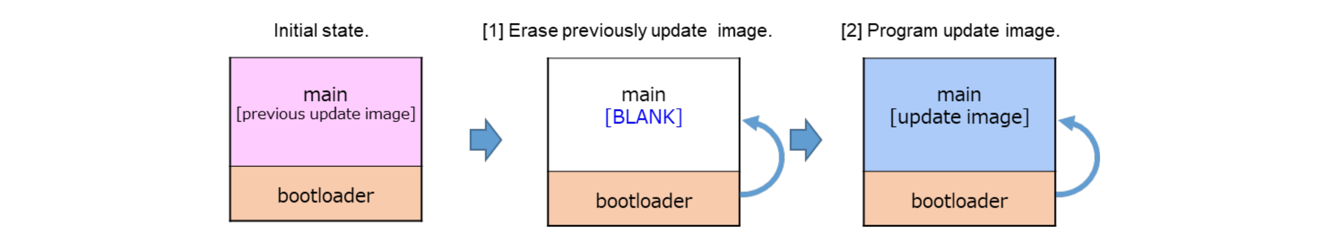


Figure 1.4 Operation of Full Update Method

[1] Erase previously update image.
The previous update image (application program) stored in the main plane configures the data indicating updates to the main plane and then applies a reset. After this, the bootloader runs and erases the initial image from the main plane.

[2] Program update image.
The bootloader downloads the update image from an external source and programs it to the main plane. The programmed update image is verified, and if verification is successful, the update image is activated.

1.4 Initial State of Firmware Update

To set the firmware update system using the firmware update module to the initial state, build the system by writing the initial image generated by the Renesas Image Generator to the built-in flash memory with a flash writer or similar device.

As an alternative method, it is also possible to build the system by first writing only the bootloader with a flash writer, etc., and then writing the updated image of the application program with the bootloader function.

1.4.1 Initial State of Dual-Bank Method

The following figure shows the construction of the initial state of the dual-bank method using the bootloader.

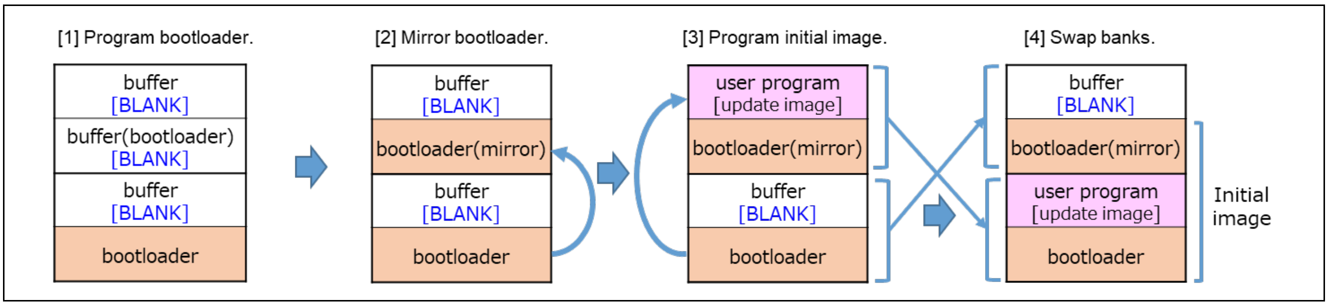


Figure 1.5 Initial Firmware Update Settings Utilizing Bootloader (Example of Dual-Bank Method)

[1] Program bootloader.
The bootloader is programmed to the on-chip flash memory using a tool such as Flash Writer.

[2] Mirror bootloader.
The bootloader is mirrored to bank 1 by the bootloader.

[3] Program initial image.
The initial image is downloaded from an external source and programmed to the buffer plane using the functionality of the bootloader. The programmed firmware is verified.

[4] Swap banks.
If verification is successful, the banks are swapped and processing ends.

1.4.2 Initial State of Linear Mode Partial Update Method

The following figure shows the construction of the initial state of the dual-bank method using the bootloader.

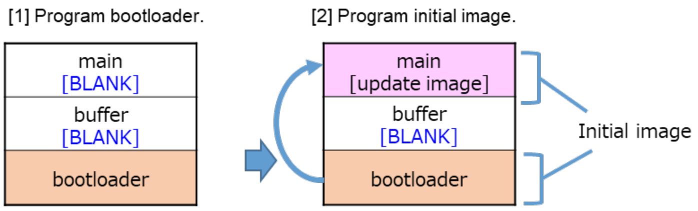


Figure 1.6 Initial Firmware Update Settings Utilizing Bootloader (Example of Partial Update Method)

[1] Program bootloader.
The bootloader is programmed to the on-chip flash memory using a tool such as Flash Writer.

[2] Program initial image.
The initial image is downloaded from an external source and programmed to the main plane using the functionality of the bootloader. The programmed firmware is verified, and if verification is successful, processing ends.

1.4.3 Initial State of Linear Mode Full Update Method

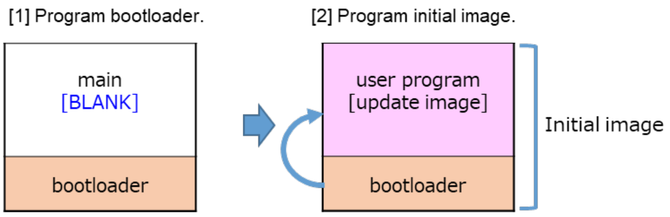


Figure 1.7 Initial Firmware Update Settings Utilizing Bootloader (Example of Full Update Method)

[1] Program bootloader.
The bootloader is programmed to the on-chip flash memory using a tool such as Flash Writer.

[2] Program initial image.
The initial image is downloaded from an external source and programmed to the main plane using the functionality of the bootloader. The programmed firmware is verified, and if verification is successful, processing ends.

1.5 Package Contents

The firmware update module package contains several files, including software and tools. These are listed in the table below.

Table 1.3 Folder Structure of Firmware Update Module Package

Folder Name	Description
r01an6850jj0200-rx-fwupdate.zip\	
├─Demos	Sample projects
│ ├─rx	
│ │ ├─modules	Drivers and libraries
│ │ │ ├─3rd_party	
│ │ │ │ └─tinycrypt	Crypto library
│ │ │ └─etc	
│ │ │ └─base64	Base64 decode
│ │ ├─rx24t-rsk	Sample project for RX24T
│ │ │ ├─w_buffer	Linear Mode Partial Update Method
│ │ │ │ ├─e2_ccrx	CC-RX version
│ │ │ │ │ ├─boot_loader	Bootloader
│ │ │ │ │ ├─fwup_leddemo	LED illumination application
│ │ │ │ │ └─fwup_main	User applications including firmware update
│ │ │ │ ├─e2_gcc	GCC version
│ │ │ │ └─iar	IAR version
│ │ │ └─wo_buffer	Linear Mode Full Update Method
│ │ │ ├─e2_ccrx	CC-RX version
│ │ │ ├─e2_gcc	GCC version
│ │ │ └─iar	IAR version
│ │ ├─rx26t-mck	Sample project for RX26T
│ │ │ ├─dualbank	Dual-Bank Method
│ │ │ │ ├─e2_ccrx	CC-RX version
│ │ │ │ ├─e2_gcc	GCC version
│ │ │ │ └─iar	IAR version
│ │ │ ├─w_buffer	Linear Mode Partial Update Method
│ │ │ │ ├─e2_ccrx	CC-RX version
│ │ │ │ ├─e2_gcc	GCC version
│ │ │ │ └─iar	IAR version
│ │ │ └─wo_buffer	Linear Mode Full Update Method
│ │ │ ├─e2_ccrx	CC-RX version
│ │ │ ├─e2_gcc	GCC version
│ │ │ └─iar	IAR version
│ │ ├─rx65n-rsk	Sample project for RX65N
│ │ │ ├─dualbank	Dual-Bank Method
│ │ │ │ ├─e2_ccrx	CC-RX version
│ │ │ │ ├─e2_gcc	GCC version
│ │ │ │ └─iar	IAR version
│ │ │ ├─w_buffer	Linear Mode Partial Update Method
│ │ │ │ ├─e2_ccrx	CC-RX version
│ │ │ │ ├─e2_gcc	GCC version
│ │ │ │ └─iar	IAR version

Folder Name	Description
│ │ │ └─wo_buffer	Linear Mode Full Update Method
│ │ │ ├─e2_ccrx	CC-RX version
│ │ │ ├─e2_gcc	GCC version
│ │ │ └─iar	IAR version
│ │ ├─rx66t-rsk	Sample project for RX66T
│ │ │ ├─w_buffer	Linear Mode Partial Update Method
│ │ │ │ ├─e2_ccrx	CC-RX version
│ │ │ │ ├─e2_gcc	GCC version
│ │ │ │ └─iar	IAR version
│ │ │ └─wo_buffer	Linear Mode Full Update Method
│ │ │ ├─e2_ccrx	CC-RX version
│ │ │ ├─e2_gcc	GCC version
│ │ │ └─iar	IAR version
│ │ ├─rx660-rsk	Sample project for RX660
│ │ │ ├─w_buffer	Linear Mode Partial Update Method
│ │ │ │ ├─e2_ccrx	CC-RX version
│ │ │ │ ├─e2_gcc	GCC version
│ │ │ │ └─iar	IAR version
│ │ │ └─wo_buffer	Linear Mode Full Update Method
│ │ │ ├─e2_ccrx	CC-RX version
│ │ │ ├─e2_gcc	GCC version
│ │ │ └─iar	IAR version
│ │ ├─rx671-rsk	Sample project for RX671
│ │ │ ├─dualbank	Dual-Bank Method
│ │ │ │ ├─e2_ccrx	CC-RX version
│ │ │ │ ├─e2_gcc	GCC version
│ │ │ │ └─iar	IAR version
│ │ │ ├─w_buffer	Linear Mode Partial Update Method
│ │ │ │ ├─e2_ccrx	CC-RX version
│ │ │ │ ├─e2_gcc	GCC version
│ │ │ │ └─iar	IAR version
│ │ │ └─wo_buffer	Linear Mode Full Update Method
│ │ │ ├─e2_ccrx	CC-RX version
│ │ │ ├─e2_gcc	GCC version
│ │ │ └─iar	IAR version
│ │ └─rx72n-rsk	Sample project for RX72N
│ │ ├─dualbank	Dual-Bank Method
│ │ │ ├─e2_ccrx	CC-RX version
│ │ │ ├─e2_gcc	GCC version
│ │ │ └─iar	IAR version
│ │ ├─w_buffer	Linear Mode Partial Update Method
│ │ │ ├─e2_ccrx	CC-RX version
│ │ │ ├─e2_gcc	GCC version
│ │ │ └─iar	IAR version
│ │ └─wo_buffer	Linear Mode Full Update Method
│ │ ├─e2_ccrx	CC-RX version
│ │ ├─e2_gcc	GCC version
│ │ └─iar	IAR version
│ └─r_fwup	Firmware update module (for sample projects)

Folder Name	Description
├─FITModules	Firmware update module
│ ├─r_config	Configuration file
│ └─r_fwup	Source code
└─RenesasImageGenerator	Renesas Image Generator (Python program and parameter files)
├─image-gen.py	Python program for Renesas Image Generator
└─RX*xx*_*xxxx*_Full_ImageGenerator_PRM.csv	Parameter file for demo project

1.6 API Overview

Table 1.3 lists the API functions included in the firmware update module.

Table 1.4 API Functions

Function	Function Description
R_FWUP_Open	Opens the module.
R_FWUP_Close	Performs processing to close the module.
R_FWUP_IsExistImage	Confirms the existence of an image in the specified area.
R_FWUP_EraseArea	Erases the specified area.
R_FWUP_GetImageSize	Obtains the size of the image.
R_FWUP_WriteImageHeader	Writes the header portion of the image.
R_FWUP_WriteImageProgram	Writes the program portion of the image.
R_FWUP_WriteImage	Writes the image (header portion + program portion).
R_FWUP_VerifyImage	Validates the image.
R_FWUP_ActivateImage	Activates a new image.
R_FWUP_ExecImage	Launches a new image.
R_FWUP_SoftwareReset	Applies a software reset.
R_FWUP_SoftwareDelay	Applies a software delay.
R_FWUP_GetVersion	Returns the version number of the module.

Figure 1.8 to Figure 1.12 show flowcharts of example implementations of a bootloader and application program corresponding to each firmware update method described in in 1.3, Firmware Update Operation, using the APIs provided in this module.

For details, refer to the source code of the demo projects included in the package associated with this application note.

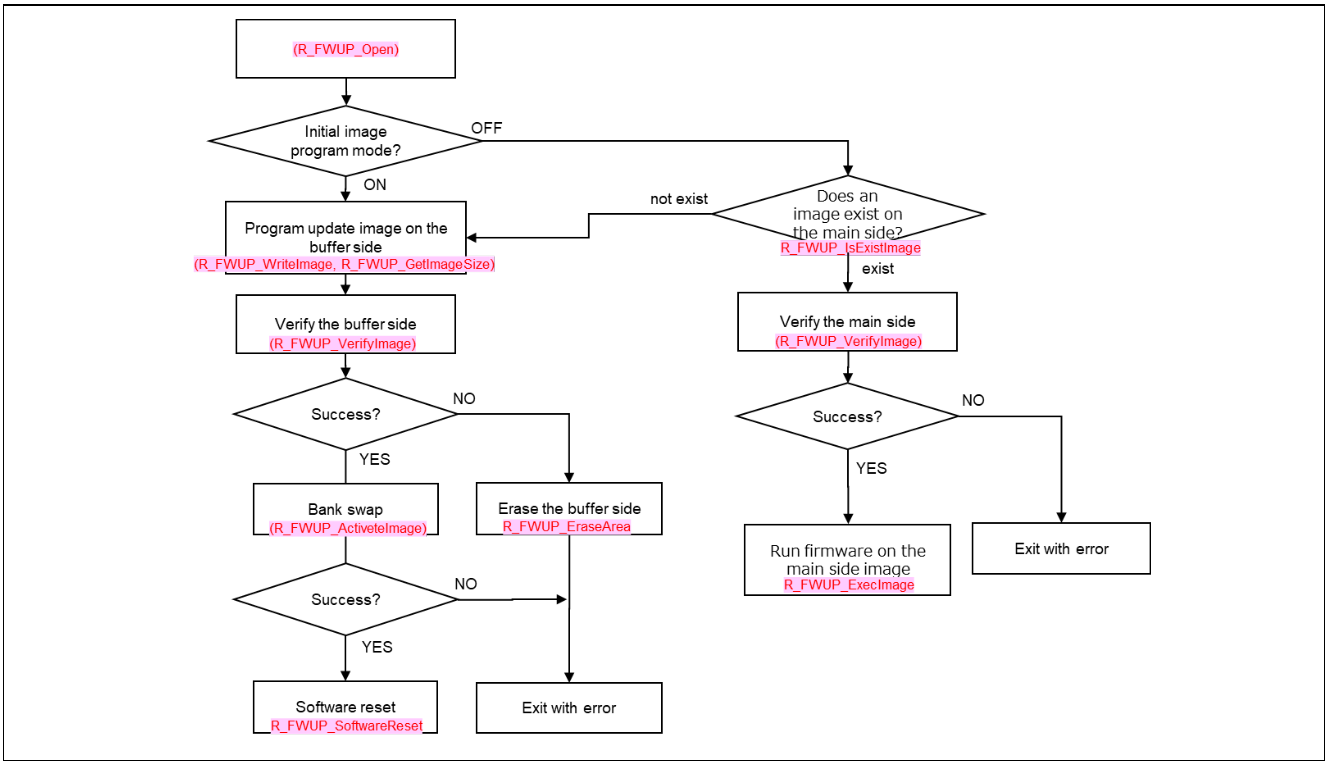


Figure 1.8 Bootloader Implementation Example for Partial/Full Update Method (Using Buffer Plane)

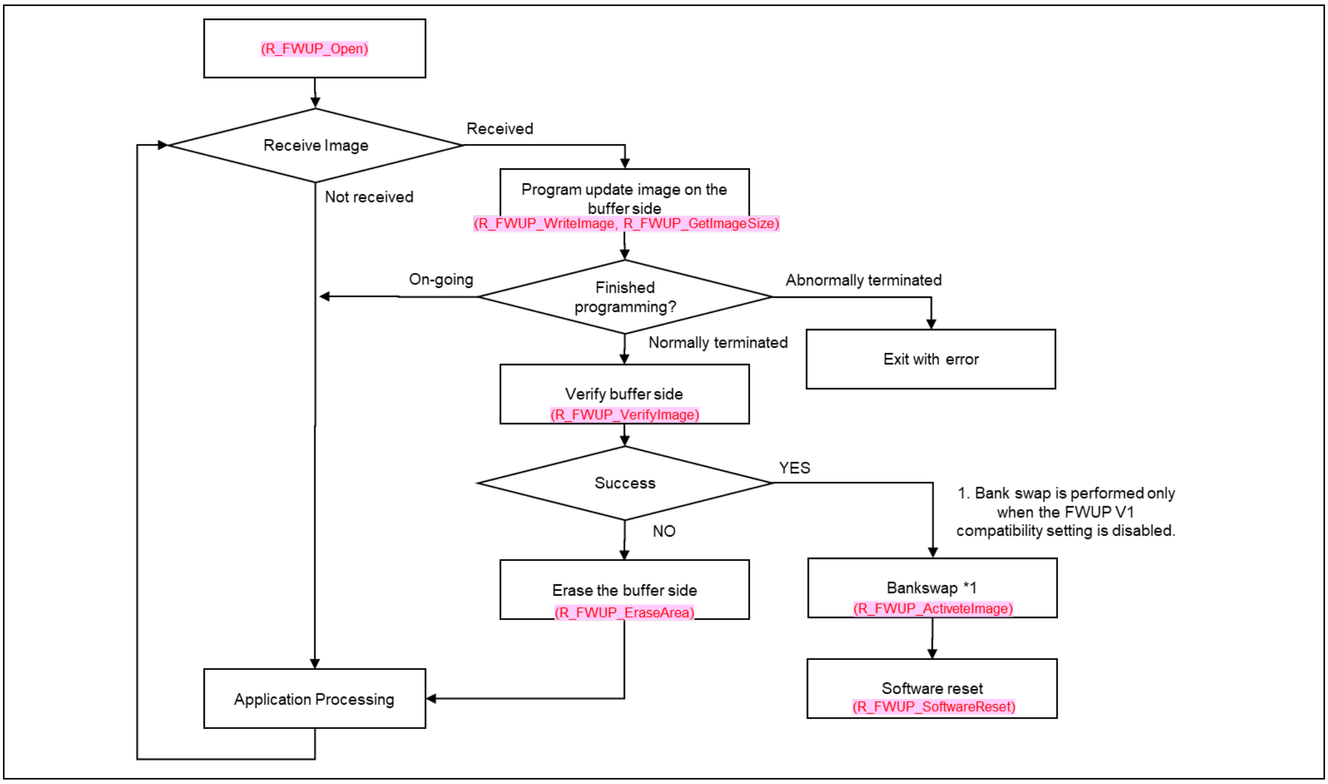


Figure 1.9 Application Program Implementation Example for Partial/Full Update Method (Using Buffer Plane)

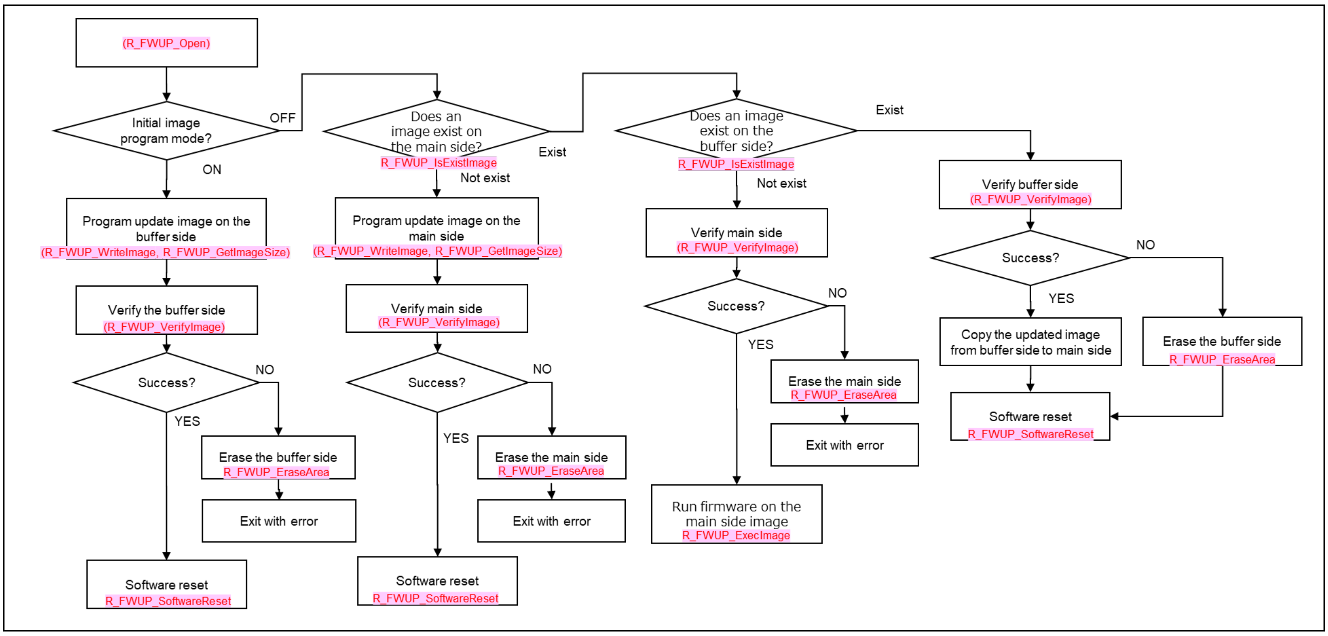


Figure 1.10 Bootloader Implementation Example for Full Update Method (No Buffer)

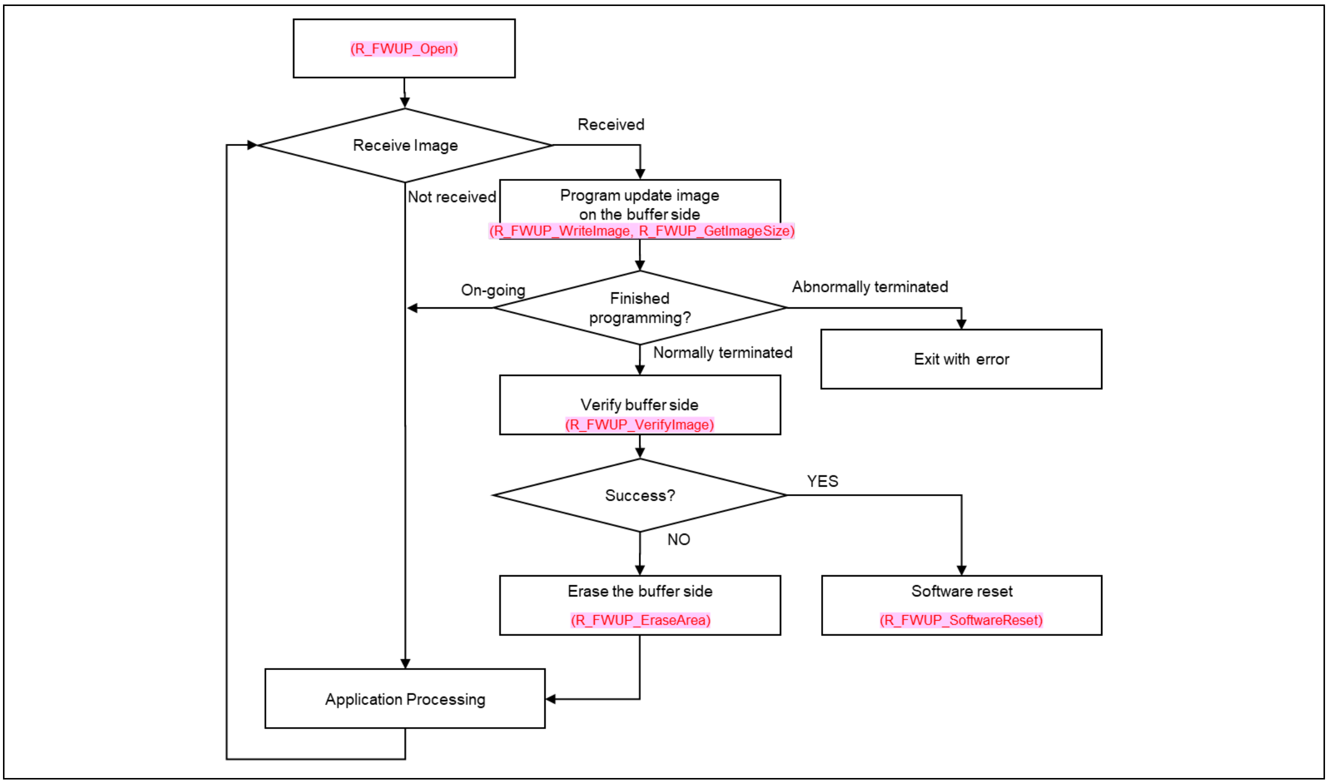


Figure 1.11 Application Program Implementation Example for Linea Mode Partial Update Method

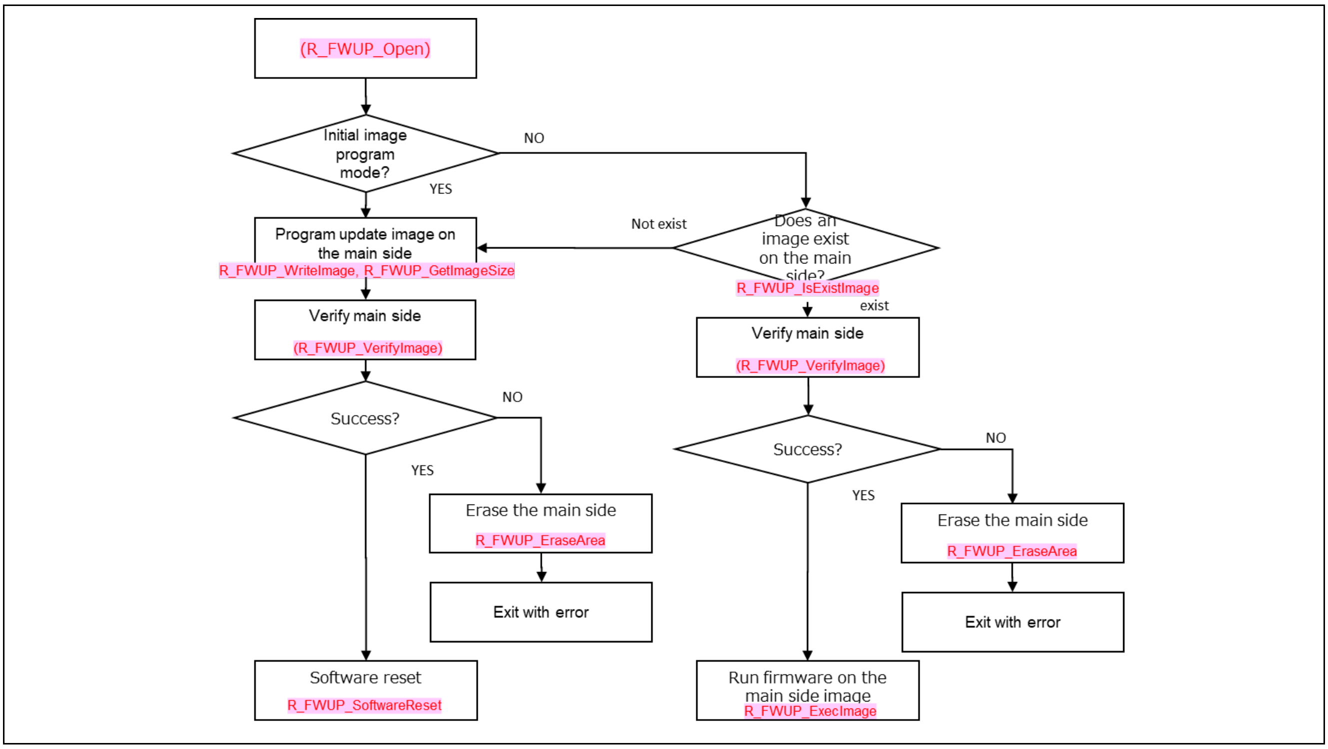


Figure 1.12 Bootloader Implementation Example for Full Update Method

2. API Information

2.1 Hardware Requirements

The MCU used must support the following functions:

- Flash memory

2.2 Software Requirements

The module is dependent upon the following drivers:

- Board support package (r_bsp)
- Flash module (r_flash_rx)
- Serial communications interface (SCI: asynchronous/clock synchronous) (r_sci_rx)
- Byte queue buffer module (r_byteq)

2.3 Supported Toolchains

The module has been confirmed to work with the toolchains listed in 6.1, Confirmed Operation Environments.

2.4 Header Files

All API calls and their supporting interface definitions are located in r_fwup_if.h.

2.5 Integer Types

The driver uses ANSI C99. These types are defined in stdint.h.

2.6 Compile Settings

The configuration option settings of the module are contained in r_fwup_config.h.

The names of the options and descriptions of their setting values are listed in Table 2.1.

Table 2.1 Configuration Settings

Configuration options in r_fwup _config.h	
FWUP_CFG_UPDATE_MODE	Update method 0: Dual-Bank Method 1: Linea Mode Partial Update Method 2: Linea Mode Full Update Method 3: Not available for RX
FWUP_CFG_FUNCTION_MODE	Specifies how the module is used. 0: Bootloader 1: Application program
FWUP_CFG_MAIN_AREA_ADDR_L	Specifies the start address of the main plane.
FWUP_CFG_BUF_AREA_ADDR_L	Specifies the start address of the buffer plane (in on-chip flash memory).
FWUP_CFG_AREA_SIZE	Specifies the size of the main plane and buffer plane.
FWUP_CFG_CF_BLK_SIZE	Specifies the block size of the on-chip code flash.
FWUP_CFG_CF_W_UNIT_SIZE	Specifies the writing unit for the on-chip code flash.
FWUP_CFG_EXT_BUF_AREA_ADDR_L	Specifies the start address of the buffer plane in external flash memory. (Not subject to change in RX)
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE	Specifies the block size or sector size of the external flash memory. (Not subject to change in RX)
FWUP_CFG_DF_ADDR_L	Start address of data flash.
FWUP_CFG_DF_BLK_SIZE	Block size of data flash.
FWUP_CFG_DF_NUM_BLKS	Block count of data flash. Specify 0 if there is no data flash.
FWUP_CFG_FWUPV1_COMPATIBLE	FWUP V1 Compatibility Setting 0: Disable 1: Enable
FWUP_CFG_SIGNATURE_VERIFICATION	Verification method 0: ECDSA + SHA256 1: SHA256
FWUP_CFG_PRINTF_DISABLE	Log display setting 0: Enable 1: Disable

2.7 Sample Project Code Sizes

The tables below show the ROM, RAM, and maximum stack sizes for the sample projects included in the package associated with this application note. The values in the table below have been confirmed under the following conditions:

Module revision: Firmware update module for RX, v2.0.0

Compiler version: Renesas Electronics C/C++ Compiler for RX Family V3.04.00
GCC for Renesas RX 8.3.0.202202
IAR C/C++ Compiler for Renesas RX 4.20.1

CC-RX

- Optimization level: Size and execution speed (-O default)
- Delete variables/functions that have never been referenced (optimize=symbol_delete)
- Generate reduced function I/O functions (Yes: maximum reduced version)

GCC

- Optimization level: Size (-Os)
- Use newlib-nano (--specs=nano.specs)

IAR

- Optimization level: High (balanced)

Table 2.2 ROM, RAM, and Maximum Stack Sizes for Sample Projects

ROM, RAM, and Stack Code Sizes					
Device	Category	Memory Used (byte)			Remarks
		CC-RX	GCC	IAR	
RX24T	ROM	21480	22776	18341	boot_loader*1
		11474	11528	8452	fwup_leddemo
		20930	21868	17807	fwup_main
	RAM	5114	7956	3806	boot_loader
		3411	5116	2464	fwup_leddemo
		5242	8084	3934	fwup_main
	Stack	552	576	2696	boot_loader
		188	68	960	fwup_leddemo
		552	576	2684	fwup_main
RX26T	ROM	24074	25156	22328	boot_loader*1
		13180	14188	11383	fwup_leddemo
		23701	24768	21874	fwup_main
	RAM	4077	5912	5606	boot_loader
		3289	5500	5409	fwup_leddemo
		4302	6552	5734	fwup_main
	Stack	552	576	3032	boot_loader
		188	68	1304	fwup_leddemo
		552	576	3024	fwup_main
RX65N	ROM	24711	26406	23076	boot_loader*2
		14402	15736	12343	fwup_leddemo
		24272	26410	22725	fwup_main
	RAM	5282	6680	4875	boot_loader
		8017	9212	5533	fwup_leddemo
		5554	6936	6171	fwup_main
	Stack	552	576	3100	boot_loader
		188	68	1364	fwup_leddemo
		552	576	3092	fwup_main
RX66T	ROM	23362	24720	21301	boot_loader*2
		13240	14164	10892	fwup_leddemo
		22827	24612	20647	fwup_main
	RAM	6744	8600	6443	boot_loader
		7853	8956	5369	fwup_leddemo
		7144	8856	7867	fwup_main
	Stack	552	576	2964	boot_loader
		188	68	1228	fwup_leddemo
		552	576	2952	fwup_main

ROM, RAM, and Stack Code Sizes					
Device	Category	Memory Used (byte)			Remarks
		CC-RX	GCC	IAR	
RX660	ROM	23984	25464	22163	boot_loader*2
		14079	15140	11779	fwup_leddemo
		23442	25332	21653	fwup_main
	RAM	6599	8472	6240	boot_loader
		7869	9084	5385	fwup_leddemo
		6999	8728	7664	fwup_main
	Stack	552	576	2964	boot_loader
		188	68	1232	fwup_leddemo
		552	576	2952	fwup_main
RX671	ROM	25211	46657	23729	boot_loader*2
		14783	16156	12800	fwup_leddemo
		24795	27015	23401	fwup_main
	RAM	5298	6688	4891	boot_loader
		8033	9212	5549	fwup_leddemo
		5570	6936	6187	fwup_main
	Stack	552	584	3100	boot_loader
		192	68	1368	fwup_leddemo
		552	576	3092	fwup_main
RX72N	ROM	25404	27287	24018	boot_loader*2
		14976	16480	13121	fwup_leddemo
		24966	27307	23678	fwup_main
	RAM	5402	6808	4995	boot_loader
		8137	9340	5653	fwup_leddemo
		5674	7064	6291	fwup_main
	Stack	552	576	3168	boot_loader
		192	68	1432	fwup_leddemo
		552	576	3160	fwup_main

*1) Allocate 32KB of space to place the bootloader.

*2) Allocate 64KB of space to place the bootloader.

2.8 Arguments

The return values of the API functions are shown below. This enumeration is located in r_fwup_if.h, as are the prototype declarations of the API functions.

```
typedef enum fwup_area
{
    FWUP_AREA_MAIN = 0,
    FWUP_AREA_BUFFER,
    FWUP_AREA_DATA_FLASH
} e_fwup_area_t;

typedef enum e_fwup_delay_units
{
    FWUP_DELAY_MICROSECS = 0,
    FWUP_DELAY_MILLISECS,
    FWUP_DELAY_SECS
} e_fwup_delay_units_t;
```

2.9 Return Values

The return values of the API functions are shown below. This enumeration is located in r_fwup_if.h, as are the prototype declarations of the API functions.

```
typedef enum fwup_err
{
    FWUP_SUCCESS = 0,                // Normally terminated.
    FWUP_PROGRESS,                   // Firmware update is in progress.
    FWUP_ERR_FLASH,                  // Detect error of flash module.
    FWUP_ERR_VERIFY,                 // Verify error.
    FWUP_ERR_FAILURE,                // General error.
} e_fwup_err_t;
```

2.10 Adding the FIT Module to Your Project

The module must be added to each project in which it is used. Renesas recommends the method using the Smart Configurator described in (1) below. However, the Smart Configurator only supports some RX devices. Please use the methods of (2) for RX devices that are not supported by the Smart Configurator.

(1) Adding the FIT module to your project using the Smart Configurator in e2 studio

By using the Smart Configurator in e2 studio, the FIT module is automatically added to your project. Refer to “RX Smart Configurator User’s Guide: e2 studio (R20AN0451)” for details.

(2) Adding the FIT module to your project using the FIT Configurator in e2 studio

By using the FIT Configurator in e2 studio, the FIT module is automatically added to your project. Refer to “RX Family Adding Firmware Integration Technology Modules to Projects (R01AN1723)” for details.

(3) Adding the FIT module to your project using the FIT Configurator in the IAR Embedded Workbench for Renesas RX environment

If you want to add a FIT module in the IAR Embedded Workbench for Renesas RX environment, use the RX Smart Configurator to add the FIT module to your project. Refer to “RX Smart Configurator User’s Guide:

3. API Functions

3.1 R_FWUP_Open Function

Table 3.1 R_FWUP_Open Function Specifications

Format	e_fwup_err_t R_FWUP_Open (void)	
Description	Performs processing to open the firmware update module. Implements processing to open the flash module.	
Parameters	None	
Return Values	FWUP_SUCCESS	Normal end
	FWUP_ERR_FLASH	Flash module error
Special Notes	—	

3.2 R_FWUP_Close Function

Table 3.2 R_FWUP_Close Function Specifications

Format	void R_FWUP_Close (void)
Description	Performs processing to close the firmware update module. Implements processing to close the flash module.
Parameters	None
Return Values	None
Special Notes	—

3.3 R_FWUP_IsExistImage Function

Table 3.3 R_FWUP_IsExistImage Function Specifications

Format	bool R_FWUP_IsExistImage(e_fwup_area_t area)	
Description	Confirms the existence of an image in the specified area.	
Parameters	area: Main plane (FWUP_AREA_MAIN) or buffer plane (FWUP_AREA_BUFFER)	
Return Values	true	Image exists.
	false	Image does not exist.
Special Notes	When FWUP_CFG_FWUPV1_COMPATIBLE is enabled, the magic code in the RSU header area used for processing is "Renesas" for FWUP V1.	

3.4 R_FWUP_EraseArea Function

Table 3.4 R_FWUP_EraseArea Function Specificationss

Format	e_fwup_err_t R_FWUP_EraseArea(e_fwup_area_t area)	
Description	Erases the specified area.	
Parameters	area: Main plane (FWUP_AREA_MAIN) or buffer plane (FWUP_AREA_BUFFER), Data Flash (FWUP_AREA_DATA_FLASH)	
Return Values	FWUP_SUCCES	Normal end
	FWUP_ERR_FLASH	Flash module error
Special Notes	Erasure of the main plane can only be performed by the bootloader.	

3.5 R_FWUP_GetImageSize Function

Table 3.5 R_FWUP_GetImageSize Function Specificationss

Format	uint32_t R_FWUP_GetImageSize(void)	
Description	Returns the size of the image in bytes. This function obtains the byte size of the image based on the RSU header address information shown in Figure 4.1. Therefore, first write the RSU header address information to code flash using the R_FWUP_WriteImage function or the R_FWUP_WriteImageProgram function.	
Parameters	None	
Return Values	0	Acquisition in progress
	1 or more	Image size
Special Notes	—	

3.6 R_FWUP_WriteImageHeader Function

Table 3.6 R_FWUP_WriteImageHeader Function Specifications

Format	e_fwup_err_t R_FWUP_WriteImageHeader (e_fwup_area_t area, uint8_t FWUP_FAR *p_sig_type, uint8_t FWUP_FAR *p_sig, uint32_t sig_size)	
Description	Writes a signature that the bootloader uses for verification to the header of the image in the designated area.	
Parameters	area: Main plane (FWUP_AREA_MAIN) or buffer plane (FWUP_AREA_BUFFER) p_sig_type: Signature type character string “hash-sha256” or “sig-sha256-ecdsa” p_sig: Signature sig_size: Length of signature (Should be set to 64.)	
Return Values	FWUP_SUCCES	Write completed
	FWUP_ERR_FLASH	Flash module error
	FWUP_ERR_FAILURE	Illegal parameter
Special Notes	When FWUP_CFG_FWUPV1_COMPATIBLE is enabled, the magic code in the RSU header area used for processing is "Renesas" for FWUP V1.	

3.7 R_FWUP_WriteImageProgram Function

Table 3.7 R_FWUP_WriteImageProgram Function Specifications

Format	e_fwup_err_t R_FWUP_WriteImageProgram (e_fwup_area_t area, uint8_t *p_buf, uint32_t offset, uint32_t buf_size)	
Description	Writes the program portion of the image to the specified area. Continue calling this function until the total size of the image is reached. The image size is obtained by R_FWUP_GetImageSize(). This function writes a program by offset based on the address information in the RSU header shown in Figure 4.1. Therefore, be sure to set 0x100 bytes of data from the offset (0x200) in Table 4.3 in the first call to this function. (Specify 0x200 for the offset argument and 0x100 or more for the buf_size argument.)	
Parameters	area: Main plane (FWUP_AREA_MAIN) or buffer plane (FWUP_AREA_BUFFER) p_buf: Buffer for program portion of image offset: Offset*1 buf_size: Buffer size*2	
Return Values	FWUP_SUCCES	Writing of all images is completed.
	FWUP_PROGRESS	Writing of all images not completed (Writing of the specified number of images completed)
	FWUP_ERR_FLASH	Flash module error
	FWUP_ERR_FAILURE	Illegal parameter
Special Notes	1. The offset must be 0x200 or greater. 2. Specify a multiple of the code flash write unit (for example, 64, 128, or 256). This size also applies to data flash.	

3.8 R_FWUP_WriteImage Function

Table 3.8 R_FWUP_WriteImage Function Specifications

Format	e_fwup_err_t R_FWUP_WriteImage(e_fwup_area_t area, uint8_t *p_buf, uint32_t buf_size)	
Description	Writes an image (header portion + program portion) to the specified area. Continue calling this function until the total size of the image is reached. The image size is obtained by R_FWUP_GetImageSize().	
Parameters	area: Main plane (FWUP_AREA_MAIN) or buffer plane (FWUP_AREA_BUFFER) p_buf: Image (header + program) buffer buf_size: Buffer size*1	
Return Values	FWUP_SUCCES	Writing of all images is completed.
	FWUP_PROGRESS	Writing of all images not completed (Writing of the specified number of images completed)
	FWUP_ERR_FLASH	Flash module error
	FWUP_ERR_FAILURE	Illegal parameter
Special Notes	1. Specify a multiple of the code flash write unit (for example, 64, 128, or 256). This size also applies to data flash. When FWUP_CFG_FWUPV1_COMPATIBLE is enabled, the magic code in the RSU header area used for processing is "Renesas" for FWUP V1.	

3.9 R_FWUP_VerifyImage Function

Table 3.9 R_FWUP_VerifyImage Function Specifications

Format	e_fwup_err_t R_FWUP_VerifyImage(e_fwup_area_t area)	
Description	Verifies an image using the cryptographic library embedded in the module.	
Parameters	area: Main plane (FWUP_AREA_MAIN) or buffer plane (FWUP_AREA_BUFFER)	
Return Values	FWUP_SUCCES	Verification successful
	FWUP_ERR_VERIFY	Verification failed
	FWUP_ERR_FAILURE	Illegal parameter
Special Notes	—	

3.10 R_FWUP_ActivateImage Function

Table 3.10 R_FWUP_ActivateImage Function Specifications

Format	e_fwup_err_t R_FWUP_ActivateImage(void)	
Description	Activates a new image. • Dual-bank update method — FWUP_CFG_FWUPV1_COMPATIBLE is enabled: Bank swap. — FWUP_CFG_FWUPV1_COMPATIBLE is disabled: Returns FWUP_SUCCESS without doing anything. • Linea mode partial update method — Bootloader: Copies the buffer plane image to the main plane. — User program: Returns FWUP_SUCCESS without doing anything. • Linea mode full update method — Returns FWUP_SUCCESS without doing anything.	
Parameters	None	
Return Values	FWUP_SUCCESS	Normal end
	FWUP_ERR_FLASH	Flash module error
Special Notes	—	

3.11 R_FWUP_ExecImage Function

Table 3.11 R_FWUP_ExecImage Function Specifications

Format	void R_FWUP_ExecImage(void)
Description	Runs the program in a valid image.
Parameters	None
Return Values	None
Special Notes	Set the interrupt allow flag of PSW to 0 to disable interrupts.

3.12 R_FWUP_SoftwareReset Function

Table 3.12 R_FWUP_SoftwareReset Function Specifications

Format	void R_FWUP_SoftwareReset(void)
Description	Execute software reset processing.
Parameters	None
Return Values	None
Special Notes	—

3.13 R_FWUP_SoftwareDelay Function

Table 3.13 R_FWUP_SoftwareDelay Function Specifications

Format	uint32_t R_FWUP_SoftwareDelay(uint32_t delay, e_fwup_delay_units_t units)	
Description	Execute software delay processing.	
Parameters	delay: Delay time units: Unit (μs, ms, or sec.)	
Return Values	0	Normal end
	Other	Abnormal end
Special Notes	—	

3.14 R_FWUP_GetVersion Function

Table 3.14 R_FWUP_GetVersion Function Specifications

Format	uint32_t R_FWUP_GetVersion(void)
Description	Returns the version number of the module.
Parameters	None
Return Values	Version number
Special Notes	—

3.15 Wrapper Functions (r_fwup_wrap_verify.c, h)

The demo project provided in this package uses Tinycrypt as the crypto library. If you wish to use another crypto library, please implement the crypto library you wish to use and modify the implementation in this wrapper function.

3.15.1 r_fwup_wrap_sha256_init Function

Table 3.15 r_fwup_wrap_sha256_init Function Specifications

Format	int32_t r_fwup_wrap_sha256_init (void *vp_ctx)	
Description	Start hash value calculation.	
Parameters	vp_ctx: Pointer to the context of the cryptographic library	
Return Values	0	Normal end
	Other	Abnormal end
Special Notes	—	

3.15.2 r_fwup_wrap_sha256_update Function

Table 3.16 r_fwup_wrap_sha256_update Function Specifications

Format	int32_t r_fwup_wrap_sha256_update (void *vp_ctx, const uint8_t *p_data, uint32_t datalen)	
Description	Calculates hash values for the specified range.	
Parameters	vp_ctx: Pointer to the context of the cryptographic library p_data: Starting address datalen: Data length (bytes)	
Return Values	0	Normal end
	Other	Abnormal end
Special Notes	—	

3.15.3 r_fwup_wrap_sha256_final Function

Table 3.17 r_fwup_wrap_sha256_final Function Specifications

Format	int32_t r_fwup_wrap_sha256_final (uint8_t *p_hash, void *vp_ctx)	
Description	Finishes computing the hash value and returns the hash value.	
Parameters	P_hash: Pointer to buffer to store the calculated hash value vp_ctx: Pointer to the context of the cryptographic library	
Return Values	0	Normal end
	Other	Abnormal end
Special Notes	—	

3.15.4 r_fwup_wrap_verify_ecdsa Function

Table 3.18 r_fwup_wrap_verify_ecdsa Function Specifications

Format	int32_t r_fwup_wrap_verify_ecdsa (uint8_t *p_hash, uint8_t *p_sig_type, uint8_t *p_sig, uint32_t sig_size)	
Description	ECDSA performs the verification.	
Parameters	p_hash: Pointer to the buffer where the hash value is stored p_sig_type: Signature type p_sig: Signature sig_size: Signature size	
Return Values	0	Normal end
	Other	Abnormal end
Special Notes	—	

3.15.5 r_fwup_wrap_get_crypt_context Function

Table 3.19 r_fwup_wrap_get_crypt_context Function Specifications

Format	void * r_fwup_wrap_get_crypt_context (void);	
Description	Returns a pointer to the context of the cryptographic library.	
Parameters	None	
Return Values	Void *	Pointer to the context of the cryptographic library
Special Notes	—	

4. Renesas Image Generator

Renesas Image Generator is a utility tool that generates firmware images for use with firmware update modules. The Renesas Image Generator can generate the following images used by the firmware update module.

- Initial image: An image file containing the bootloader and application program that is programmed using Flash Writer at the time of initial system configuration (extension: mot).
- Update image: An image file containing the firmware update (extension: rsu).

See 4.1 for how to generate an image, and 4.2 to 4.3 for details on image configuration and parameter files.

Renesas Image Generator is a program that runs on Python.

4.1 Image Generation Methods

Describes the specifications of Renesas Image Generator (image-gen.py) and how to generate an image file (initial image or update image) using this tool.

See 4.1.1 for how to generate an initial image, and 4.1.2 for how to generate an update image.

The format of the image-gen.py command is as follows:

```
python image-gen.py < options >
```

Some image-gen.py command options are required and others are optional. Table 4.1 lists the required image-gen.py options, and Table 4.2 lists the optional image-gen.py options.

Table 4.1 Required Options of image-gen.py

Option	Description
-iup <file>	Specifies the application program. For the character string < file >, specify the mot file name (the full path including the file name) of the user application program.
-ip <file>	Specifies a parameter file. For the character string < file >, specify the name of the file (the full path including the file name) containing the parameters to be input.
-o <file>	Specifies the file name of the output image. For the character string < file >, specify the file name (the full path including the file name), excluding the extension, of the firmware update image file to be output. The file extension is .mot because the output image is determined to be the initial image when the bootloader is specified with the -ibp <file> option. If you omit the -ibp <file> specification, the output image is determined to be an update image and becomes .rsu.

Table 4.2 Optional Options of image-gen.py

Option	Description
-ibp <file>	Specifies the bootloader. For the character string < file >, specify mot file name (the full path including the file name) of the bootloader program. Specify this option when generating a mot file.
--key <file>	Specify the name of the key file to be used to sign the image using ECDSA. (This option does not need to be set if sha256 is specified for the -vt option.) Store the file **secp256r1.privatekey** in the command execution folder. If the file name has been changed, specify the full path including the file name.
-vt <VerificationType>[sha256 / ecdsa]	Specifies the image verification method in the firmware update module. Appends the hash of the image if sha256 is specified, and the signature of the image if ecdsa is specified. If this option is omitted, SHA-256 is used. If ecdsa is specified, a key file specified with -key is required.
-ff <FileFormat>	Specifies the RSU format type. If BareMetal is specified, it will generate an updated image for this demo project. The updated image for BareMetal adds RSU header signature information. If RTOS is specified, generate update image for FreeRTOS OTA. The update image for FreeRTOS OTA does not add RSU header signature information (0x200 bytes data from the beginning of the update image). If this option is omitted, BareMetal is used.
-h	Output a list of commands. Specify this option to display help information for the tool.

4.1.1 Initial Image Generation Method

Renesas Image Generator has the bootloader file name (.mot) generated by build, application program (.mot), parameter file name (.csv), output file name (no extension), image verification method in firmware update module. Specify (ecdsa/sha256) as a command line option to generate an initial image file (.mot).

Command input example

```
> python image-gen.py -iup fwup_main.mot -ip
RX65N_DualBank_ImageGenerator_PRM.csv -o initial_firm -ibp
boot_loader.mot -vt ecdsa
```

fwup_main.mot: The mot file name of the user application program
RX65N_DualBank_ImageGenerator_PRM.csv: The name of the file containing the parameters to be input (Example of dual bank mode)
initial_firm: The file name of the initial image file to be output
boot_loader.mot: The mot file name of the bootloader program
ecdsa: Specifies that ECDSA is used to sign the image.

4.1.2 Update Image Generation Method

The Renesas Image Generator uses the update application program (.mot) generated by the build, parameter file name (.csv), output file name (no extension), image verification method (ecdsa/sha256) for the firmware update module. Set the command line options to generate an update image file (.rsu).

Command input example

```
> python image-gen.py -iup fwup_leddemo.mot -ip
RX65N_DualBank_ImageGenerator_PRM.csv -o fwup_leddemo -vt ecdsa
```

fwup_leddemo.mot: The mot file name of the user application program to be applied as an update
RX65N_DualBank_ImageGenerator_PRM.csv: The name of the file containing the parameters to be input (Example of dual bank mode)
fwup_leddemo: The file name of the update image file to be output
ecdsa: Specifies that ECDSA is used to sign the image.

4.2 Image File

4.2.1 Update Image File

Figure 4.1 shows the configuration diagram of the update image file generated by Renesas Image Generator.

For the format of the RSU header, see Table 43.

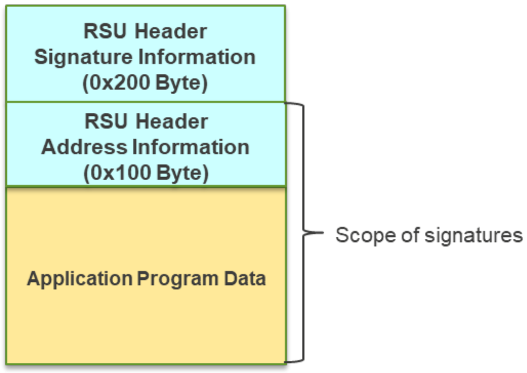


Figure 4.1 Configuring the update image file

The update image file consists of RSU header and application program data. The RSU header stores the application program location information required to verify the validity of the application program, as well as the signature value and hash value of the application program calculated based on the information. Following the RSU header, place the application program data corresponding to the program allocation information stored in the RSU header. The Renesas Image Generator arranges the application program data in the order of the data to be placed in the code flash and the data to be placed in the data flash. Valid code flash data and data flash data are extracted from the user-generated application program file (.mot), converted to binary data, and set.

The update image file has the same configuration for the dual bank method, linear mode half-updating method, and linear mode full-updating method.

Table 4.3 RSU Header Format (1/2)

Offset	Item	Length (Bytes)	Description
0x00000000	Magic Code	7	Magic code (“RELFWV2”)
0x00000007	Reserved	1	Reserved area
0x00000008	Firmware Verification Type	32	Image verification method Set sig-sha256-ecdsa to use ECDSA for image verification, and hash-sha256 to use hash.
0x00000028	Signature size	4	Data size of signature value or hash value stored in Signature Set 0x40 if Firmware Verification Type is sig-sha256-ecdsa, and 0x20 if hash-sha256.
0x0000002C	Signature	64	Signature value used for firmware verification For SHA-256 signature data, bytes 33 to 64 are set to 0x00.
0x0000006C	RSU File Size	4	File size of entire update image file
0x00000070	Reserved	400	Reserved area

Table 4.3 RSU Header Format (2/2)

Offset	Item	Length (Bytes)	Description
0x00000200	Program Data Num	4	Number of subsequent divided application programs or data flashes (maximum 31)
0x00000204	Start Address[0]	4	Start address of the first application program or data flash
0x00000208	Data Size[0]	4	Size of the first application program or data flash
0x0000020C	Start Address[1]	4	Start address of second application program or data flash
0x00000210	Data Size[1]	4	Second application program or data flash size
:	:		
0x000002F4	Start Address[30]	4	Start address of the 31st application program or data flash
0x000002F8	Data Size[30]	4	Size of the 31st application program or data flash
0x000002FC	Reserved	4	Reserved area

See Figure 4.2 for the mechanism of generating the update image file.

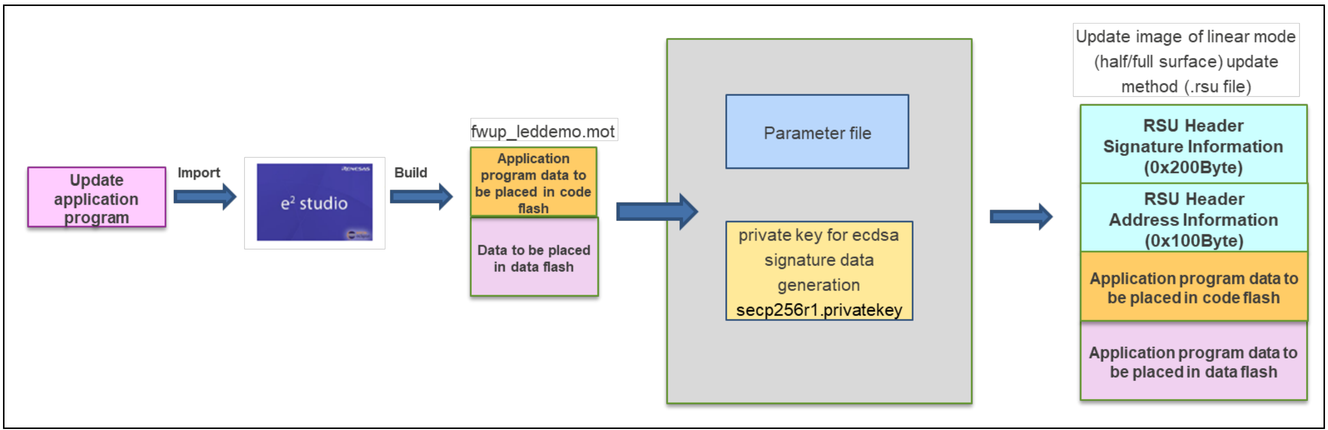


Figure 4.2 Updating image of dual bank method/linear mode (half side/whole side) updating method

- The parameter file is a CSV format file that contains the device address information required to generate the image file.
- The private key for generating the ecdsa signature value is used when ecdsa is specified as the image verification method in the firmware update module.

4.2.1 Update Image File

The initial image file is the RSU header and application program data plus the bootloader program data.

Figure 4.3 and Figure 4.4 also show a diagram of the initial image file (dual bank method/linear mode (half/full surface) update method).

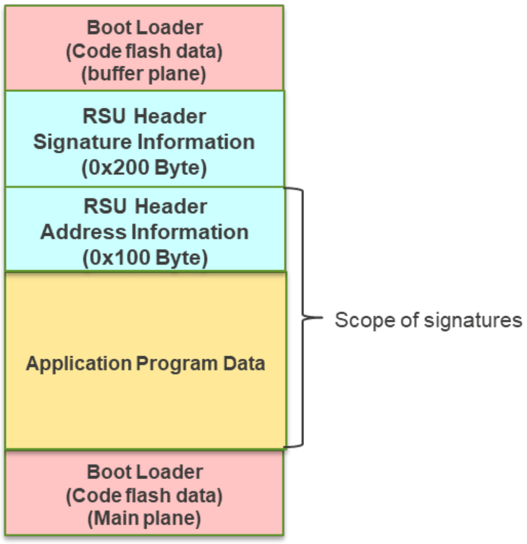


Figure 4.3 Initial image file (dual bank method) configuration

The initial image of the dual-bank method places the same bootloader code flash data on both the main and buffer sides of the code flash to support the bank switching function. The bootloader data to be placed on the main side of Code Flash uses the data in the user-generated bootloader file (boot_loader.mot) as is. The bootloader to be placed on the buffer side of the code flash is the bootloader data on the main side with the address information replaced with the bootloader location on the buffer side.

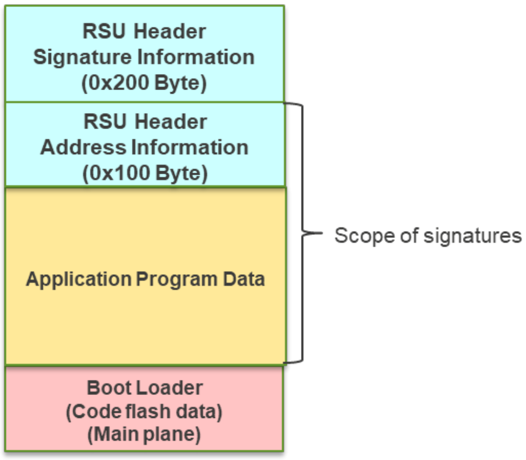


Figure 4.4 Composition of initial image file (linear mode (half/full surface) update method)

In the initial image file of the linear mode (half-face/full-face) update method, the bootloader data to be placed on the main side of the code flash uses the data in the user-generated bootloader file (boot_loader.mot) as is.

See Figure 4.5 and Figure 4.6 for the mechanism that generates the initial image file.

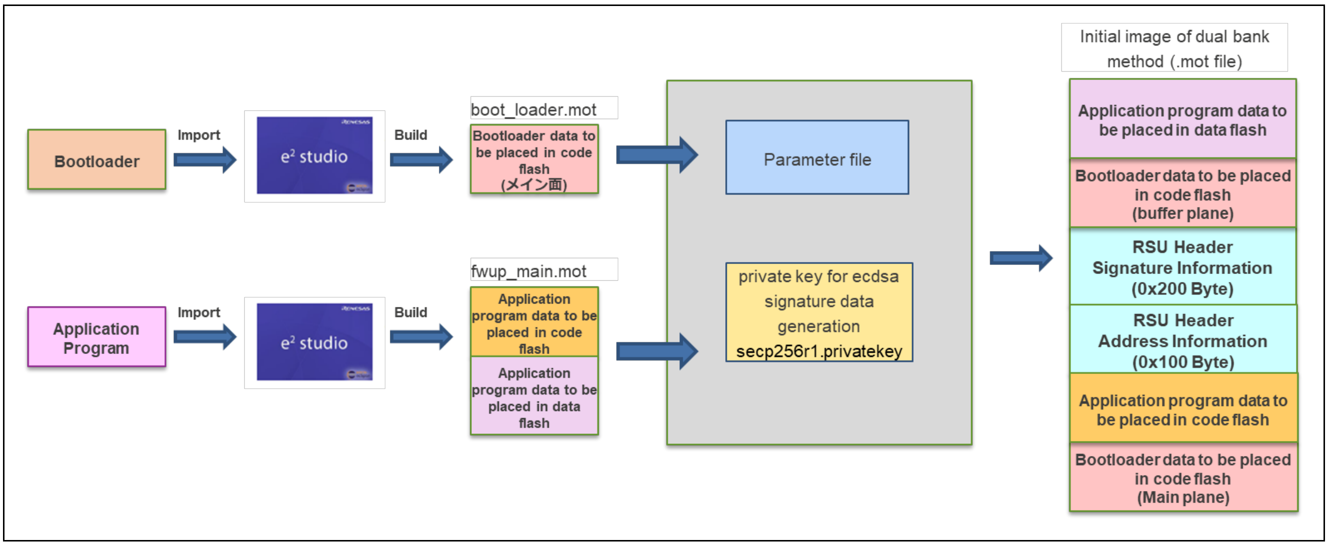


Figure 4.5 Initial image of the dual bank method

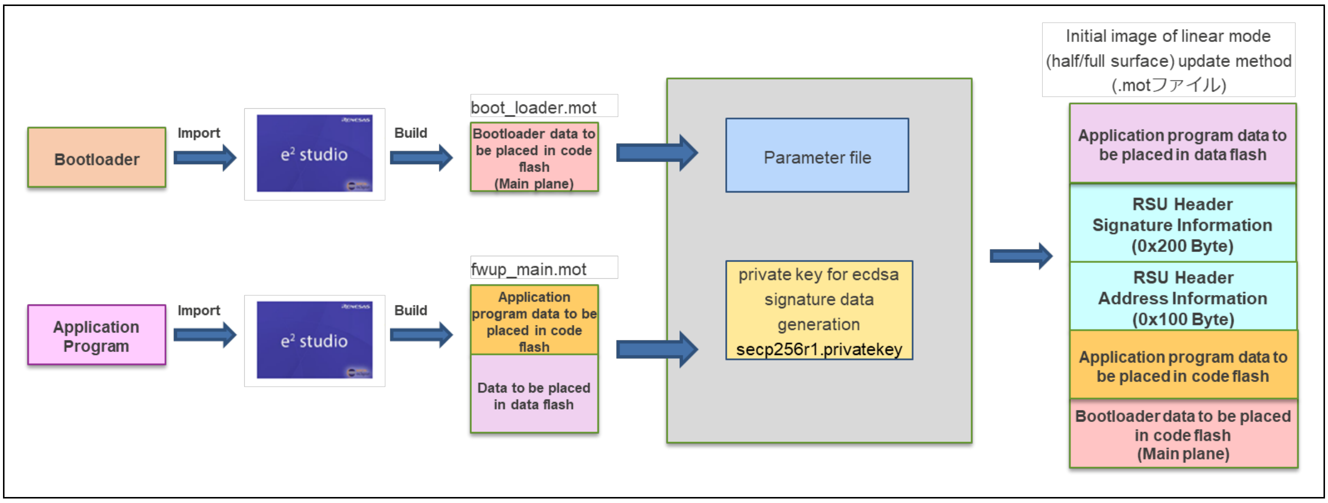


Figure 4.6 Initial image of linear mode (half/full) update method

- The parameter file is a CSV format file that contains the device address information required to generate the image file.
- The private key for generating the ecdsa signature value is used when ecdsa is specified as the image verification method in the firmware update module.

4.3 Parameter File

The parameter file is the information required for Renesas Image Generator to generate the initial and updated image files for the sample program, and is included in the release package as part of the Renesas Image Generator Python It is included in the release package as part of the Renesas Image Generator Python program set (see1.5). When a customer generates an initial or updated image for a demo project, there is no need to change the contents of the parameter file.

If you are using a product with a different flash size than the demo project (see 4.3.2) or if you do not want to include data from the data flash in the image (see 4.3.3), you can do so by editing the parameter file.

As an example, the contents of the parameter file for the RX65N (2MB) dual bank system are shown in 4.3.1.

4.3.1 Contents of Parameter File

The items listed in the parameter file are the same for all devices, but the settings differ for each device. Table 4.4 shows the contents of the parameter file for the RX65N (2MB) dual bank method demonstration project. Figure 4.7 shows the parameters referenced for image generation, and Figure 4.8 shows an example of parameters referenced for initial image generation for the RX65N (2MB) dual bank system.

Table 4.4 Contents of parameter file

Parameter name	Description	Example of setting contents RX65N(2MB)
device Type	Dual Mode：Generation of mot file for dual bank system Linear Mode：Linear mode (half/full surface) update method Mot file generation for	Dual Mode
Code Flash Size(Dual Mode Only)	Code Flash Size (Used to calculate the bootloader address of the buffer surface in the dual bank method)	0x00200000
Bootloader Start Address	Bootloader start address	0xFFFF0000
Bootloader End Address	Bootloader end address	0xFFFFFFFF
User Program Start Address	Starting address of the application program on the main face (In dual mode, application program area on main side)	0xFFF00000
User Program End Address	End address of the application program on the main side (in dual mode, application program area on main side)	0xFFFEFFFF
OFS Data Start Address	OFSM data start address (Set 'No Used.' for non-dual bank products)	0xFE7F5D00
OFS Data End Address	OFSM data end address (Set 'No Used.' for non-dual bank products)	0xFE7F5D7F
Data Flash Start Address	Data flush start address (Set 'No Used.' if data flush data is not to be generated)	0x00100000
Data Flash End Address	Data flash end address (Set 'No Used.' if data flash data is not to be generated)	0x00107FFF
Near Data Start Address(RL78 Only)	Near bootloader start address for RL78 (For RX, set 'No Used.)	No Used.
Near Data End Address(RL78 Only)	Near boot loader start address for RL78 (For RX, set 'No Used.')	No Used.
Flash Write Size	Flash write size (number of bytes required for one write to the flash in decimal)	128

The value specified for each parameter is specified in decimal for Flash Write Size and in hexadecimal (with 0x added at the beginning) for other parameters.

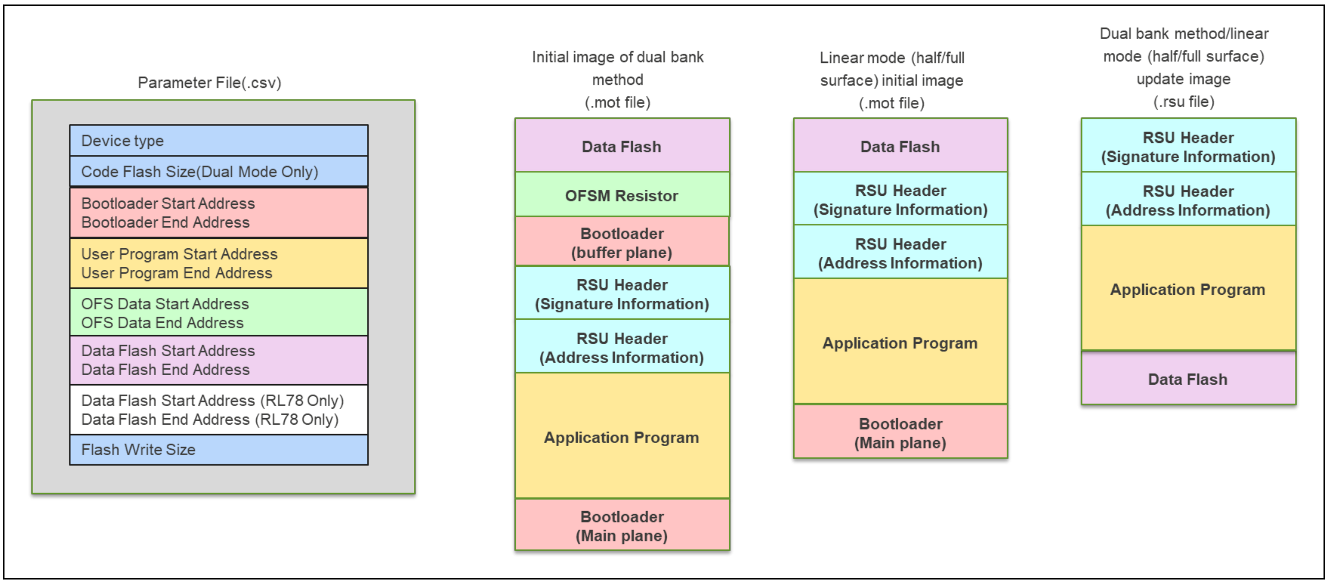


Figure 4.7 Parameters referenced when generating image files

- Device type is used to determine whether to generate a dual-banked initial image; if Device type is Dual Mode, a bootloader (main side) and a bootloader (buffer side) are generated; if Device type is Linear Mode, only the bootloader (Main plane) is generated only in the case of Linear Mode.
- Code Flash Size (Dual Mode Only) is used to determine the address where the bootloader (buffer plane) is placed.
- Using the bootloader file (boot_loader.mot) as input data, the range from Bootloader Start Address to Bootloader End Address is generated as a code flash for the bootloader (main plane).
- With the application program file (.mot) as input data, the range from User Program Start Address to User Program End Address is generated as an application program code flash.
- Using the bootloader file (boot_loader.mot) as input data, the range from OFS Data Start Address to OFS Data End Address is generated as OFS registers.
- Using the application program file (.mot) as input data, the range from Data Flash Start Address to Data Flash End Address is generated as a data flash.
- Flash Write Size is used to set the data size of the RSU header (address information) as the minimum unit when writing to the flash.

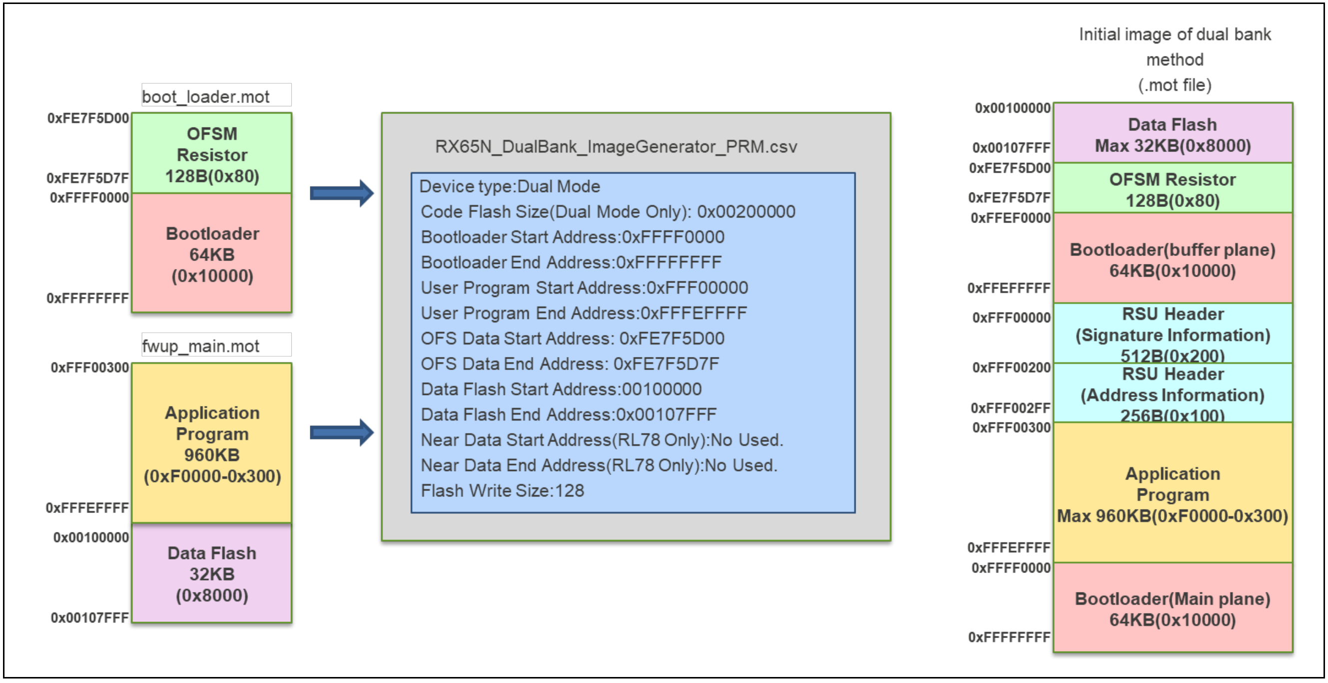


Figure 4.8 RX65N (2MB) Example of parameters referenced for initial image generation for dual bank method

4.3.2 How to generate an image with a flash size different from the demo project

If you want to perform firmware updates on products with different flash sizes that are compatible with the demo project, you can generate initial and updated images by editing the parameter file.

Figure 4.9 shows the contents of the parameter file using the RX65N dual bank method (1.5 MB) as an example. (The parameters in the red box show the differences from RX65N_DualBank_ImageGenerator_PRM.csv)

The parameter file for the RX65N dual bank method (1.5MB) is not included in the package, so customers must edit the parameters themselves.

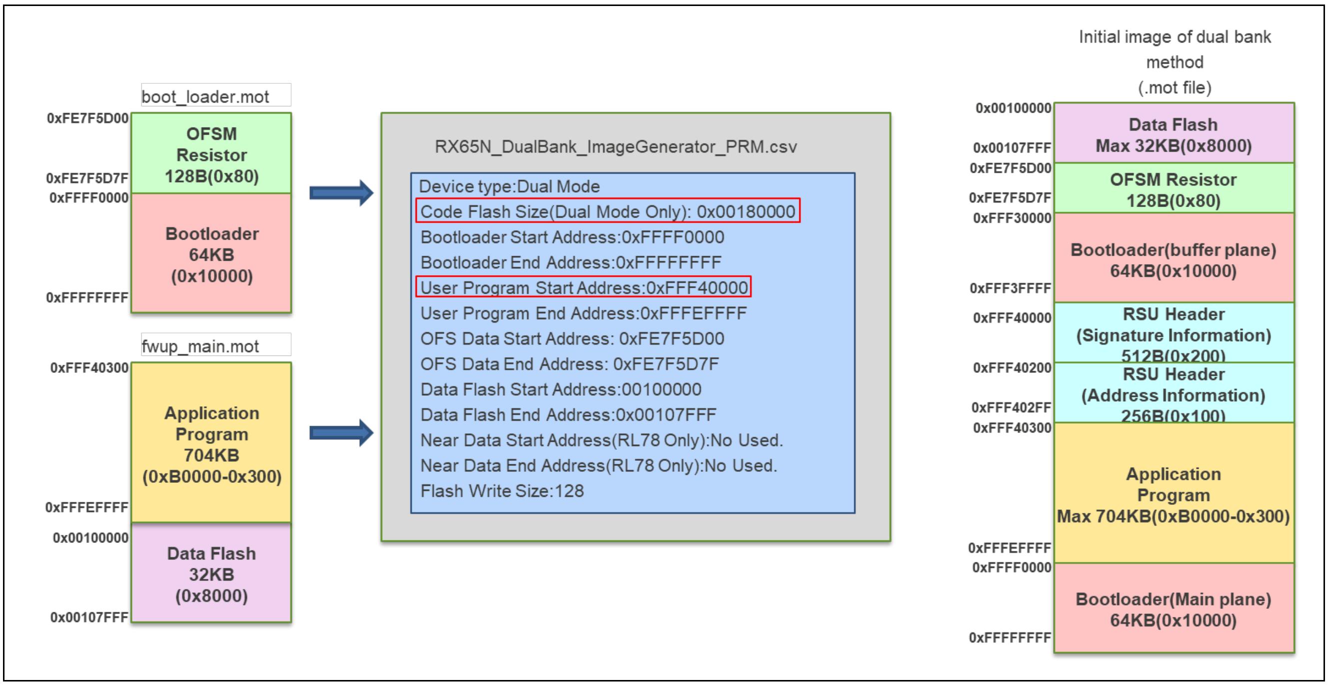


Figure 4.9 Example of parameter setting for RX65N (1.5MB) dual bank method

- Code Flash Size (Dual Mode Only) describes the code flash memory capacity of RX65N (1.5MB).
- The User Program Start Address is the address following the last address of the bootloader (buffer plane).

4.3.3 How to prevent data flash data from being included in the image

By setting the Data Flash Start Address and Data Flash End Address to "No Used." in the parameter file, the data flash data is not included in the initial image or update image. Data flash data is not included in the initial image or update image.

Figure 4.10 shows an example of parameter file settings for the RX65N (2MB) dual-bank system with the data flash not included in the update image.

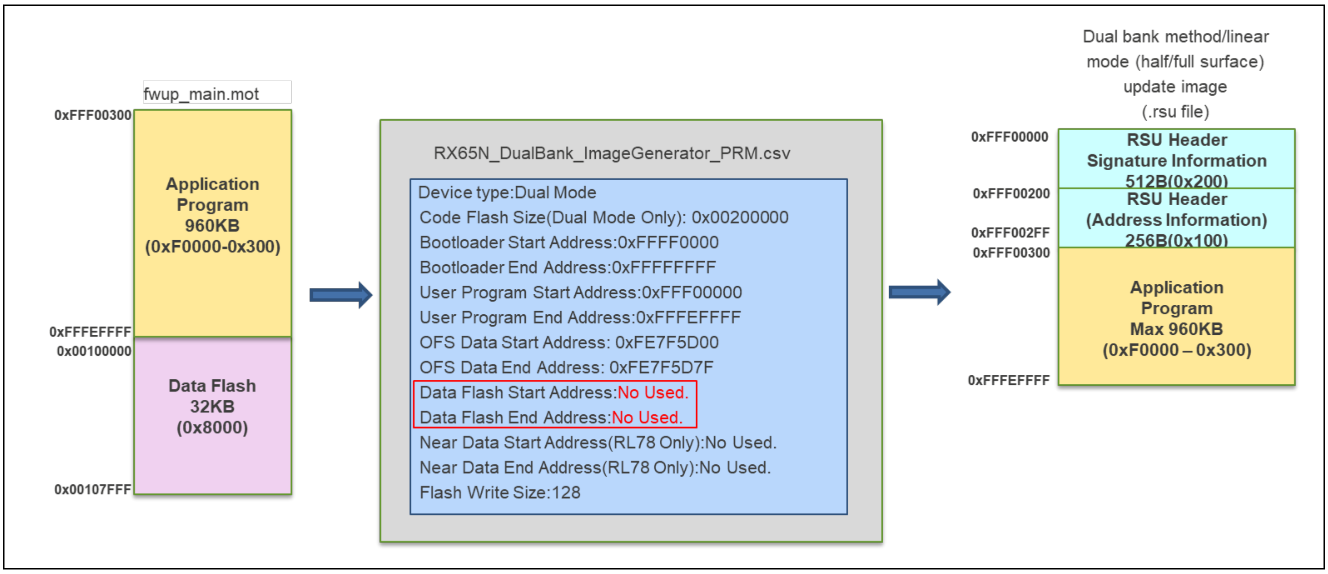


Figure 4.10 Parameter settings when data flash is not included in the update image (example for RX65N)

4.4 Image Generation Methods

Renesas Image Generator (image-gen.py) is used to generate firmware update image files. When executing a command, it is necessary to specify the information necessary for generating an image. Refer to section 3.3.1 for the initial image generation method and to section 3.3.2 for the update image generation method.

The format of the image-gen.py command is as follows:

```
python image-gen.py < options >
```

Some image-gen.py command options are required and others are optional. Table 3.2 lists the required image-gen.py options, and Table 3.3 lists the optional image-gen.py options.

Table 4.5 Required Options of image-gen.py

Option	Description
-iup <file>	For the character string < file >, specify the mot file name (the full path including the file name) of the user application program.
-ip <file>	For the character string < file >, specify the name of the file (the full path including the file name) containing the parameters to be input.
-o <file>	For the character string < file >, specify the file name (the full path including the file name), excluding the extension, of the firmware update image file to be output. The file extension (.rsu or .mot) is appended based on the file format output by Image Generator.

Table 4.6 Optional Options of image-gen.py

Option	Description
-ibp <file>	For the character string < file >, specify mot file name (the full path including the file name) of the bootloader program. Specify this option when generating a mot file.
--key <file>	Specify the name of the key file to be used to sign the image using ECDSA. (This option is not needed when signing an image using SHA-256.) Store the file **secp256r1.privatekey** in the command execution folder. If the file name has been changed, specify the full path including the file name.
-vt <VerificationType>[sha256 / ecdsa]	Specifies the signature verification method. If this option is omitted, SHA-256 is used. To specify ECDSA, a key file to be used to sign the image using ECDSA must be specified using the -key option.
-ff <FileFormat>	Specifies the RSU format type. If this option is omitted, **BareMetal** is used. To generate an RTOS-compatible RSU file, specify RTOS.
-h	Specify this option to display help information for the tool.

4.4.1 Initial Image Generation Method

For the initial image, specify the options for the bootloader file name (.mot) and user program (initial firmware) file name (.mot) generated by building the projects, the parameter file name (.csv), the output file name (extension omitted), and the signature verification method (ECDSA or SHA-256), then generate an image file (.mot).

A command input example is shown below:

python image-gen.py -iup fwup_main.mot -ip RX26T_ImageGenerator_PRM.csv -o initial_firm -ibp boot_loader.mot -vt ecdsa

fwup_main.mot: The mot file name of the user application program
RX26T_ImageGenerator_PRM.csv: The name of the file containing the parameters to be input
initial_firm: The file name of the initial image file to be output
boot_loader.mot: The mot file name of the bootloader program
ecdsa: Specifies that ECDSA is used to sign the image.

4.4.2 Update Image Generation Method

For the update image, specify the options for the user program (initial firmware) file name (.mot), the parameter file name (.csv), the output file name (extension omitted), and the signature verification method (ECDSA or SHA-256), then generate an image file (.rsu).

A command input example is shown below:

python image-gen.py -iup fwup_leddemo.mot -ip RX26T_ImageGenerator_PRM.csv -o fwup_leddemo -ibp boot_loader.mot -vt ecdsa

fwup_leddemo.mot: The mot file name of the user application program to be applied as an update
RX26T_ImageGenerator_PRM.csv: The name of the file containing the parameters to be input
fwup_leddemo: The file name of the update image file to be output
boot_loader.mot: The mot file name of the bootloader program
ecdsa: Specifies that ECDSA is used to sign the image.

5. Demo Project

The demo project is a sample program that shows how to implement firmware update functionality using the serial communications interface (SCI).

5.1 Demo project Structure

The demo project comprises the FIT module, modules dependent on it, and a main() function that implements the firmware update demonstration. Versions of the demo project for the devices and compilers listed in 1.5 are provided.

The firmware update demo consists of the following projects.

Dual-bank method folder structure：Under □□\dualbank\△△\

Linear mode half-surface update method folder structure：Under □□\ w_buffer \△△\

Linear mode full update method folder structure: Under □□\ wo_buffer \△△\

□□ : Device name

△△ : Compiler (ccrx/gcc/iar)

- boot_loader: Bootloader
  This program runs first after a reset. It verifies that the user program has not been tampered with and then, if verification is successful, launches the user program.
- fwup_main: Application program
  An application program (initial firmware) that downloads updated firmware and performs signature verification.
- fwup_leddemo: Application program (for update)
  This is an application program (for updating) that blinks an LED.

5.2 Operating environment preparation

To run the firmware update demo project, you need to install the tools (see 5.2.1 to 5.2.4) on your Windows PC. Also, use a USB serial conversion board (see 5.2.5) that connects the Windows PC and the target board.

5.2.1 Installing TeraTerm

Used to transfer the firmware update image via serial communication from a Windows PC to the target board. In the demo project, we have checked the operation with TeraTerm 4.105.

After installation, set the serial port communication settings as shown in Table Table 5.1

Table 5.1 Communication Specifications

Item	Description
Communication system	Asynchronous communication
Bit rate	115,200 bps
Data length	8 bits
Parity	None
Stop bit	1 bit
Flow control	CTS/RTS

5.2.2 Installing the Python execution environment

Used by Renesas Image Generator (image-gen.py) to create initial and update images.

Renesas Image Generator uses ECDSA to generate signature data. In the demo project, environment operation is confirmed with Python 3.9.0.

Install Python 3.9.0 or higher.

In addition, since the Python encryption library (pycryptodome) is used, after installing Python, execute the following pip command from the command prompt to install the library.

```
pip install pycryptodome
```

5.2.3 Installing the OpenSSL execution environment

OpenSSL is used to generate the keys needed to generate and verify ECDSA signature data for initial and update images.

Download the OpenSSL installer from the following URL and install it. There is no problem with the Light version.

https://slproweb.com/products/Win32OpenSSL.html

5.2.4 Installing the Flash Writer

A flash writer is required to write the initial image.

The demo project uses Renesas Flash Programmer v3.11.01.

Renesas Flash Programmer (Programming GUI) | Renesas

5.2.5 USB serial conversion board

Used to transfer the firmware update image via serial communication from a Windows PC to the target board.

For details on how to connect with the target board, refer to the operation confirmation environment (6.2) of the relevant target board.

Use Pmod USBUART (manufactured by DIGILENT).

https://reference.digilentinc.com/reference/pmod/pmodusbuart/start

5.3 Execution environment preparation

5.3.1 Generating Keys for Signature Generation and Verification

Use OpenSSL for key generation. Refer to 5.2.3 in advance and install OpenSSL.

Execute the following OpenSSL commands to generate an elliptic curve cryptography (secp256r1) key pair to be used to generate and verify image signatures, and to extract the private and public keys:

```
>openssl ecparam -genkey -name secp256r1 -out secp256r1.keypair
using curve name prime256v1 instead of secp256r1

>openssl ec -in secp256r1.keypair -outform PEM -out secp256r1.privatekey
read EC key
writing EC key

> openssl ec -in secp256r1.keypair -outform PEM -pubout -out
secp256r1.publickey
read EC key
writing EC key
```

5.3.2 Preparing the execution environment for Renesas Image Generator

Unzip ImageGenerator.zip included in the package to any folder on your Windows PC. Make sure the folder name does not contain double-byte characters.

Renesas Image Generator requires a Python execution environment, so refer to 5.2.2 and install Python in advance.

5.4 Demo Project Execution Procedure

The execution procedure of the demo project differs depending on the firmware update method.

This chapter describes the procedure for executing the demo project using the RX65N (2MB) as an example.

The demo project execution procedure is the same for other MCU products, but only the operation check environment differs for each MCU, so check the operation check environment (6.2) for the applicable MCU product.

5.4.1 Dual Bank Method

5.4.1.1 Execution Environment

Prepare the RX65N operation check environment (6.2.1). For MCU products other than RX65N, please refer to the operation confirmation environment of the applicable MCU product.

5.4.1.2 Building The Demo Project

Follow the steps below to build three demo projects for the dual-bank method in linear mode.

1. Import the boot_loader, fwup_leddemo, and fwup_main demo projects into the integrated development environment.
2. Add the public key to be used to verify the image to the demo project.
   Paste the contents of secp256r1.publickey generated as described in 5.3.1 into code_signer_public_key.h in the boot_loader and fwup_main projects.

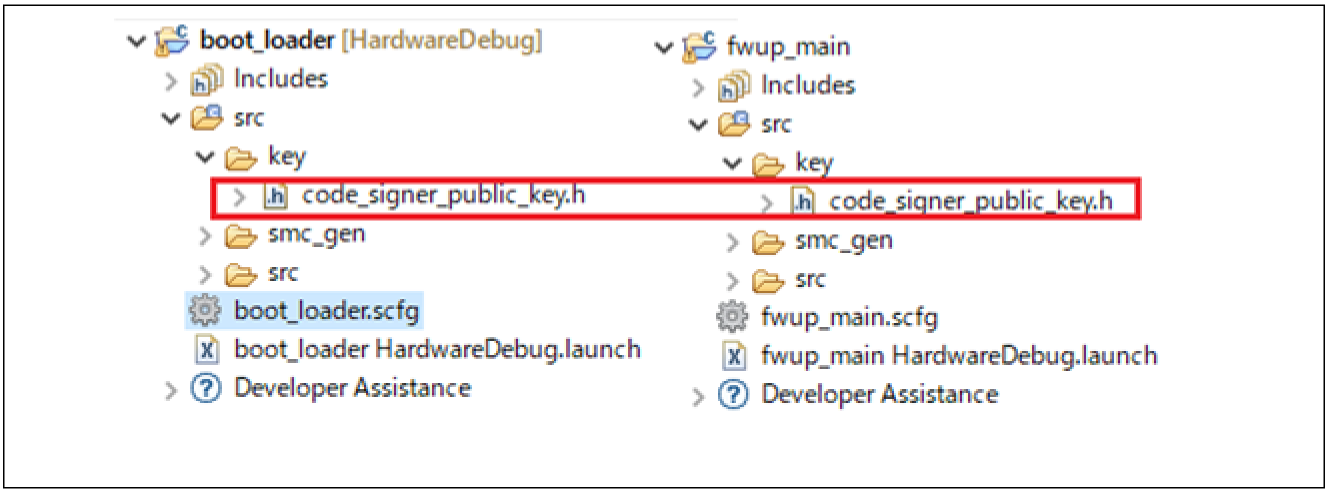


Figure 5.1 Storage Location of code_signer_public_key.h File in Demo Project

```
/*
 * PEM-encoded code signer public key.
 *
 * Must include the PEM header and footer:
 * "-----BEGIN CERTIFICATE-----\n"\
 * "...base64 data...\n"\
 * "-----END CERTIFICATE-----"
 */
#define CODE_SIGNER_PUBLIC_KEY_PEM \
"-----BEGIN PUBLIC KEY-----"\
Paste the contents of secp256r1.publickey here.
"-----END PUBLIC KEY-----"\
#endif /* CODE_SIGNER_PUBLIC_KEY_H_ */
```

3. Build the demo projects.
   Build the three demo projects and confirm that the following mot files have been generated: boot_loader.mot, fwup_leddemo.mot, and fwup_main.mot.

5.4.1.3 Creating Initial and Update Images

The procedure for creating the initial and update images, using initial_firm.mot as the name of the initial image and fwup_leddemo.rsu as the name of the update image is, is described below.

1. Store the mot files created by building the demo projects in the same folder as Renesas Image Generator.

```
image-gen.py
RX65N_DualBank_ImageGenerator_PRM.csv
RX65N_Linear_Full_ImageGenerator_PRM.csv
RX65N_Linear_Half_ImageGenerator_PRM.csv
boot_loader.mot
fwup_main.mot
fwup_leddemo.mot
secp256r1.privatekey
```

2. Execute the following command to create the initial image:

```
> python image-gen.py -iup fwup_main.mot -ip
RX65N_DualBank_ImageGenerator_PRM.csv -o initial_firm -ibp
boot_loader.mot -vt ecdsa

Successfully generated the initial_firm.mot file.
```

3. Execute the following command to create the update image:

```
> python image-gen.py -iup fwup_leddemo.mot -ip
RX65N_DualBank_ImageGenerator_PRM.csv -o fwup_leddemo -vt ecdsa

Successfully generated the fwup_leddemo.rsu file.
```

Confirm that the initial and update image have been created in the same folder as Renesas Image Generator.

```
image-gen.py
RX65N_DualBank_ImageGenerator_PRM.csv
RX65N_Linear_Full_ImageGenerator_PRM.csv
RX65N_ Linear_Half_ImageGenerator_PRM.csv
boot_loader.mot
fwup_main.mot
fwup_leddemo.mot
secp256r1.privatekey
fwup_leddemo.rsu
initial_firm.mot
```

5.4.1.4 Programming the Initial Image

Use Flash Writer to program the initial image (initial_firm.mot) to the MCU board. After programming, turn off the power to the board and disconnect the debugger (E2 Lite).

5.4.1.5 Executing a Firmware Update

Once the initial image firmware is activated, it waits for the transfer of the update image via the terminal emulator. The received update image is programmed to the flash memory, and after the transfer completes, the signature of the update image is verified and the firmware is activated.

Follow the steps below to execute a firmware update.

1. Refer to 6.2.1, Execution Environment, and connect the devices.
2. Launch the terminal emulator software on the PC, select the serial COM port, and configure the connection settings.
3. Power on the board. The following message is output:

```
==== RX65N : BootLoader [dual bank] ====
verify install area main [sig-sha256-ecdsa]...OK
execute new image ...

==== RX65N : Update from User [dual bank] ver 1.0.0 ====
send user program (*.rsu) via UART.
```

4. Send the updated image via the terminal emulator.
   Send file… > check **Binary** > fwup_leddemo.rsu
   The following message is output during the transfer of the update image, and a software reset is applied after installation and signature verification complete.

```
W 0xFFE00000, 256 ... OK
W 0xFFE00100, 256 ... OK
...
W 0xFFE03B00, 128 ... OK
W 0xFFEEFF80, 128 ... OK
```

5. After installing the updated firmware and verifying the signature, it jumps to the updated firmware and executes the program after processing such as bank swap.

```
verify install area buffer [sig-sha256-ecdsa]...OK
bank swap ...
software reset...
```

6. When the bootloader completes signature verification, the update image firmware is activated. When the process completes successfully, the following message is output and the LED flashes.

```
==== RX65N : BootLoader [dual bank] ====
verify install area main [sig-sha256-ecdsa]...OK
execute image ...

----------------------------------------
FWUP demo (ver 0.1.1)
----------------------------------------
Check the LEDs on the board.
```

Note: The demo program does not erase the buffer side. If you need to erase the image before updating for rollback measures, please add a process to erase the buffer side image.

5.4.2 Operation of Linear Mode Partial Update Method

5.4.2.1 Execution Environment

Prepare the RX65N operation check environment (6.2.1). For MCU products other than RX65N, please refer to the operation confirmation environment of the applicable MCU product.

5.4.2.2 Building The Demo Project

Follow the steps below to build three demo projects for the partial update method in linear mode.

1. Import the boot_loader, fwup_leddemo, and fwup_main demo projects into the integrated development environment.
2. Add the public key to be used to verify the image to the demo project.
   Paste the contents of secp256r1.publickey generated as described in 5.3.1 into code_signer_public_key.h in the boot_loader and fwup_main projects.

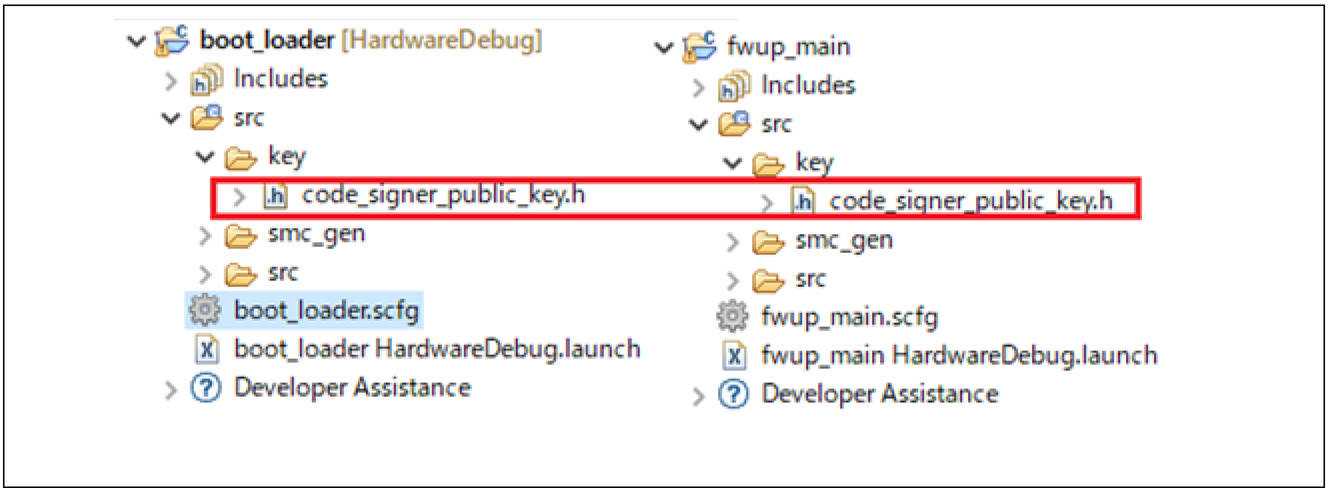


Figure 5.2 Storage Location of code_signer_public_key.h File in Demo Project

```
/*
 * PEM-encoded code signer public key.
 *
 * Must include the PEM header and footer:
 * "-----BEGIN CERTIFICATE-----\n"\
 * "...base64 data...\n"\
 * "-----END CERTIFICATE-----"
 */
#define CODE_SIGNER_PUBLIC_KEY_PEM \
"-----BEGIN PUBLIC KEY-----"\
Paste the contents of secp256r1.publickey here.
"-----END PUBLIC KEY-----"\
#endif /* CODE_SIGNER_PUBLIC_KEY_H_ */
```

3. Build the demo projects.
   Build the three demo projects and confirm that the following mot files have been generated: boot_loader.mot, fwup_leddemo.mot, and fwup_main.mot.

5.4.2.3 Creating Initial and Update Images

The procedure for creating the initial and update images, using initial_firm.mot as the name of the initial image and fwup_leddemo.rsu as the name of the update image is, is described below.

1. Store the mot files created by building the demo projects in the same folder as Renesas Image Generator.

```
image-gen.py
RX65N_DualBank_ImageGenerator_PRM.csv
RX65N_Linear_Full_ImageGenerator_PRM.csv
RX65N_Linear_Half_ImageGenerator_PRM.csv
boot_loader.mot
fwup_main.mot
fwup_leddemo.mot
secp256r1.privatekey
```

2. Execute the following command to create the initial image:

```
> python image-gen.py -iup fwup_main.mot -ip
RX65N_Linear_Half_ImageGenerator_PRM.csv -o initial_firm -ibp
boot_loader.mot -vt ecdsa

Successfully generated the initial_firm.mot file.
```

3. Execute the following command to create the update image:

```
> python image-gen.py -iup fwup_leddemo.mot -ip
RX65N_Linear_Half_ImageGenerator_PRM.csv -o fwup_leddemo -vt ecdsa

Successfully generated the fwup_leddemo.rsu file.
```

Confirm that the initial and update image have been created in the same folder as Renesas Image Generator.

```
image-gen.py
RX65N_DualBank_ImageGenerator_PRM.csv
RX65N_Linear_Full_ImageGenerator_PRM.csv
RX65N_ Linear_Half_ImageGenerator_PRM.csv
boot_loader.mot
fwup_main.mot
fwup_leddemo.mot
secp256r1.privatekey
fwup_leddemo.rsu
initial_firm.mot
```

5.4.2.4 Programming the Initial Image

Use Flash Writer to program the initial image (initial_firm.mot) to the MCU board. After programming, turn off the power to the board and disconnect the debugger (E2 Lite).

5.4.2.5 Executing a Firmware Update

Once the initial image firmware is activated, it waits for the transfer of the update image via the terminal emulator. The received update image is programmed to the flash memory, and after the transfer completes, the signature of the update image is verified and the firmware is activated.

Follow the steps below to execute a firmware update.

1. Refer to 6.2.1, Execution Environment, and connect the devices.
2. Launch the terminal emulator software on the PC, select the serial COM port, and configure the connection settings.
3. Power on the board. The following message is output:

```
==== RX65N : BootLoader [with buffer] ====
verify install area main [sig-sha256-ecdsa]...OK
execute image ...

==== RX65N : Update from User [with buffer]  ver 1.0.0 ====
send image(*.rsu) via UART.
```

4. Send the updated image via the terminal emulator.
   Send file… > check **Binary** > fwup_leddemo.rsu
   The following message is output during the transfer of the update image, and a software reset is applied after installation and signature verification complete.

```
W 0xFFF00000, 256 ... OK
W 0xFFF00100, 256 ... OK
...
W 0xFFF03B00, 128 ... OK
W 0xFFFEFF80, 128 ... OK
verify install area buffer [sig-sha256-ecdsa]...OK
software reset...
```

5. Activation processing is performed by the bootloader and a second software reset is applied.

```
==== RX65N : BootLoader [with buffer] ====
verify install area buffer [sig-sha256-ecdsa]...OK
activating image ... OK
software reset...
```

6. When the bootloader completes signature verification, the update image firmware is activated. When the process completes successfully, the following message is output and the LED flashes.

```
==== RX65N : BootLoader [with buffer] ====
verify install area main [sig-sha256-ecdsa]...OK
execute image ...

----------------------------------------
FWUP demo (ver 0.1.1)
----------------------------------------
Check the LEDs on the board.
```

Note: The demo program does not erase the buffer side. If you need to erase the image before updating for rollback measures, please add a process to erase the buffer side image.

5.4.3 Operation of Linear Mode Full Update Method

5.4.3.1 Execution Environment

Prepare the RX65N operation check environment (6.2.1). For MCU products other than RX65N, please refer to the operation confirmation environment of the applicable MCU product.

5.4.3.2 Building The Demo Project

Follow the steps below to build two demo projects for the full update method in linear mode.

1. Import the boot_loader, fwup_leddemo, and fwup_main demo projects into the integrated development environment.
2. Add the public key to be used to verify the image to the demo project.
   Paste the contents of secp256r1.publickey generated as described in 5.3.1 into code_signer_public_key.h in the boot_loader and fwup_main projects.

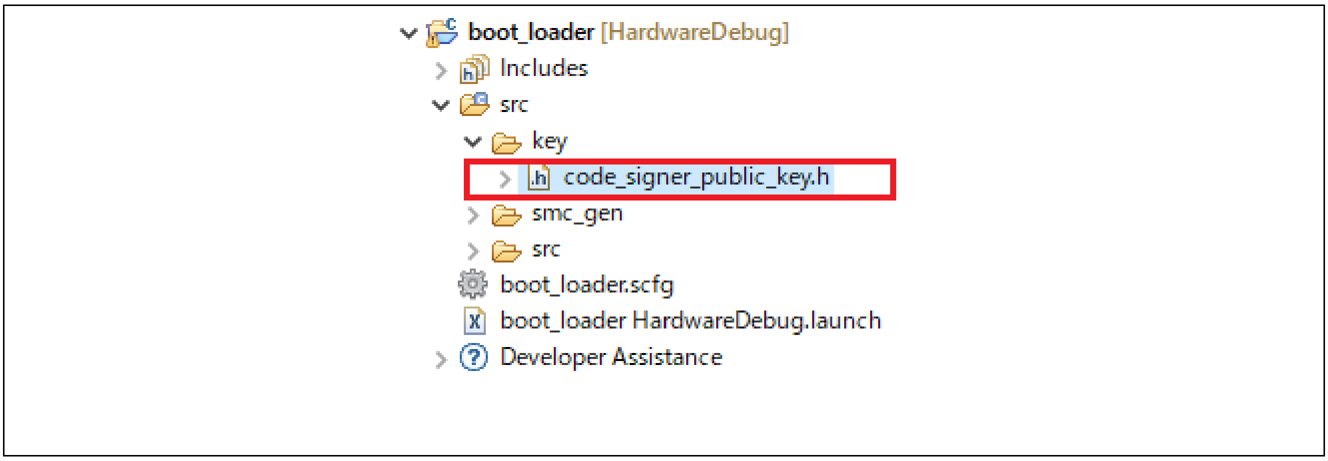


Figure 5.3 Storage Location of code_signer_public_key.h File in Demo Project

```
/*
 * PEM-encoded code signer public key.
 *
 * Must include the PEM header and footer:
 * "-----BEGIN CERTIFICATE-----\n"\
 * "...base64 data...\n"\
 * "-----END CERTIFICATE-----"
 */
#define CODE_SIGNER_PUBLIC_KEY_PEM \
"-----BEGIN PUBLIC KEY-----"\
Paste the contents of secp256r1.publickey here.
"-----END PUBLIC KEY-----"\
#endif /* CODE_SIGNER_PUBLIC_KEY_H_ */
```

3. Build the demo projects.
   Build the first project (boot_loader) to generate boot_loader.mot.
   Build the second project (fwup_leddemo) to generate fwup_leddemo.mot.
   Rename fwup_leddemo.mot to fwup_leddemo_011.mot.
   Change the version of the second project (fwup_leddemo) as follows, then build it to generate fwup_leddemo.mot.

```
fwup_leddemo.c
---
#define FWUP_DEMO_VER_MAJOR        (0)
#define FWUP_DEMO_VER_MINOR        (1)
#define FWUP_DEMO_VER_BUILD        (1)★1->2
```

   Rename fwup_leddemo.mot to fwup_leddemo_012.mot.

5.4.3.3 Creating Initial and Update Images

The procedure for creating the initial and update images, using initial_firm.mot as the name of the initial image and fwup_leddemo.rsu as the name of the update image is, is described below.

1. Store the mot files created by building the demo projects in the same folder as Renesas Image Generator.

```
image-gen.py
RX65N_DualBank_ImageGenerator_PRM.csv
RX65N_Linear_Full_ImageGenerator_PRM.csv
RX65N_Linear_Half_ImageGenerator_PRM.csv
boot_loader.mot
fwup_leddemo_011.mot
fwup_leddemo_012.mot
secp256r1.privatekey
```

2. Execute the following command to create the initial image:

```
> python image-gen.py -iup fwup_leddemo_011.mot -ip
RX65N_Linear_Full_ImageGenerator_PRM.csv -o initial_firm -ibp
boot_loader.mot -vt ecdsa

Successfully generated the initial_firm.mot file.
```

3. Execute the following command to create the update image:

```
> python image-gen.py -iup fwup_leddemo_012.mot -ip
RX65N_Linear_Full_ImageGenerator_PRM.csv -o fwup_leddemo_012 -vt
ecdsa

Successfully generated the fwup_leddemo.rsu file.
```

Confirm that the initial and update image have been created in the same folder as Renesas Image Generator.

```
image-gen.py
RX65N_DualBank_ImageGenerator_PRM.csv
RX65N_Linear_Full_ImageGenerator_PRM.csv
RX65N_Linear_Half_ImageGenerator_PRM.csv
boot_loader.mot
fwup_main.mot
fwup_leddemo.mot
secp256r1.privatekey
fwup_leddemo_012.rsu
initial_firm.mot
```

5.4.3.4 Programming the Initial Image

Use Flash Writer to program the initial image (initial_firm.mot) to the MCU board. After programming, turn off the power to the board and disconnect the debugger (E2 Lite).

5.4.3.5 Executing a Firmware Update

Once the initial image firmware is activated, it waits for the transfer of the update image via the terminal emulator. The received update image is programmed to the flash memory, and after the transfer completes, the signature of the update image is verified and the firmware is activated.

Follow the steps below to execute a firmware update.

1. Refer to 6.2.1, Execution Environment, and connect the devices.

2. Launch the terminal emulator software on the PC, select the serial COM port, and configure the connection settings.

3. Power on the board. The following message is output:

```
==== RX65N : BootLoader [without buffer] ====
verify install area main [sig-sha256-ecdsa]...OK
execute image ...

----------------------------------------
FWUP demo (ver 0.1.1)
----------------------------------------
```

4. Press RESET_SW while holding down USER_SW.

```
==== RX65N : Image updater [without buffer] ====
send image(*.rsu) via UART.
```

5. Send the updated image via the terminal emulator.
   Send file… > check **Binary** > fwup_leddemo_012.rsu

   The following message is output during the transfer of the update image, and a software reset is applied after installation and signature verification complete.

```
W 0xFFE00000, 128 ... OK
W 0xFFE00080, 128 ... OK
...
W 0xFFE03B00, 128 ... OK
W 0xFFFEFF80, 128 ... OK
verify install area main [sig-sha256-ecdsa]...OK
software reset...
```

6. When the bootloader completes signature verification, the update image firmware is activated. When the process completes successfully, the following message is output and the LED flashes.

```
==== RX65N : BootLoader [without buffer] ====
verify install area main [sig-sha256-ecdsa]...OK
execute image ...

---------------------------------------
FWUP demo (ver 0.1.2)
---------------------------------------
Check the LEDs on the board.
```

6. Appendices

6.1 Confirmed Operation Environments

This section describes environments on which the operation of the FIT module has been confirmed.

Table 6.1 Confirmed Operation Environment (CC-RX)

Item	Description
Integrated development environment	Renesas Electronics e2 studio 2023-01
C compiler	Renesas Electronics C/C++ Compiler for RX Family V3.04.00 Compiler option: The following option is added to the default settings of the integrated development environment. -lang = c99
Endian order	Little endian
Revision of the module	Rev.2.01
Board used	Renesas Starter Kit+ for RX24T（product No.：RTK500524TS00000BE） Renesas Flexible Motor Control Kit for RX26T (product No.: RTK0EMXE70S00020BJ) Renesas Starter Kit+ for RX65N（product No.：RTK50565N2SxxxxxBE） Renesas Starter Kit for RX66T (product No.:RTK50566T0S00000BE) Renesas Starter Kit for RX660 (product No.:RTK556609HCxxxxxBJ) Renesas Starter Kit+ for RX671 (product No.:RTK55671EHS10000BE) Renesas Starter Kit+ for RX72N (product No.:RTK5572NNxxxxxxxBE)

Table 6.2 Confirmed Operation Environment (GCC)

Item	Description
Integrated development environment	Renesas Electronics e2 studio 2023-01
C compiler	GCC for Renesas RX 8.3.0.202202 Compiler option: The following option is added to the default settings of the integrated development environment. -std=gnu99
Endian order	Little endian
Revision of the module	Rev.2.01
Board used	Renesas Starter Kit+ for RX24T（product No.：RTK500524TS00000BE） Renesas Flexible Motor Control Kit for RX26T (product No.: RTK0EMXE70S00020BJ) Renesas Starter Kit+ for RX65N（product No.：RTK50565N2SxxxxxBE） Renesas Starter Kit for RX66T (product No.:RTK50566T0S00000BE) Renesas Starter Kit for RX660 (product No.:RTK556609HCxxxxxBJ) Renesas Starter Kit+ for RX671 (product No.:RTK55671EHS10000BE) Renesas Starter Kit+ for RX72N (product No.:RTK5572NNxxxxxxxBE)

Table 6.3 Confirmed Operation Environment (IAR)

Item	Description
Integrated development environment	IAR Embedded Workbench for Renesas RX 4.20.1
C compiler	IAR C/C++ Compiler for Renesas RX 4.20.1 Compiler option: The default settings of the integrated development environment
Endian order	Little endian
Revision of the module	Rev.2.01
Board used	Renesas Starter Kit+ for RX24T（product No.：RTK500524TS00000BE） Renesas Flexible Motor Control Kit for RX26T (product No.: RTK0EMXE70S00020BJ) Renesas Starter Kit+ for RX65N（product No.：RTK50565N2SxxxxxBE） Renesas Starter Kit for RX66T (product No.:RTK50566T0S00000BE) Renesas Starter Kit for RX660 (product No.:RTK556609HCxxxxxBJ) Renesas Starter Kit+ for RX671 (product No.:RTK55671EHS10000BE) Renesas Starter Kit+ for RX72N (product No.:RTK5572NNxxxxxxxBE)

The versions of the FIT modules used by the demo project to confirm firmware update operation are listed below.

(1) Renesas Electronics C/C++ Compiler Package for RX Family

Table 6.4 FIT Module Versions (CC-RX)

Device	Project	r_bsp	r_byteq	r_flash_rx	r_sci_rx	r_fwup_rx
RX24T	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX26T	boot_loader	7.30	2.10	5.00	4.80	2.01
	fwup_main	7.30	2.10	5.00	4.80	2.01
	fwup_leddemo	7.30	2.10	-	4.80	-
RX65N	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX65T	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX660	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX671	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX72N	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-

(2) GCC for Renesas RX

Table 6.5 FIT Module Versions (GCC)

Device	Project	r_bsp	r_byteq	r_flash_rx	r_sci_rx	r_fwup_rx
RX24T	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX26T	boot_loader	7.30	2.10	5.00	4.80	2.01
	fwup_main	7.30	2.10	5.00	4.80	2.01
	fwup_leddemo	7.30	2.10	-	4.80	-
RX65N	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX65T	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX660	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX671	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX72N	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-

(3) IAR C/C++ Compiler for RX

Table 6.6 FIT Module Versions (IAR)

Device	Project	r_bsp	r_byteq	r_flash_rx	r_sci_rx	r_fwup_rx
RX24T	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX26T	boot_loader	7.40	2.10	5.00	4.80	2.01
	fwup_main	7.40	2.10	5.00	4.80	2.01
	fwup_leddemo	7.40	2.10	-	4.80	-
RX65N	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX65T	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX660	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX671	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-
RX72N	boot_loader	7.21	2.10	4.91	4.60	2.01
	fwup_main	7.21	2.10	4.91	4.60	2.01
	fwup_leddemo	7.21	2.10	-	4.60	-

6.2 Operating Environment for Demo Project

This module supports multiple compilers. When using this module, the different settings for each compiler are shown below.

6.2.1 Operation Confirmation Environment for RX65N

The execution environment and connection diagram are shown below.

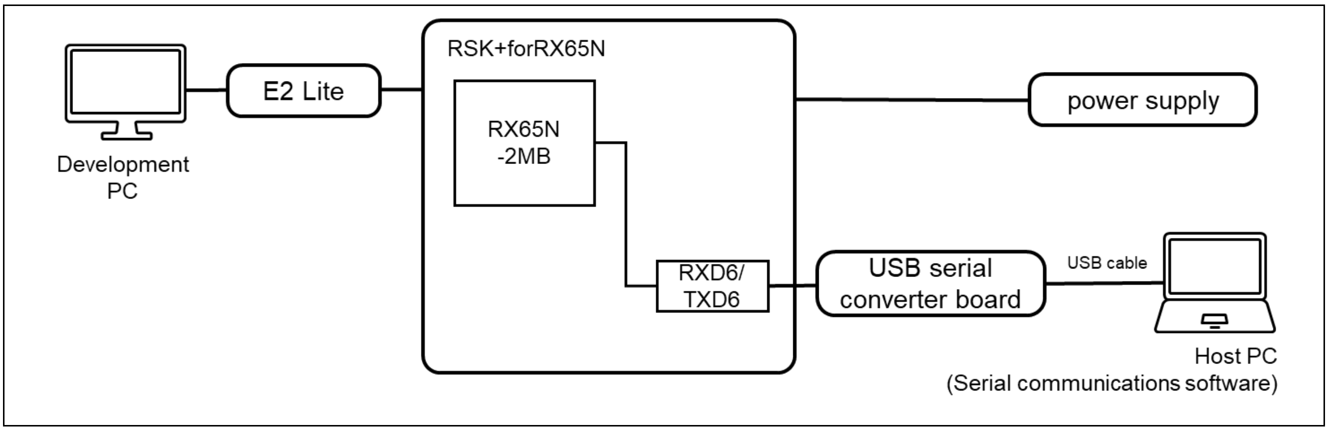


Figure 6.1 RSK-RX65N Device Connection Diagram

The pin assignment is shown in the figure below.

■ UART(Red)

PMOD1		USB-UART
2	TXD6	RX
3	RXD6	TX
4	P02(RTS)	CTS

■ USER_SW (Green)

SW1	Note
P03	LOW : USER_SW is ON

■ Reset Switch (Blue)

RES1	Note
RES#	LOW : USER_SW is ON

■ USER_LED (Yellow)

LED0	Note
P73	LED0

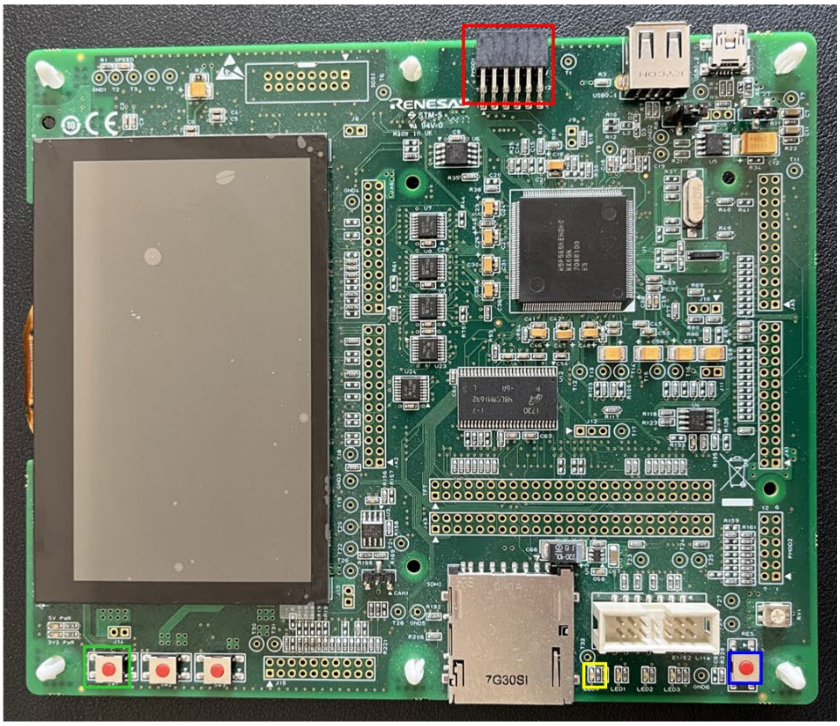
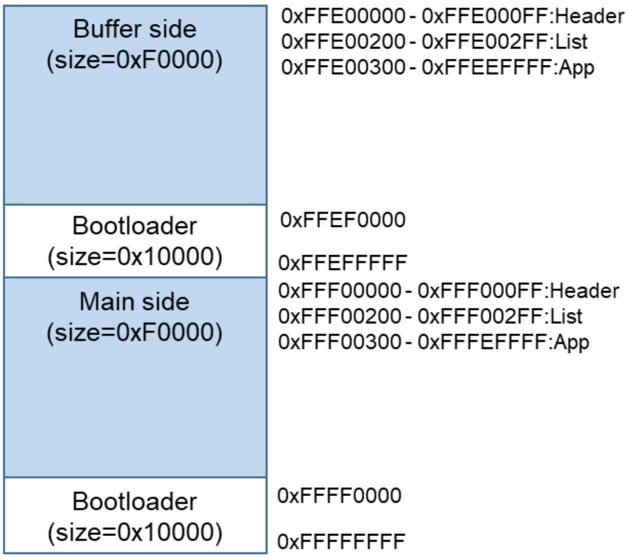


Figure 6.2 RSK-RX65N Pin Information

6.2.1.1 Memory map of dual bank method demo project

The memory map and configuration settings for the RX65N dual-bank method demo project are shown below.



boot_loader

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	RPFRAM*
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
0xFFFF0000	PResetPRG
	C_1
	C_2
	C
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFFFF80	EXCEPTVECT
0xFFFFFFFC	RESETVECT

fwup_main

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	RPFRAM*
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
0xFFF00300	PResetPRG
	C_1
	C_2
	C
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFEFF80	EXCEPTVECT
0xFFFEFFFC	RESETVECT

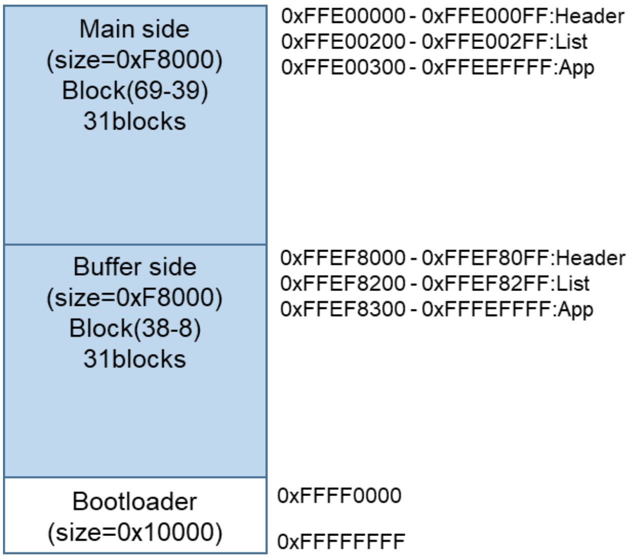
Figure 6.3 RX65N dual bank method demo project memory map

Table 6.7 RX65N dual bank method configuration settings

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	0	0
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFF00000	0xFFF00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFE00000	0xFFE00000
FWUP_CFG_AREA_SIZE	0xF0000	0xF0000
FWUP_CFG_CF_BLK_SIZE	0x8000	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	128	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x0000	0x0000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x00100000	0x00100000
FWUP_CFG_DF_BLK_SIZE	64	64
FWUP_CFG_DF_NUM_BLKS	512	512
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.1.2 Memory map of demo project for half-surface update method in linear mode

Shown below are the memory map of the RX65N linear mode half-surface update method demo project and the memory map of the configuration settings.



boot_loader

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	RPFRAM*
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
0xFFFF0000	PResetPRG
	C_1
	C_2
	C
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFFFF80	EXCEPTVECT
0xFFFFFFFC	RESETVECT

fwup_main

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	RPFRAM*
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
0xFFE00300	PResetPRG
	C_1
	C_2
	C
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFEF7F80	EXCEPTVECT
0xFFEF7FFC	RESETVECT

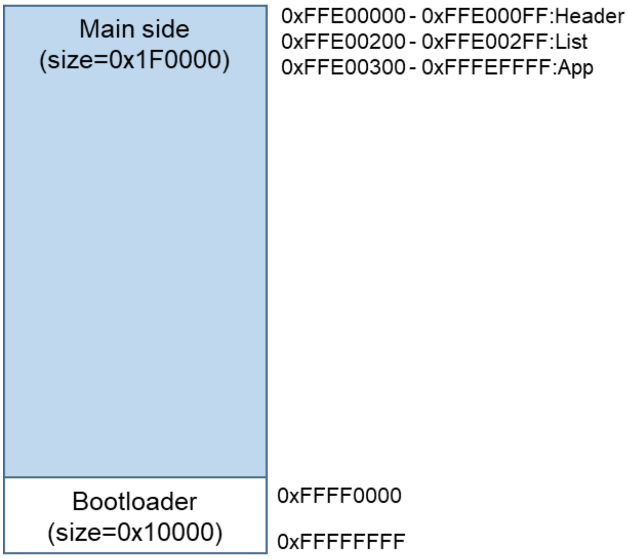
Figure 6.4 RX65N linear mode half-surface update method demo project memory map

Table 6.8 RX65N linear mode half-surface update method configuration setting

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	1	1
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFE00000	0xFFE00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFEF8000	0xFFEF8000
FWUP_CFG_AREA_SIZE	0xF8000	0xF8000
FWUP_CFG_CF_BLK_SIZE	0x8000	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	128	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x0000	0x0000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x00100000	0x00100000
FWUP_CFG_DF_BLK_SIZE	64	64
FWUP_CFG_DF_NUM_BLKS	512	512
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.1.3 Memory map of demo project for full update method in linear mode

The memory map of the RX65N linear mode full update method demo project and the memory map of the configuration settings are shown below.



boot_loader

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	RPFRAM*
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
0xFFFF0000	PResetPRG
	C_1
	C_2
	C
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFFFF80	EXCEPTVECT
0xFFFFFFFC	RESETVECT

fwup_leddemo

0x00000004	SU
	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
0xFFE00300	PResetPRG
	C_1
	C_2
	C
	C$*
	D*
	W*
	L
	P
0xFFFEFF80	EXCEPTVECT
0xFFFEFFFC	RESETVECT

Figure 6.5 RX65N linear mode full update method demo project memory map

Table 6.9 RX65N linear mode full update method configuration setting

Configuration options in r_fwup _config.h	
parameter name	**boot_loader**
FWUP_CFG_UPDATE_MODE	2
FWUP_CFG_FUNCTION_MODE	0
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFE00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFE00000
FWUP_CFG_AREA_SIZE	0x1F0000
FWUP_CFG_CF_BLK_SIZE	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x0000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096
FWUP_CFG_DF_ADDR_L	0x00100000
FWUP_CFG_DF_BLK_SIZE	64
FWUP_CFG_DF_NUM_BLKS	512
FWUP_CFG_FWUPV1_COMPATIBLE	0
FWUP_CFG_SIGNATURE_VERIFICATION	0
FWUP_CFG_PRINTF_DISABLE	0

6.2.2 Operation Confirmation Environment for RX26T

The execution environment and connection diagram are shown below.

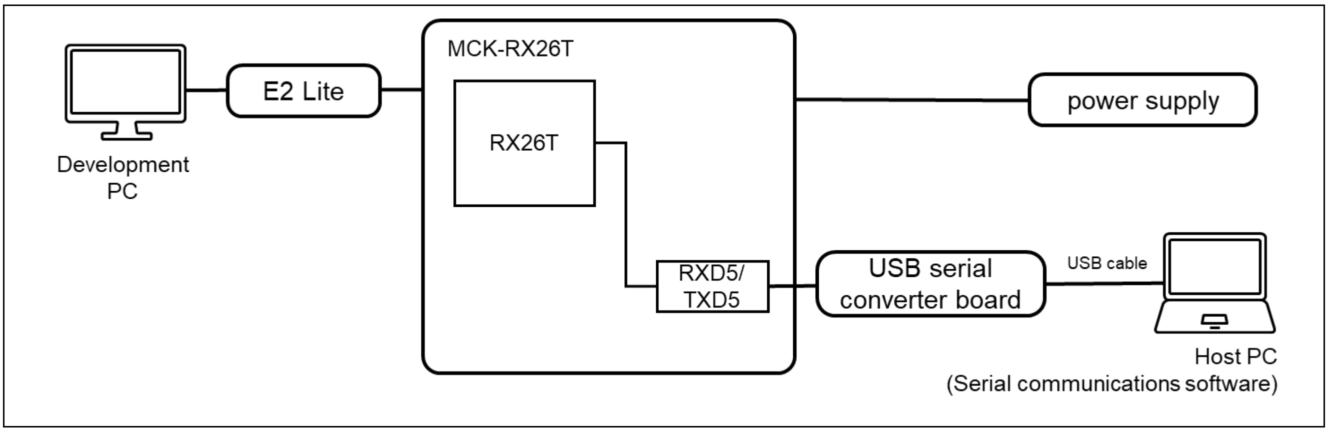


Figure 6.6 MCK-RX26T Device Connection Diagram

The pin assignment is shown in the figure below.

■ UART(Red)

PMOD2		USB-UART
2	TXD5	RX
3	RXD5	TX
4	PB0(RTS)	CTS

■ USER_SW (Green)

PMOD1		Note
1	PB3	LOW : USER_SW is ON
5	GND	Connect to PMOD1. 1

■ Reset Switch (Blue)

R92	Note
RES#	Reset SW for MCU

■ USER_LED (Yellow)

LED0	Note
P20	LED2

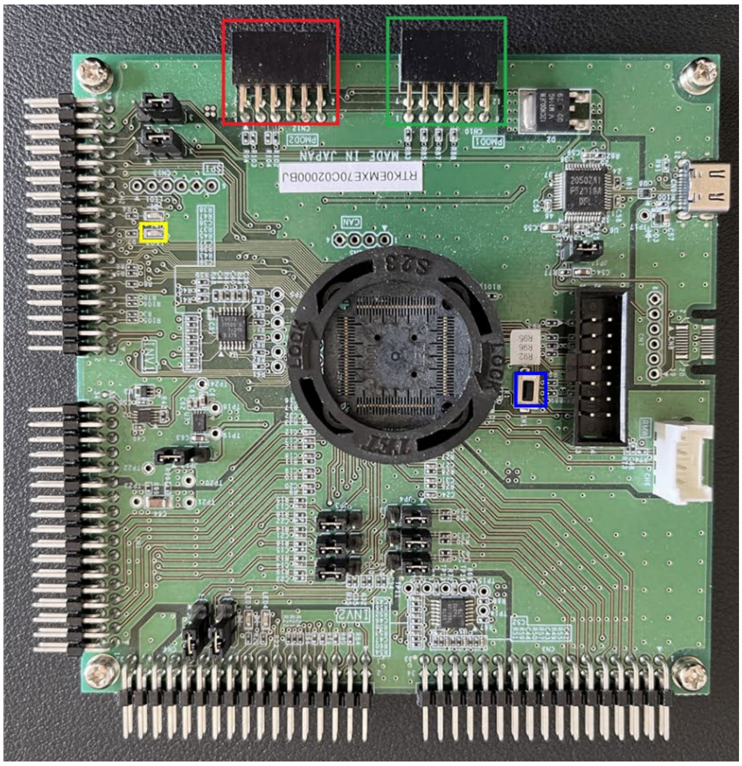
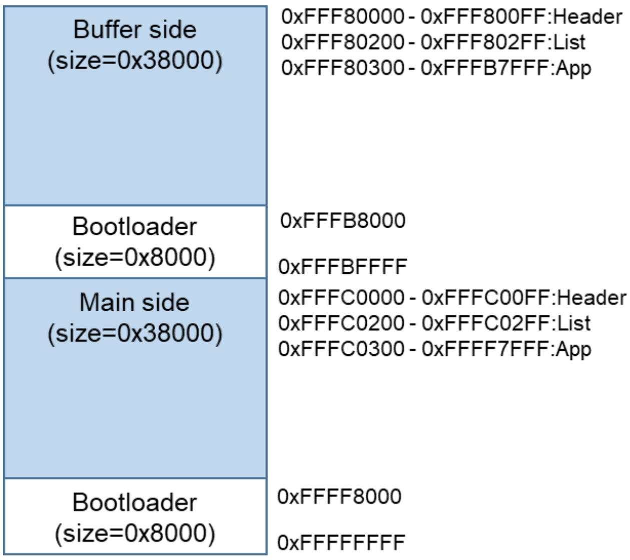


Figure 6.7 MCK-RX26T Pin Information

6.2.2.1 Memory map of dual bank method demo project

The memory map and configuration settings for the RX26T dual-bank method demo project are shown below.



boot_loader

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	RPFRAM*
0xFFFF8000	PResetPRG
	C_1
	C_2
	C
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFFFF80	EXCEPTVECT
0xFFFFFFFC	RESETVECT

fwup_main

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	RPFRAM*
0xFFFC0300	PResetPRG
	C_1
	C_2
	C
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFF7F80	EXCEPTVECT
0xFFFF7FFC	RESETVECT

Figure 6.8 RX26T dual bank method demo project memory map

Table 6.10 RX26T dual bank method configuration settings

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	0	0
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFFC0000	0xFFFC0000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFF80000	0xFFF80000
FWUP_CFG_AREA_SIZE	0x38000	0x38000
FWUP_CFG_CF_BLK_SIZE	0x4000	0x4000
FWUP_CFG_CF_W_UNIT_SIZE	128	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x0000	0x0000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x00100000	0x00100000
FWUP_CFG_DF_BLK_SIZE	64	64
FWUP_CFG_DF_NUM_BLKS	256	256
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.2.2 Memory map of demo project for half-surface update method in linear mode

Shown below are the memory map of the RX26T linear mode half-surface update method demo project and the memory map of the configuration settings.

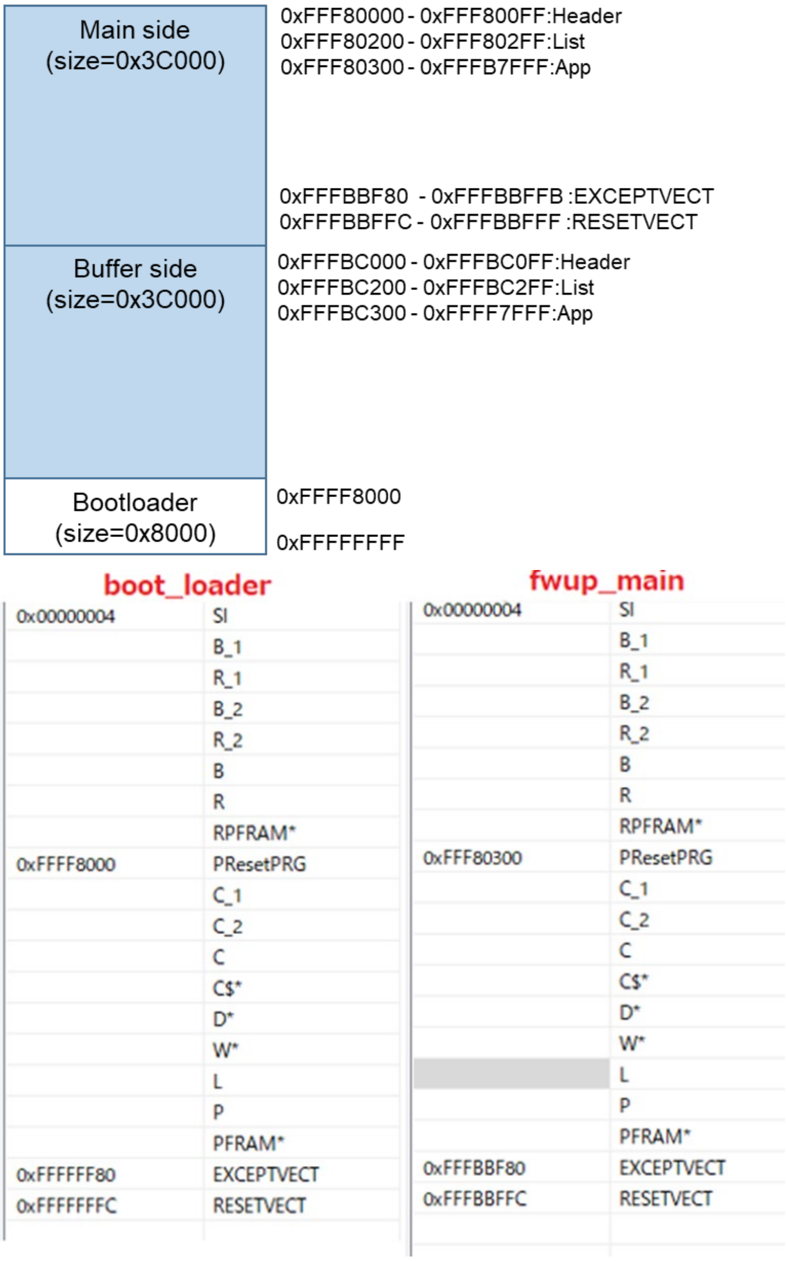


Figure 6.9 RX26T linear mode half-surface update method demo project memory map

Table 6.11 RX26T linear mode half-surface update method configuration setting

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	1	1
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFF80000	0xFFF80000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFFBC000	0xFFFBC000
FWUP_CFG_AREA_SIZE	0x3C000	0x3C000
FWUP_CFG_CF_BLK_SIZE	0x4000	0x4000
FWUP_CFG_CF_W_UNIT_SIZE	128	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x0000	0x0000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x00100000	0x00100000
FWUP_CFG_DF_BLK_SIZE	64	64
FWUP_CFG_DF_NUM_BLKS	256	256
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.2.3 Memory map of demo project for full update method in linear mode

The memory map of the RX26T linear mode full update method demo project and the memory map of the configuration settings are shown below.

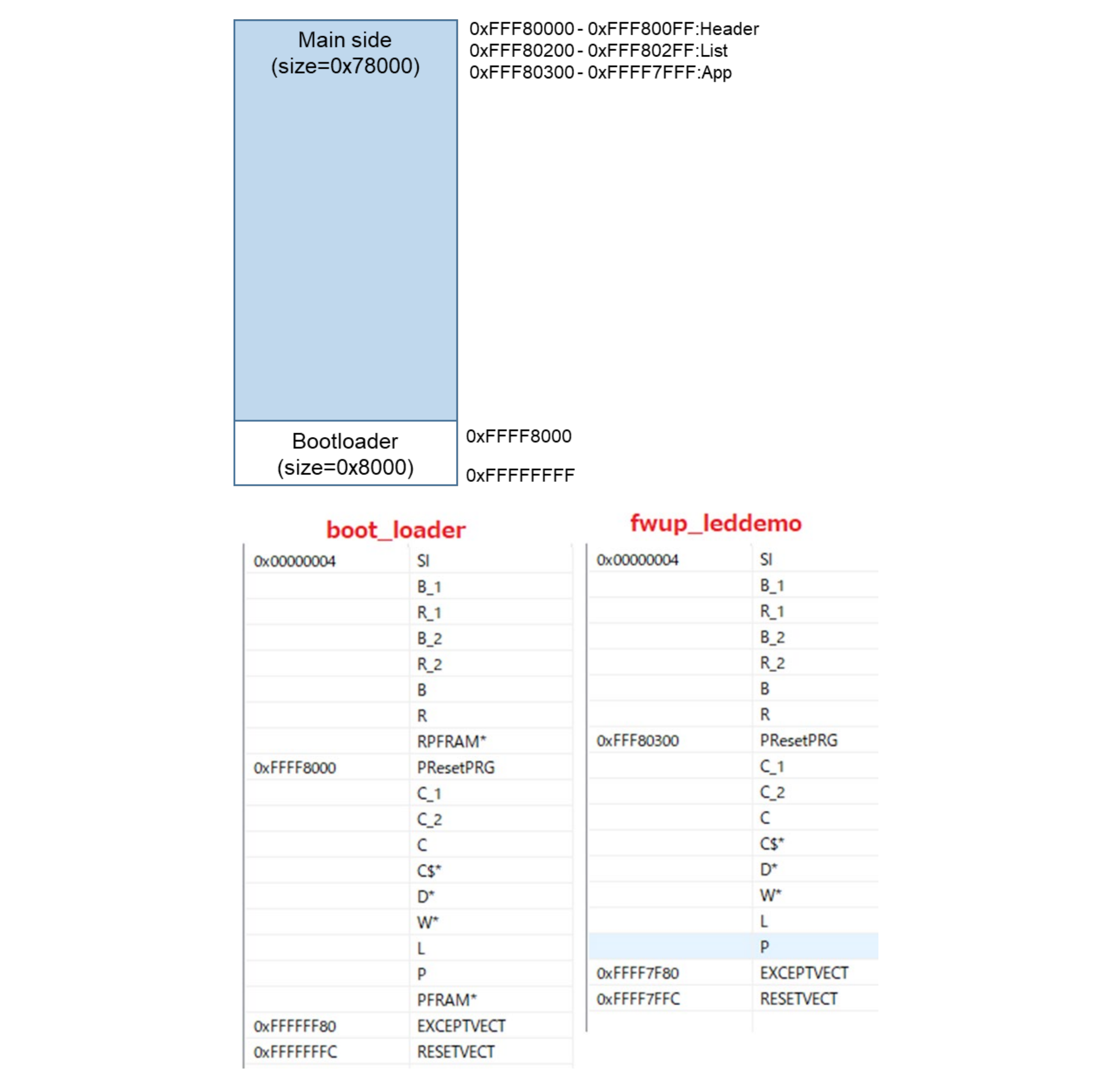


Figure 6.10 RX26T linear mode full update method demo project memory map

Table 6.12 RX26T linear mode full update method configuration setting

Configuration options in r_fwup _config.h	
parameter name	**boot_loader**
FWUP_CFG_UPDATE_MODE	2
FWUP_CFG_FUNCTION_MODE	0
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFF80000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFF80000
FWUP_CFG_AREA_SIZE	0x78000
FWUP_CFG_CF_BLK_SIZE	0x4000
FWUP_CFG_CF_W_UNIT_SIZE	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x0000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096
FWUP_CFG_DF_ADDR_L	0x00100000
FWUP_CFG_DF_BLK_SIZE	64
FWUP_CFG_DF_NUM_BLKS	256
FWUP_CFG_FWUPV1_COMPATIBLE	0
FWUP_CFG_SIGNATURE_VERIFICATION	0
FWUP_CFG_PRINTF_DISABLE	0

6.2.3 Operation Confirmation Environment for RX24T

The execution environment and connection diagram are shown below.

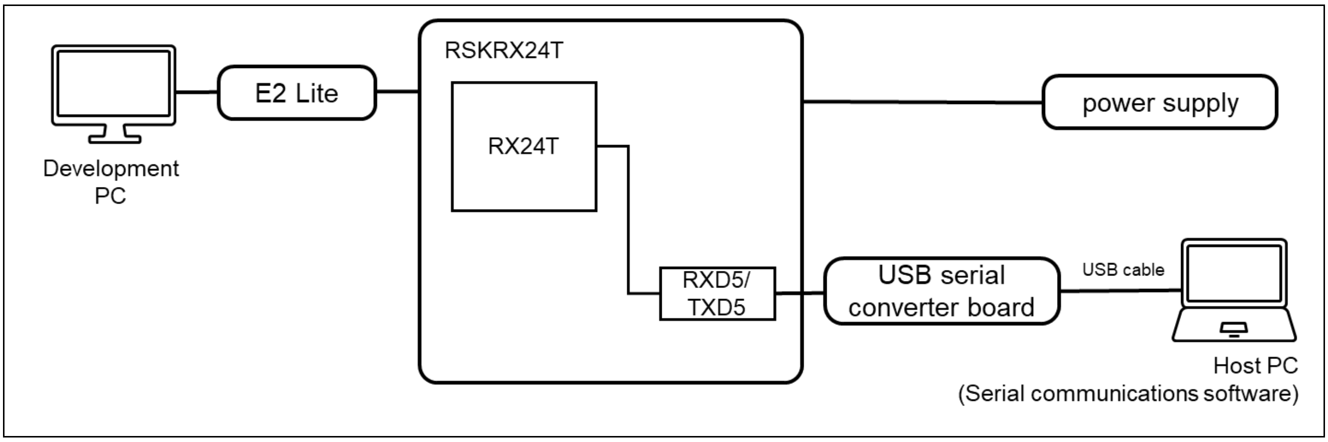


Figure 6.11 RSK-RX24T Device Connection Diagram

The pin assignment is shown in the figure below.

■ UART(Red)

PMOD1		USB-UART
2	TXD5	RX
3	RXD5	TX
4	PB7(RTS)	CTS

■ USER_SW (Green)

SW1	Note
P10	LOW : USER_SW is ON

■ Reset Switch (Blue)

RES	Note
RESn	Reset SW for MCU

■ USER_LED (Yellow)

LED0	Note
PB3	LED2

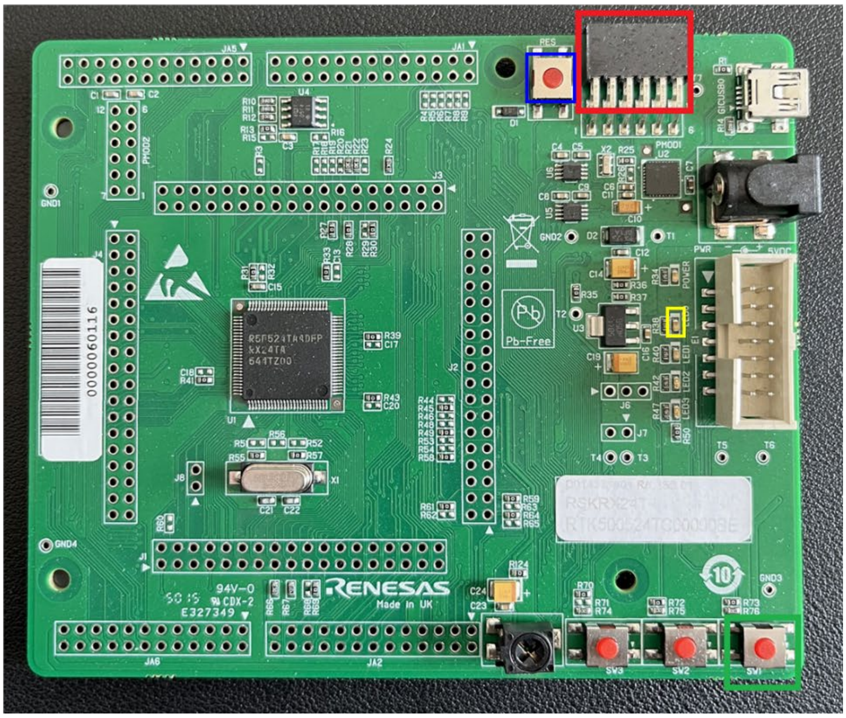
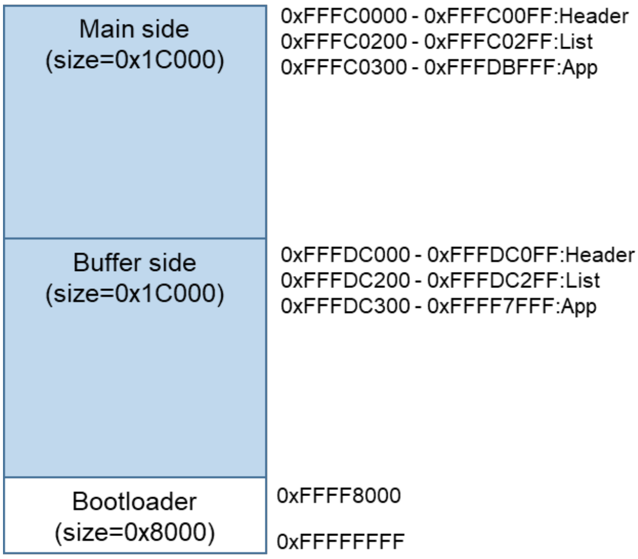


Figure 6.12 RSK-RX24T Pin Information

6.2.3.1 Memory map of demo project for half-surface update method in linear mode

Shown below are the memory map of the RX24T linear mode half-surface update method demo project and the memory map of the configuration settings.



boot_loader		fwup_main	
0x00000004	SI	0x00000004	SI
	B_1		B_1
	R_1		R_1
	B_2		B_2
	R_2		R_2
	B		B
	R		R
	RPFRAM*		RPFRAM*
0xFFFF8000	PResetPRG	0xFFFC0300	PResetPRG
	C_1		C_1
	C_2		C_2
	C		C
	C$*		C$*
	D*		D*
	W*		W*
	L		L
	P		P
	PFRAM*		PFRAM*
0xFFFFFF80	EXCEPTVECT	0xFFFDBF80	EXCEPTVECT
0xFFFFFFFC	RESETVECT	0xFFFDBFFC	RESETVECT

Figure 6.13 RX24T linear mode half-surface update method demo project memory map

Table 6.13 RX24T linear mode half-surface update method configuration setting

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	1	1
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFFC0000	0xFFFC0000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFFDC000	0xFFFDC000
FWUP_CFG_AREA_SIZE	0x1C000	0x1C000
FWUP_CFG_CF_BLK_SIZE	0x800	0x800
FWUP_CFG_CF_W_UNIT_SIZE	128	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x0000	0x0000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x100000	0x100000
FWUP_CFG_DF_BLK_SIZE	1024	1024
FWUP_CFG_DF_NUM_BLKS	8	8
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.3.2 Memory map of demo project for full update method in linear mode

The memory map of the RX24T linear mode full update method demo project and the memory map of the configuration settings are shown below.

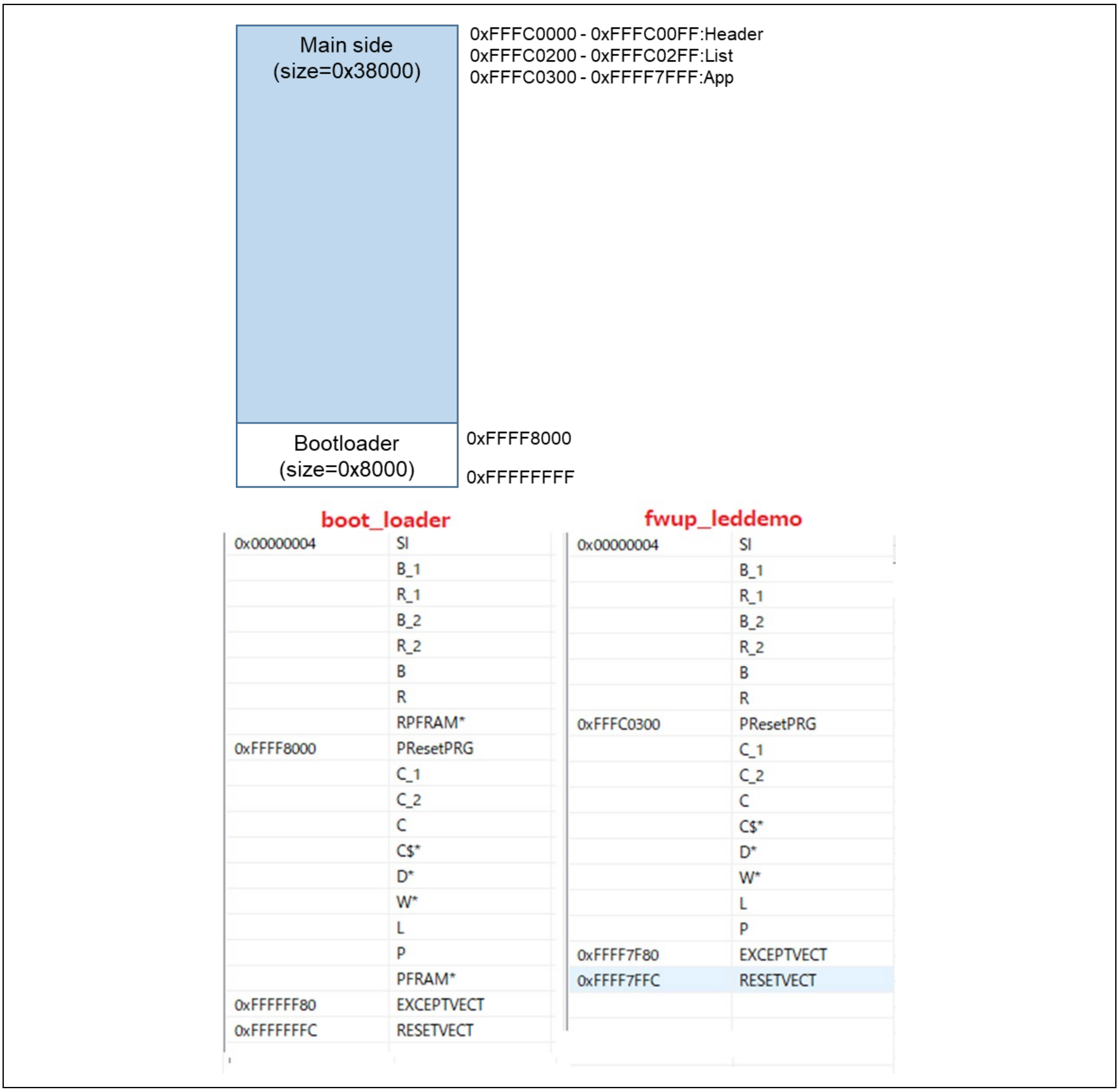


Figure 6.14 RX24T linear mode full update method demo project memory map

Table 6.14 RX24T linear mode full update method configuration setting

Configuration options in r_fwup _config.h	
parameter name	**boot_loader**
FWUP_CFG_UPDATE_MODE	2
FWUP_CFG_FUNCTION_MODE	0
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFFC0000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFFC0000
FWUP_CFG_AREA_SIZE	0x38000
FWUP_CFG_CF_BLK_SIZE	0x800
FWUP_CFG_CF_W_UNIT_SIZE	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x0000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096
FWUP_CFG_DF_ADDR_L	0x100000
FWUP_CFG_DF_BLK_SIZE	1024
FWUP_CFG_DF_NUM_BLKS	8
FWUP_CFG_FWUPV1_COMPATIBLE	0
FWUP_CFG_SIGNATURE_VERIFICATION	0
FWUP_CFG_PRINTF_DISABLE	0

6.2.4 Operation Confirmation Environment for RX72N

The execution environment and connection diagram are shown below.

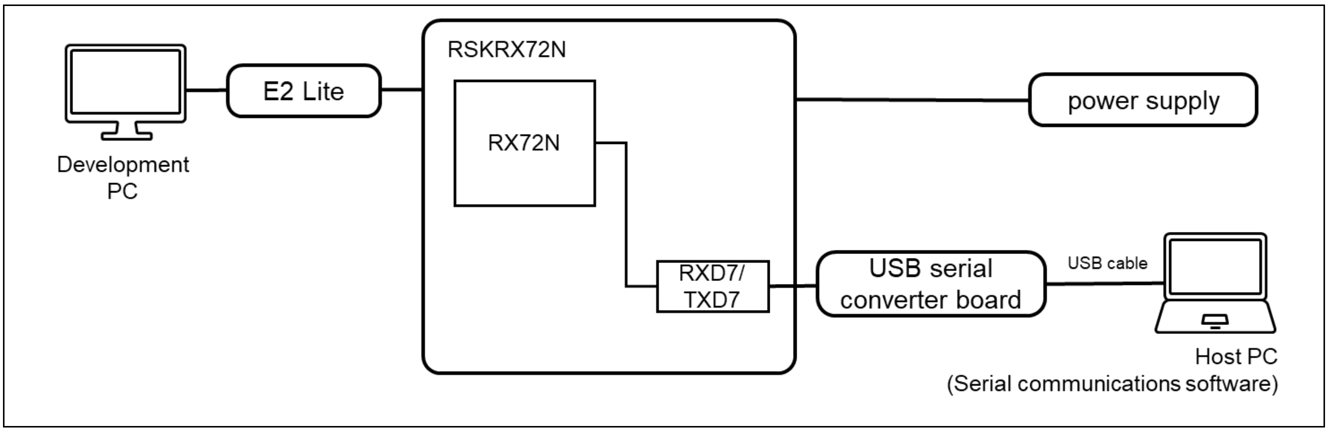


Figure 6.15 RSK-RX72N Device Connection Diagram

The pin assignment is shown in the figure below.

■ UART(Red)

PMOD1		USB-UART
2	TXD7	RX
3	RXD7	TX
4	PH0(RTS)	CTS

■ USER_SW (Green)

SW1	Note
P45	LOW : USER_SW is ON

■ Reset Switch (Blue)

RES1	Note
RES#	LOW : USER_SW is ON

■ USER_LED (Yellow)

LED0	Note
P71	LED0

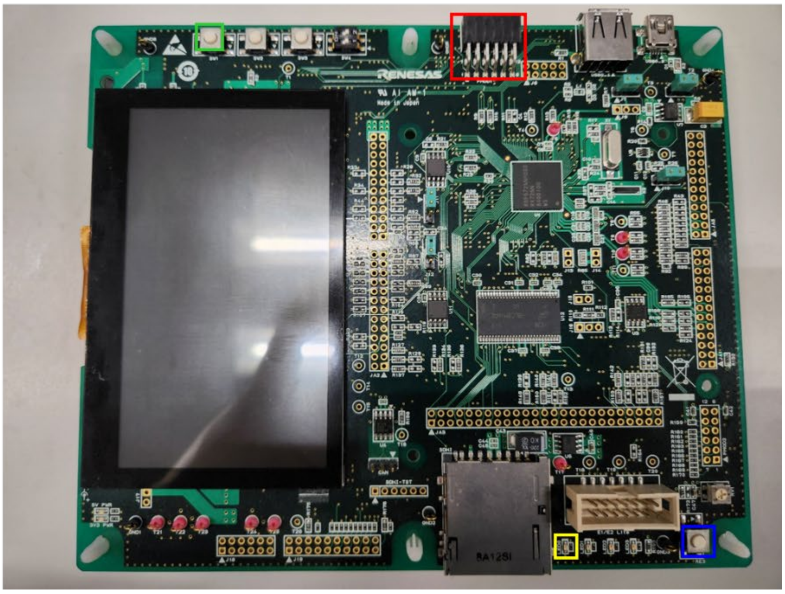
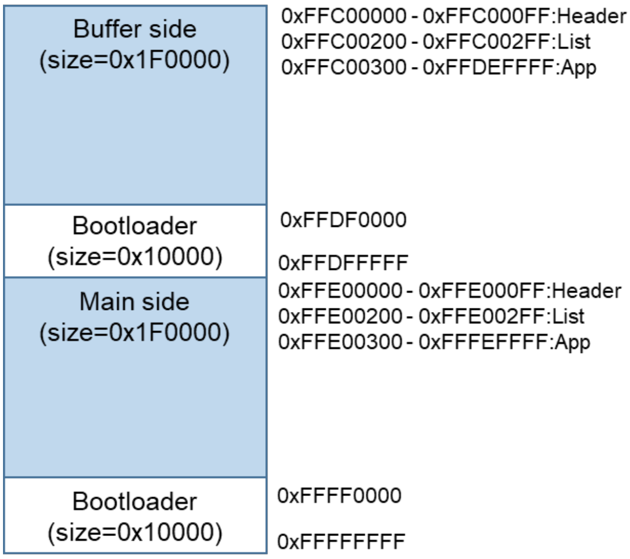


Figure 6.16 RSK-RX72N Pin Information

6.2.4.1 Memory map of dual bank method demo project

The memory map and configuration settings for the RX72N dual-bank method demo project are shown below.



boot_loader

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	B_8
	R_8
	RPFRAM*
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
	BEXRAM_8
	REXRAM_8
0xFFFF0000	PResetPRG
	C_1
	C_2
	C
	C_8
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFFFF80	EXCEPTVECT
0xFFFFFFFC	RESETVECT

fwup_main

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	B_8
	R_8
	RPFRAM*
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
	BEXRAM_8
	REXRAM_8
0xFFE00300	PResetPRG
	C_1
	C_2
	C
	C_8
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFEFF80	EXCEPTVECT
0xFFFEFFFC	RESETVECT

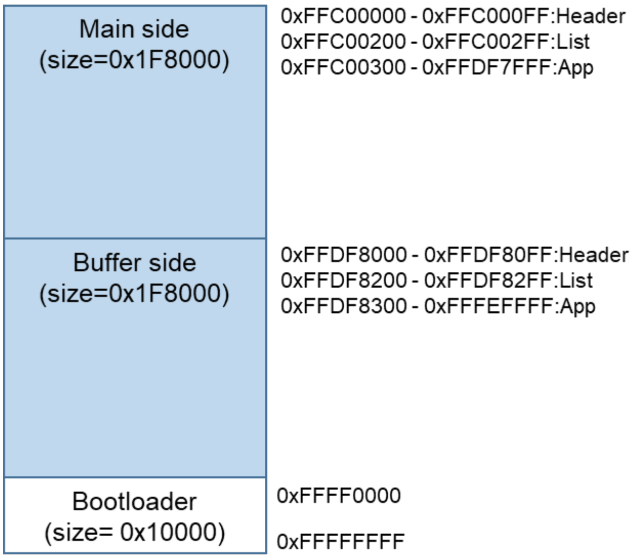
Figure 6.17 RX72N dual bank method demo project memory map

Table 6.15 RX72N dual bank method configuration settings

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	0	0
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFE00000	0xFFE00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFC00000	0xFFC00000
FWUP_CFG_AREA_SIZE	0x1F0000	0x1F0000
FWUP_CFG_CF_BLK_SIZE	0x8000	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	128	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x00000	0x00000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x00100000	0x00100000
FWUP_CFG_DF_BLK_SIZE	64	64
FWUP_CFG_DF_NUM_BLKS	512	512
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.4.2 Memory map of demo project for half-surface update method in linear mode

Shown below are the memory map of the RX72N linear mode half-surface update method demo project and the memory map of the configuration settings.



boot_loader

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	B_8
	R_8
	RPFRAM*
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
	BEXRAM_8
	REXRAM_8
0xFFFF0000	PResetPRG
	C_1
	C_2
	C
	C_8
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFFFF80	EXCEPTVECT
0xFFFFFFFC	RESETVECT

fwup_main

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	B_8
	R_8
	RPFRAM*
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
	BEXRAM_8
	REXRAM_8
0xFFC00300	PResetPRG
	C_1
	C_2
	C
	C_8
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFDF7F80	EXCEPTVECT
0xFFDF7FFC	RESETVECT

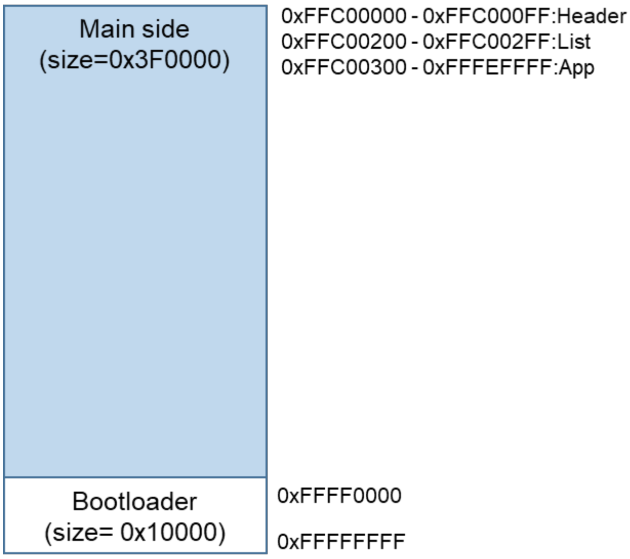
Figure 6.18 RX72N linear mode half-surface update method demo project memory map

Table 6.16 RX72N linear mode half-surface update method configuration setting

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	1	1
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFC00000	0xFFC00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFDF8000	0xFFDF8000
FWUP_CFG_AREA_SIZE	0x1F8000	0x1F8000
FWUP_CFG_CF_BLK_SIZE	0x8000	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	128	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x00000	0x00000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x00100000	0x00100000
FWUP_CFG_DF_BLK_SIZE	64	64
FWUP_CFG_DF_NUM_BLKS	512	512
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.4.3 Memory map of demo project for full update method in linear mode

The memory map of the RX72N linear mode full update method demo project and the memory map of the configuration settings are shown below.



boot_loader

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	B_8
	R_8
	RPFRAM*
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
	BEXRAM_8
	REXRAM_8
0xFFFF0000	PResetPRG
	C_1
	C_2
	C
	C_8
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFFFF80	EXCEPTVECT
0xFFFFFFFC	RESETVECT

fwup_leddemo

0x00000004	SU
	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	B_8
	R_8
0x00800000	BEXRAM_1
	REXRAM_1
	BEXRAM_2
	REXRAM_2
	BEXRAM
	REXRAM
	BEXRAM_8
	REXRAM_8
0xFFC00300	PResetPRG
	C_1
	C_2
	C
	C_8
	C$*
	D*
	W*
	L
	P
0xFFFEFF80	EXCEPTVECT
0xFFFEFFFC	RESETVECT

Figure 6.19 RX72N linear mode full update method demo project memory map

Table 6.17 RX72N linear mode full update method configuration setting

Configuration options in r_fwup _config.h	
parameter name	**boot_loader**
FWUP_CFG_UPDATE_MODE	2
FWUP_CFG_FUNCTION_MODE	0
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFC00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFC00000
FWUP_CFG_AREA_SIZE	0x3F0000
FWUP_CFG_CF_BLK_SIZE	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x00000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096
FWUP_CFG_DF_ADDR_L	0x00100000
FWUP_CFG_DF_BLK_SIZE	64
FWUP_CFG_DF_NUM_BLKS	512
FWUP_CFG_FWUPV1_COMPATIBLE	0
FWUP_CFG_SIGNATURE_VERIFICATION	0
FWUP_CFG_PRINTF_DISABLE	0

6.2.5 Operation Confirmation Environment for RX671

The execution environment and connection diagram are shown below.

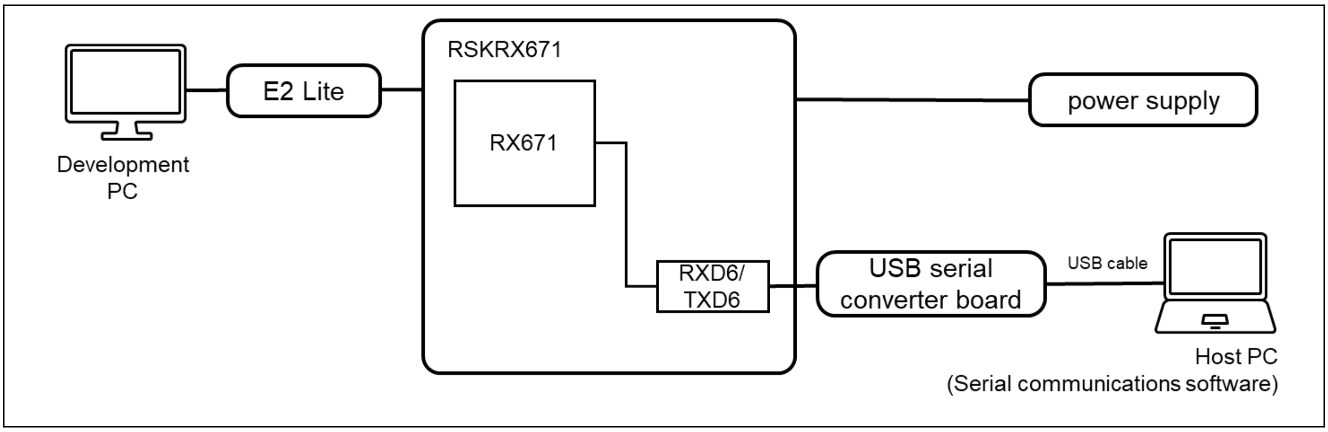


Figure 6.20 RSK-RX671 Device Connection Diagram

The pin assignment is shown in the figure below.

■ UART(Red)

PMOD1		USB-UART
2	TXD6	RX
3	RXD6	TX
4	P02(RTS)	CTS

■ USER_SW (Green)

SW1	Note
P91	LOW : USER_SW is ON

■ Reset Switch (Blue)

RES1	Note
RES#	LOW : USER_SW is ON

■ USER_LED (Yellow)

LED0	Note
P17	LED0

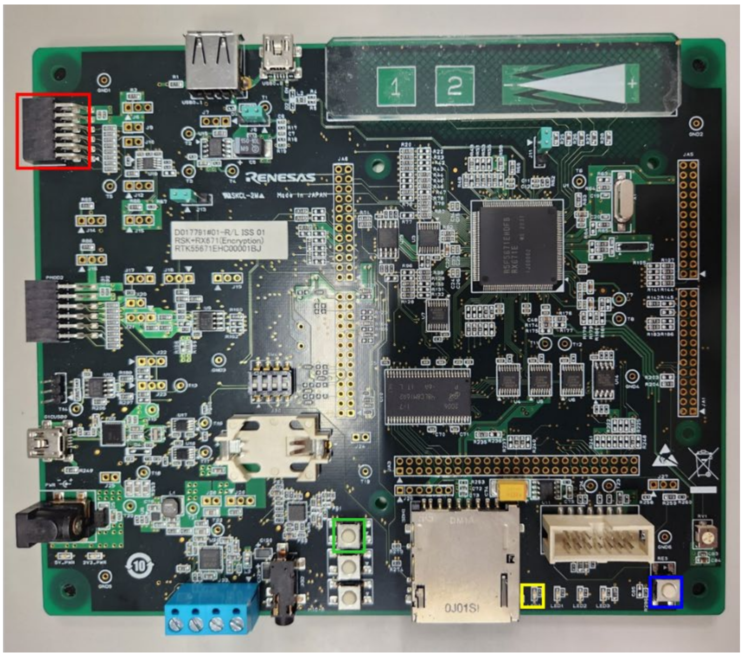


Figure 6.21 RSK-RX671 Pin Information

6.2.5.1 Memory map of dual bank method demo project

The memory map and configuration settings for the RX671 dual-bank method demo project are shown below.

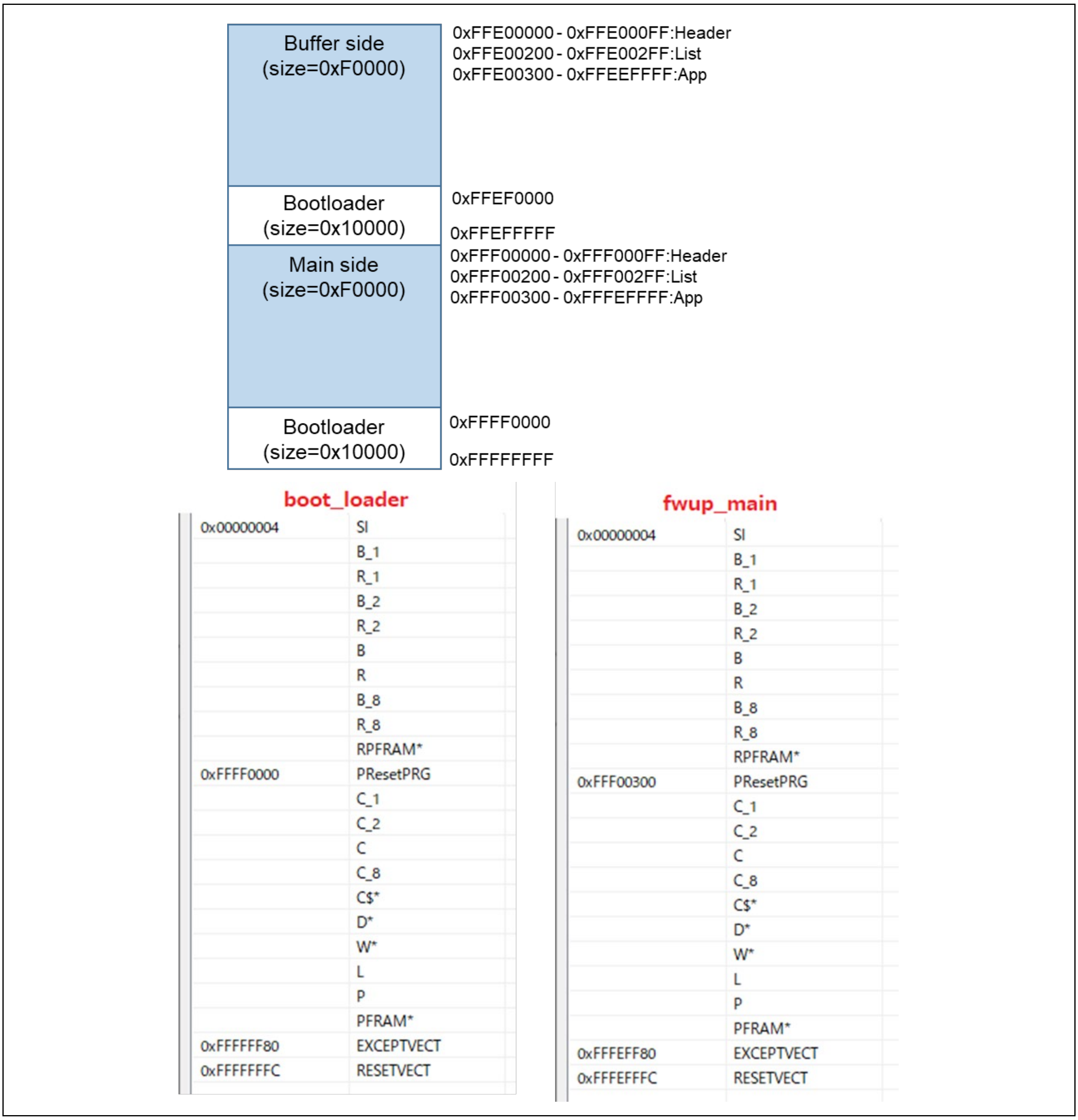


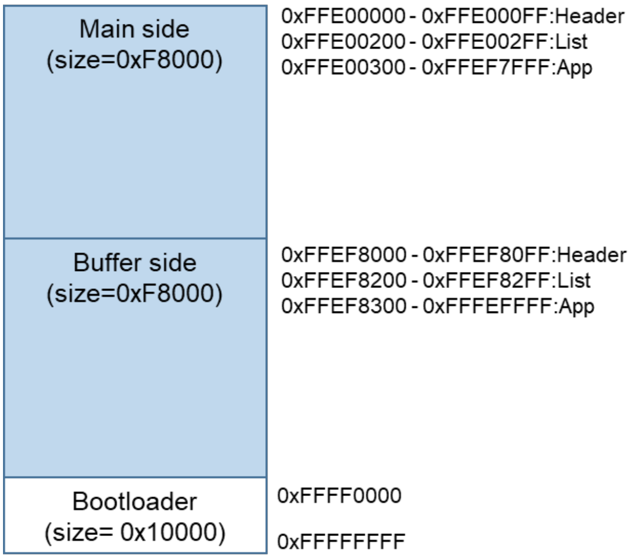
Figure 6.22 RX671 dual bank method demo project memory map

Table 6.18 RX671 dual bank method configuration settings

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	0	0
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFF00000	0xFFF00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFE00000	0xFFE00000
FWUP_CFG_AREA_SIZE	0xF0000	0xF0000
FWUP_CFG_CF_BLK_SIZE	0x8000	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	128	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x00000	0x00000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x00100000	0x00100000
FWUP_CFG_DF_BLK_SIZE	64	64
FWUP_CFG_DF_NUM_BLKS	128	128
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.5.2 Memory map of demo project for half-surface update method in linear mode

Shown below are the memory map of the RX671 linear mode half-surface update method demo project and the memory map of the configuration settings.



boot_loader

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	B_8
	R_8
	RPFRAM*
0xFFFF0000	PResetPRG
	C_1
	C_2
	C
	C_8
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFFFF80	EXCEPTVECT
0xFFFFFFFC	RESETVECT

fwup_main

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	B_8
	R_8
	RPFRAM*
0xFFE00300	PResetPRG
	C_1
	C_2
	C
	C_8
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFEF7F80	EXCEPTVECT
0xFFEF7FFC	RESETVECT

Figure 6.23 RX671 linear mode half-surface update method demo project memory map

Table 6.19 RX671 linear mode half-surface update method configuration setting

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	1	1
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFE00000	0xFFE00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFEF8000	0xFFEF8000
FWUP_CFG_AREA_SIZE	0xF8000	0xF8000
FWUP_CFG_CF_BLK_SIZE	0x8000	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	128	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x00000	0x00000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x00100000	0x00100000
FWUP_CFG_DF_BLK_SIZE	64	64
FWUP_CFG_DF_NUM_BLKS	128	128
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.5.3 Memory map of demo project for full update method in linear mode

The memory map of the RX671 linear mode full update method demo project and the memory map of the configuration settings are shown below.

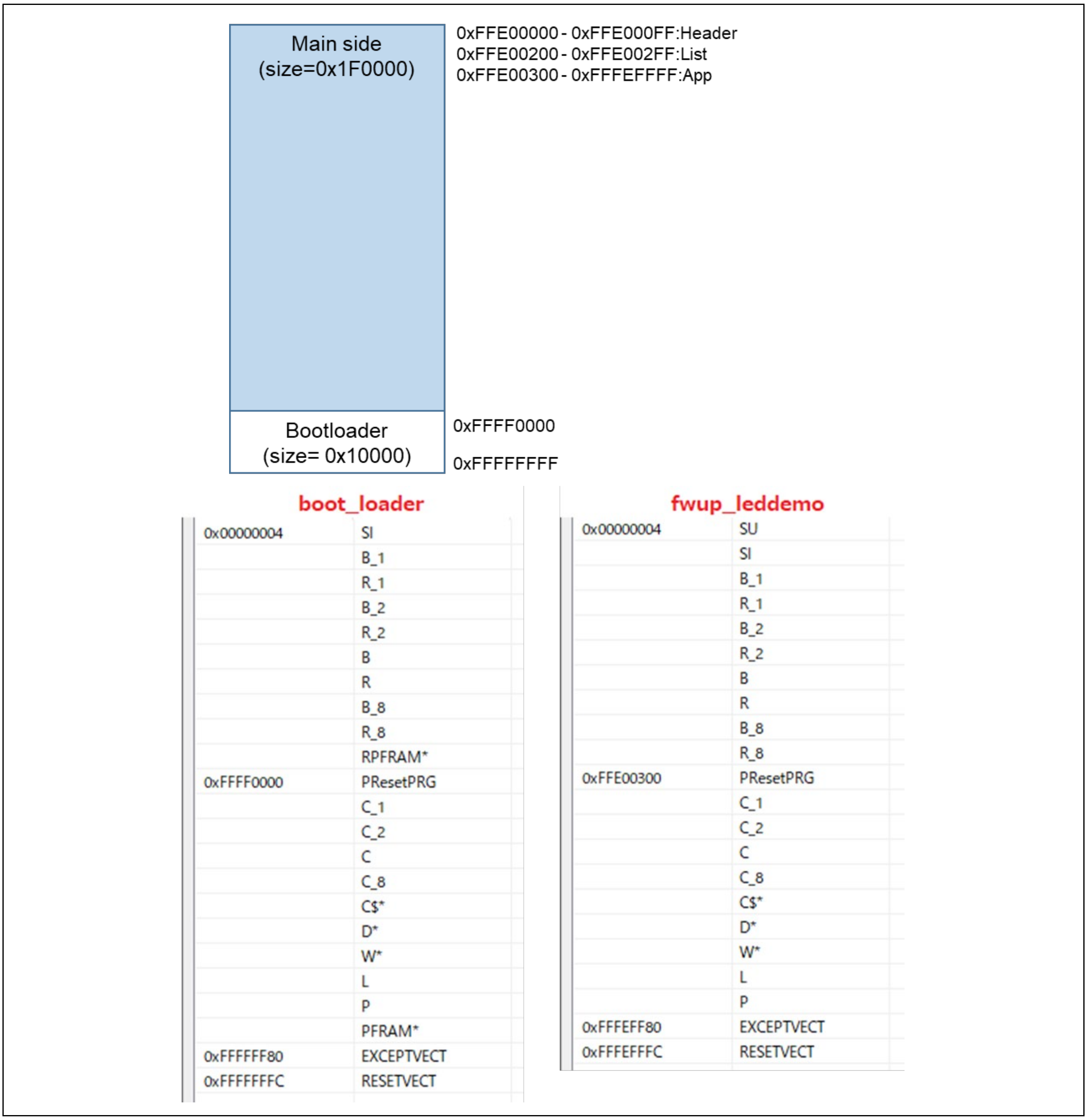


Figure 6.24 RX671 linear mode full update method demo project memory map

Table 6.20 RX671 linear mode full update method configuration setting

Configuration options in r_fwup _config.h	
parameter name	**boot_loader**
FWUP_CFG_UPDATE_MODE	2
FWUP_CFG_FUNCTION_MODE	0
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFE00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFE00000
FWUP_CFG_AREA_SIZE	0x1F0000
FWUP_CFG_CF_BLK_SIZE	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	128
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x00000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096
FWUP_CFG_DF_ADDR_L	0x00100000
FWUP_CFG_DF_BLK_SIZE	64
FWUP_CFG_DF_NUM_BLKS	128
FWUP_CFG_FWUPV1_COMPATIBLE	0
FWUP_CFG_SIGNATURE_VERIFICATION	0
FWUP_CFG_PRINTF_DISABLE	0

6.2.6 Operation Confirmation Environment for RX660

The execution environment and connection diagram are shown below.

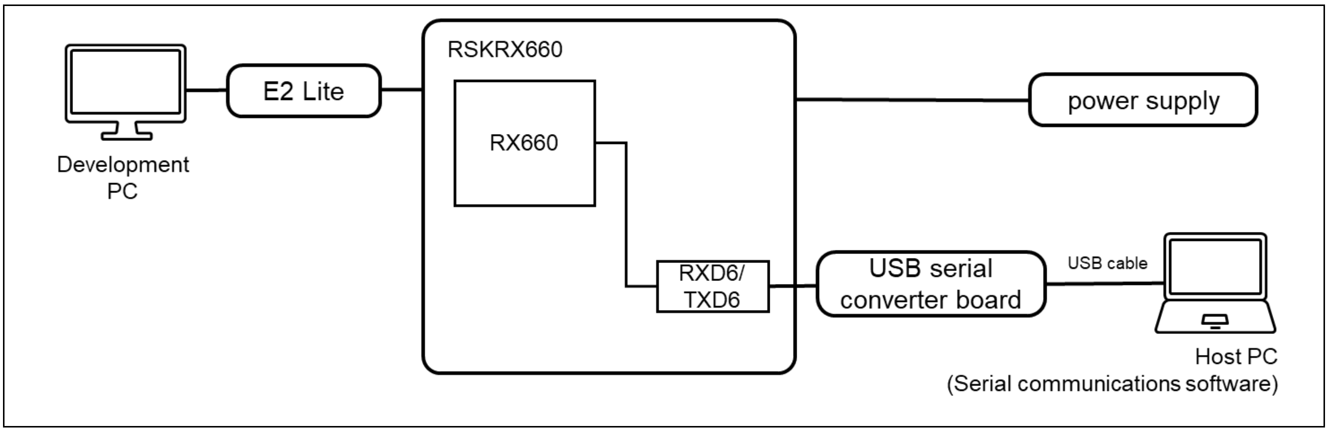


Figure 6.25 RSK-RX660 Device Connection Diagram

The pin assignment is shown in the figure below.

■ UART(Red)

PMOD1		USB-UART
2	TXD6	RX
3	RXD6	TX
4	P02(RTS)	CTS

■ USER_SW (Green)

SW1	Note
P91	LOW : USER_SW is ON

■ Reset Switch (Blue)

RES1	Note
RES#	LOW : USER_SW is ON

■ USER_LED (Yellow)

LED0	Note
P17	LED0

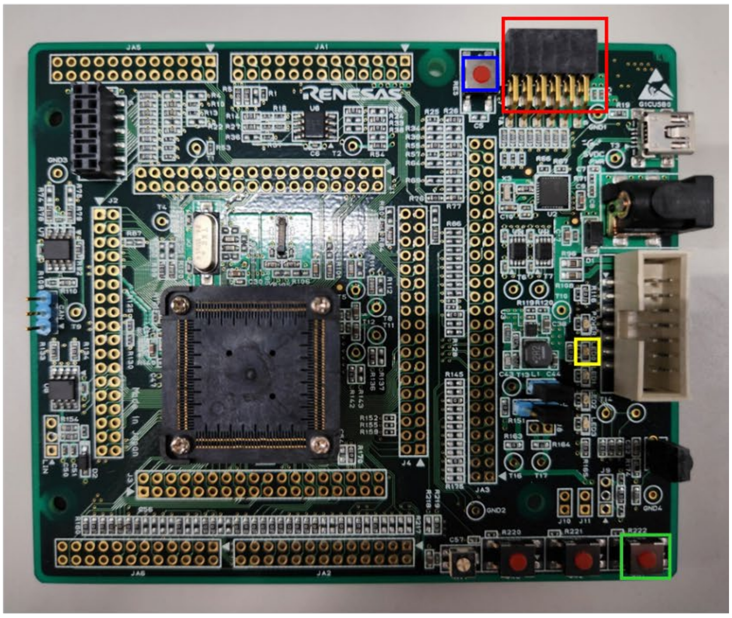


Figure 6.26 RSK-RX660 Pin Information

6.2.6.1 Memory map of demo project for half-surface update method in linear mode

Shown below are the memory map of the RX660 linear mode half-surface update method demo project and the memory map of the configuration settings.

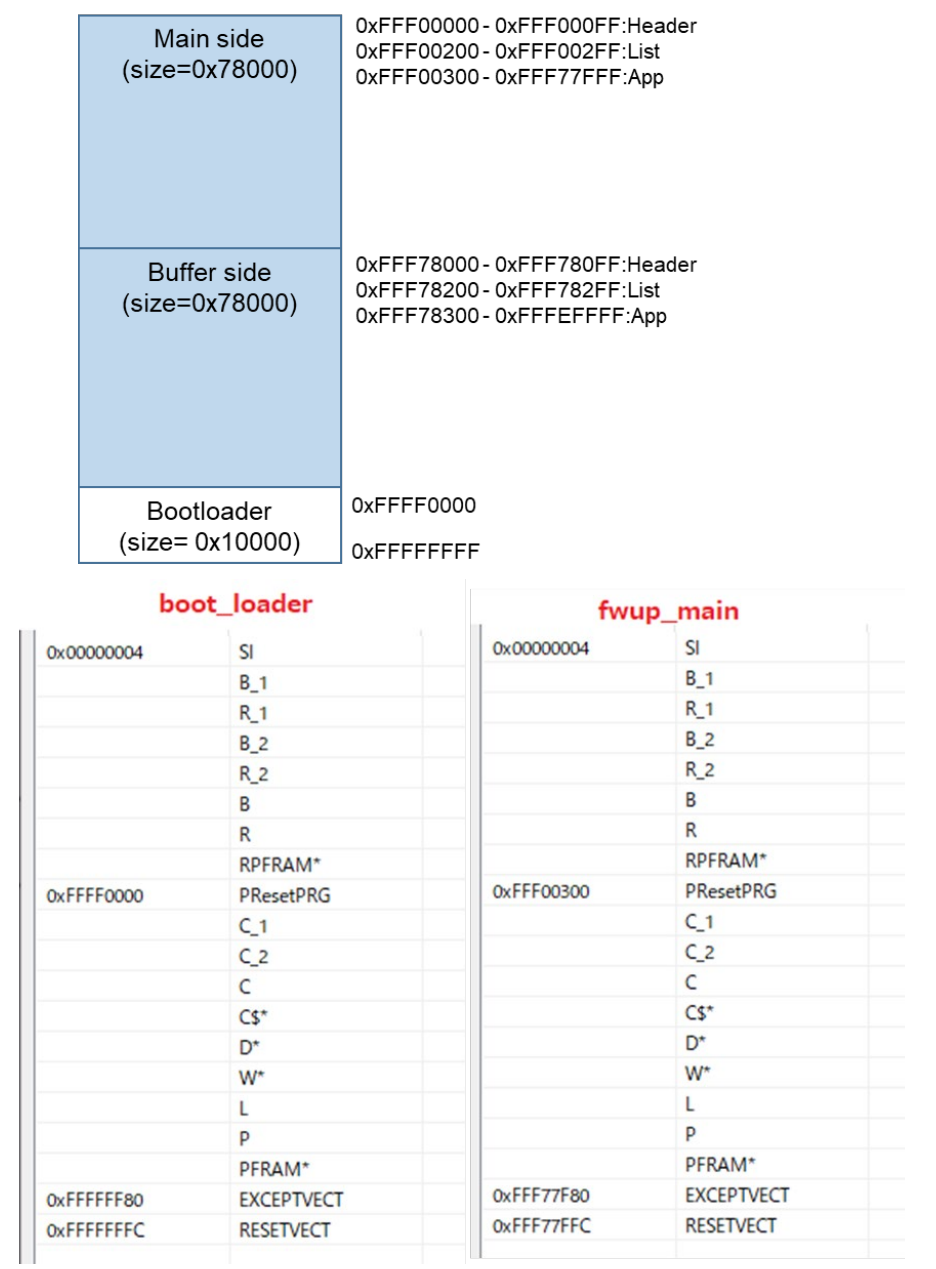


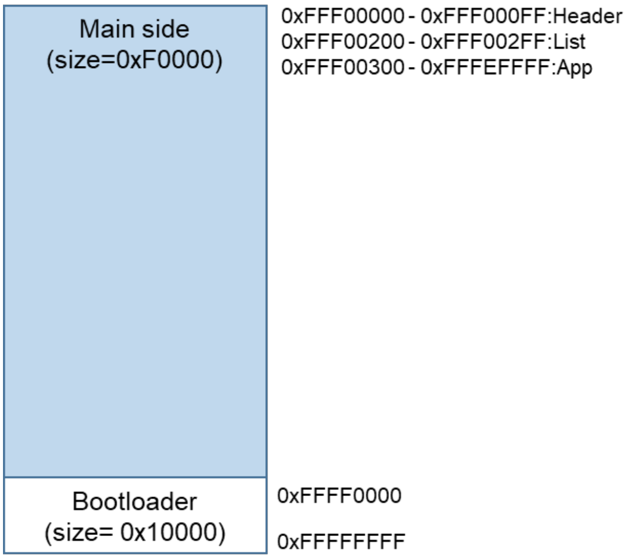
Figure 6.27 RX660 linear mode half-surface update method demo project memory map

Table 6.21 RX660 linear mode half-surface update method configuration setting

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	1	1
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFF00000	0xFFF00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFF78000	0xFFF78000
FWUP_CFG_AREA_SIZE	0x78000	0x78000
FWUP_CFG_CF_BLK_SIZE	0x8000	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	256	256
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x00000	0x00000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x00100000	0x00100000
FWUP_CFG_DF_BLK_SIZE	64	64
FWUP_CFG_DF_NUM_BLKS	512	512
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.6.2 Memory map of demo project for full update method in linear mode

The memory map of the RX660 linear mode full update method demo project and the memory map of the configuration settings are shown below.



boot_loader

0x00000004	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
	RPFRAM*
0xFFFF0000	PResetPRG
	C_1
	C_2
	C
	C$*
	D*
	W*
	L
	P
	PFRAM*
0xFFFFFF80	EXCEPTVECT
0xFFFFFFFC	RESETVECT

fwup_leddemo

0x00000004	SU
	SI
	B_1
	R_1
	B_2
	R_2
	B
	R
0xFFF00300	PResetPRG
	C_1
	C_2
	C
	C$*
	D*
	W*
	L
	P
0xFFFEFF80	EXCEPTVECT
0xFFFEFFFC	RESETVECT

Figure 6.28 RX660 linear mode full update method demo project memory map

Table 6.22 RX660 linear mode full update method configuration setting

Configuration options in r_fwup _config.h	
parameter name	boot_loader
FWUP_CFG_UPDATE_MODE	2
FWUP_CFG_FUNCTION_MODE	0
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFF00000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFF00000
FWUP_CFG_AREA_SIZE	0xF0000
FWUP_CFG_CF_BLK_SIZE	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	256
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x00000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096
FWUP_CFG_DF_ADDR_L	0x00100000
FWUP_CFG_DF_BLK_SIZE	64
FWUP_CFG_DF_NUM_BLKS	512
FWUP_CFG_FWUPV1_COMPATIBLE	0
FWUP_CFG_SIGNATURE_VERIFICATION	0
FWUP_CFG_PRINTF_DISABLE	0

6.2.7 Operation Confirmation Environment for RX66T

The execution environment and connection diagram are shown below.

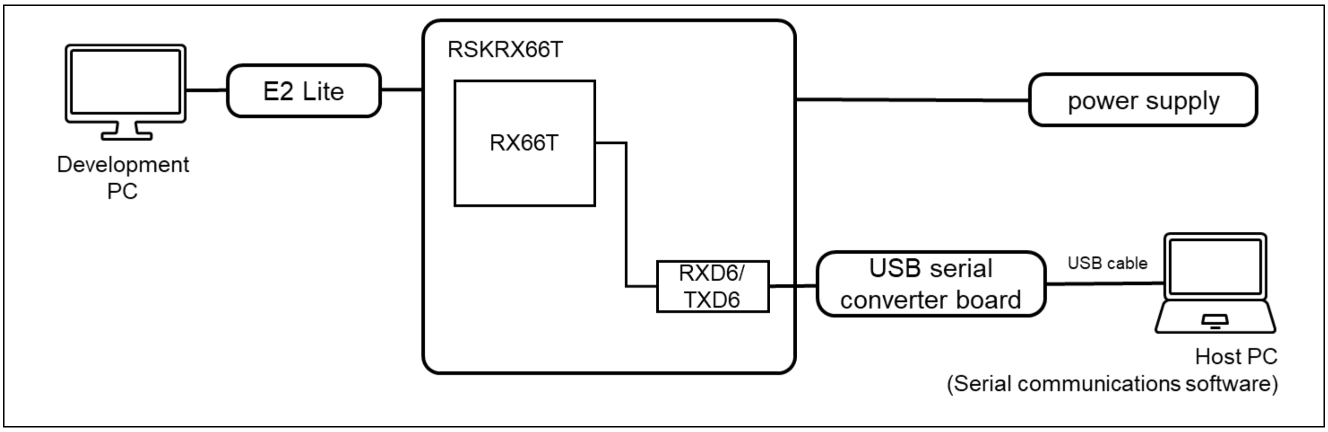


Figure 6.29 RSK-RX66T Device Connection Diagram

The pin assignment is shown in the figure below.

■ UART(Red)

PMOD1		USB-UART
2	TXD6	RX
3	RXD6	TX
4	PA4(RTS)	CTS

■ USER_SW (Green)

SW1	Note
P10	LOW : USER_SW is ON

■ Reset Switch (Blue)

RES1	Note
RES#	LOW : USER_SW is ON

■ USER_LED (Yellow)

LED0	Note
P95	LED0

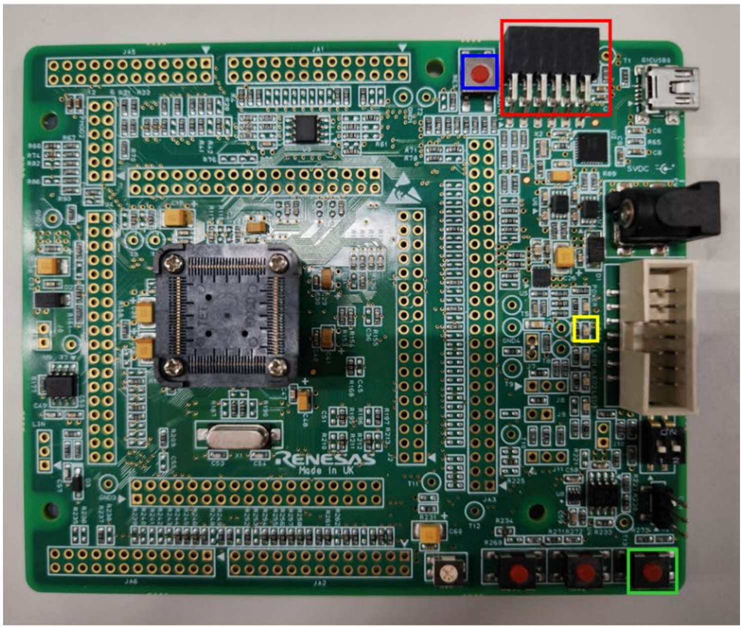


Figure 6.30 RSK-RX66T Pin Information

6.2.7.1 Memory map of demo project for half-surface update method in linear mode

Shown below are the memory map of the RX66T linear mode half-surface update method demo project and the memory map of the configuration settings.

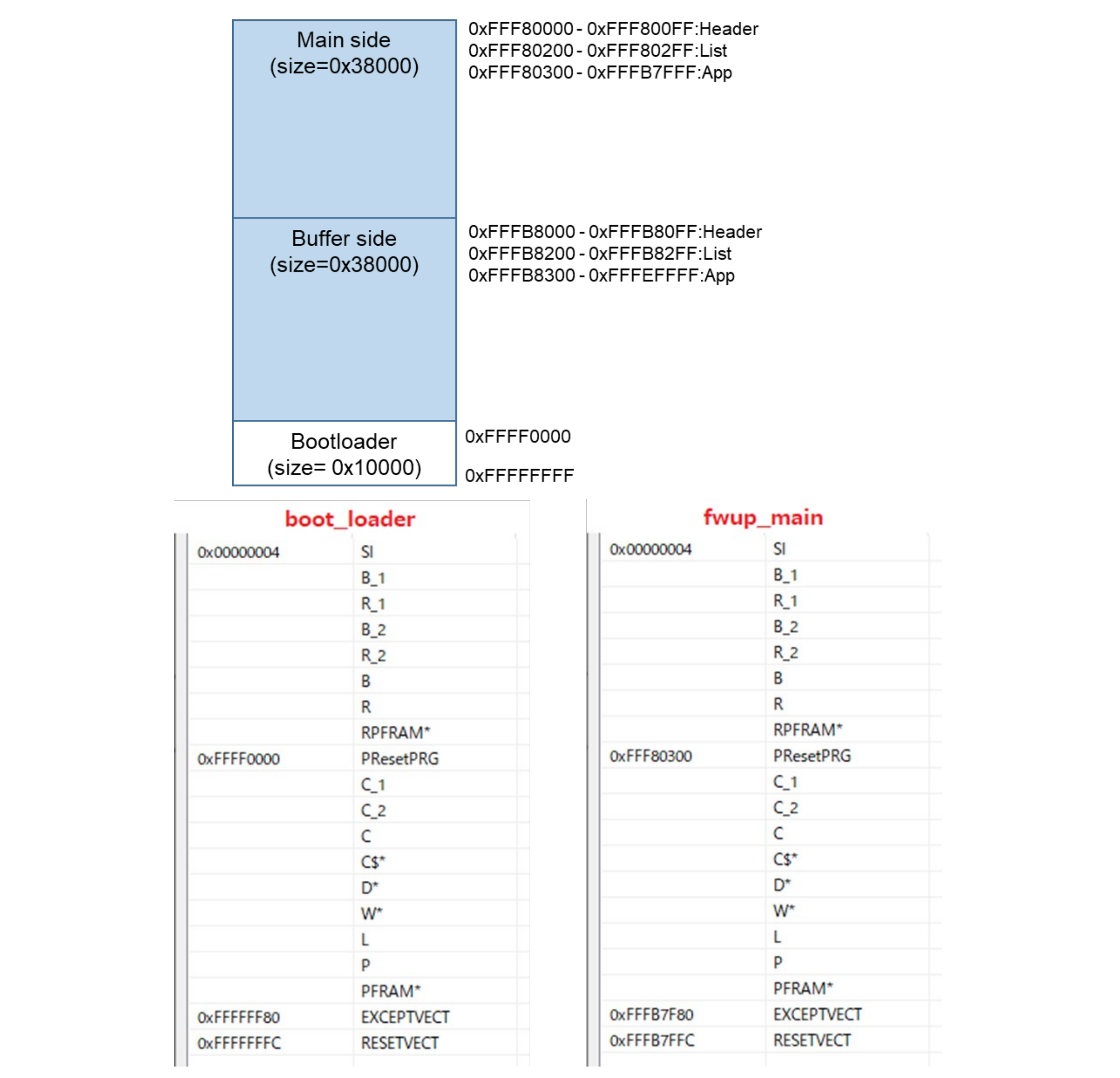


Figure 6.31 RX66T linear mode half-surface update method demo project memory map

Table 6.23 RX66T linear mode half-surface update method configuration setting

Configuration options in r_fwup _config.h		
parameter name	**boot_loader**	**fwup_main**
FWUP_CFG_UPDATE_MODE	1	1
FWUP_CFG_FUNCTION_MODE	0	1
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFF80000	0xFFF80000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFFB8000	0xFFFB8000
FWUP_CFG_AREA_SIZE	0x38000	0x38000
FWUP_CFG_CF_BLK_SIZE	0x8000	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	256	256
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x00000	0x00000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096	4096
FWUP_CFG_DF_ADDR_L	0x00100000	0x00100000
FWUP_CFG_DF_BLK_SIZE	64	64
FWUP_CFG_DF_NUM_BLKS	512	512
FWUP_CFG_FWUPV1_COMPATIBLE	0	0
FWUP_CFG_SIGNATURE_VERIFICATION	0	0
FWUP_CFG_PRINTF_DISABLE	0	0

6.2.7.2 Memory map of demo project for full update method in linear mode

The memory map of the RX66T linear mode full update method demo project and the memory map of the configuration settings are shown below.

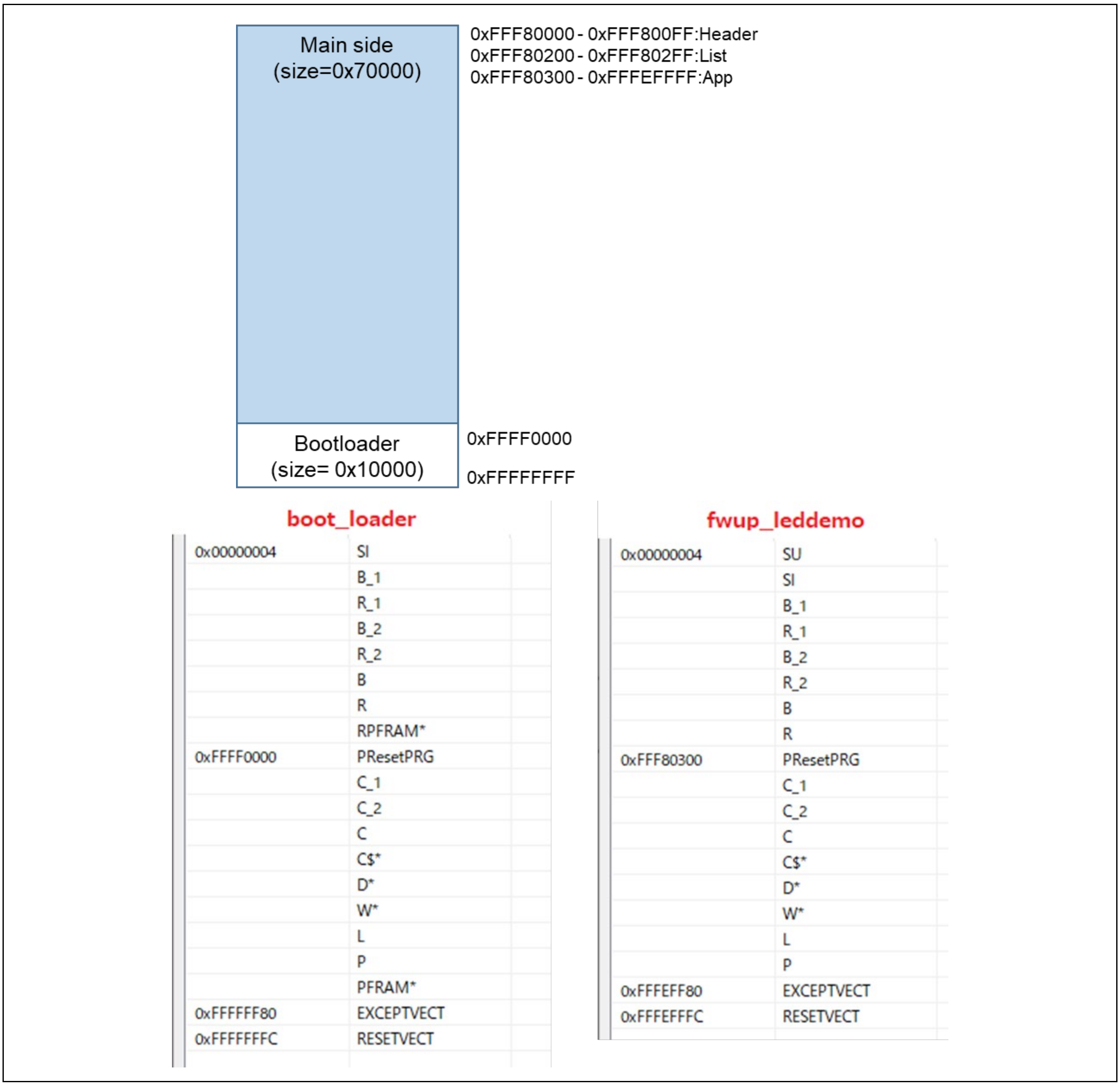


Figure 6.32 RX66T linear mode full update method demo project memory map

Table 6.24 RX66T linear mode full update method configuration setting

Configuration options in r_fwup _config.h	
parameter name	**boot_loader**
FWUP_CFG_UPDATE_MODE	2
FWUP_CFG_FUNCTION_MODE	0
FWUP_CFG_MAIN_AREA_ADDR_L	0xFFF80000
FWUP_CFG_BUF_AREA_ADDR_L	0xFFF80000
FWUP_CFG_AREA_SIZE	0x70000
FWUP_CFG_CF_BLK_SIZE	0x8000
FWUP_CFG_CF_W_UNIT_SIZE	256
FWUP_CFG_EXT_BUF_AREA_ADDR_L (unused)	0x0000
FWUP_CFG_EXT_BUF_AREA_BLK_SIZE (unused)	4096
FWUP_CFG_DF_ADDR_L	0x00100000
FWUP_CFG_DF_BLK_SIZE	64
FWUP_CFG_DF_NUM_BLKS	512
FWUP_CFG_FWUPV1_COMPATIBLE	0
FWUP_CFG_SIGNATURE_VERIFICATION	0
FWUP_CFG_PRINTF_DISABLE	0

6.3 Open source license information used in the demo project

The demo project for this product uses the open source TinyCrypt. If you use TinyCrypto for your cryptographic library, you must comply with the terms of use set forth in TinyCrypt's license terms.

Check out the TinyCrypt license terms below.

URL：https://github.com/intel/tinycrypt

license：https://github.com/intel/tinycrypt/blob/master/LICENSE

7. Notes

7.1 Notes on Transition from Bootloader to Application.

When transitioning from the sample bootloader program to the application, the settings of the bootloader's peripheral functions will be taken over by the application.

For the peripheral functions used in the sample bootloader (Table 7.1), the API functions of each FIT module are closed at the end of the bootloader. Other settings are default values when the smart configurator is used.

If the customer modifies the bootloader sample program for use, the settings of the peripheral functions set in the bootloader will be inherited by the application side. Therefore, it is recommended to initialize the settings of the peripheral functions before moving from the bootloader to the application, or to share the settings of the peripheral functions with the application.

When creating an application, please take the implementation of the bootloader into consideration.

Table 7.1 Notes on peripheral functions used in the bootloader

Peripheral Functions	FIT Modeule	Settings and Notes on the Boot Loader
Board Functions	r_bsp	These are the default values when the BSP FIT module is embedded in the Smart Configurator. The settings are not changed in the bootloader. Please note that the PMR and PFS registers are also set to match the board.
Functions of Flash Memory	r_flash_rx	The Flash FIT API performs Close for peripheral functions related to flash memory and transitions to the application.
Serial Communication Functions	r_sci_rx	For peripheral functions related to serial communication, Close is performed by the SCI FIT API and the transition is made to the application. For the SCI channels used in the bootloader, refer to the device connection diagram for each product in 6.2 Operating Environment for Demo Project.
Option Setting Memory	-	For the option setting memory, set the same value in the bootloader and the application program.
Other Functions	-	As for the settings of other functions, these are the default values when using the Smart Configurator. The PSW's interrupt enable flag is set to interrupt disabled to transition to the application.

Revision History

Rev.	Date	Description	
		Page	Summary
2.00	Jul. 20, 2023	—	First edition issued
2.01	Nov. 17, 2023	1	Added RX66N,RX66T,RX660,RX671,RX72M,RX72N Group to Target Devices
		8	Added device to Supported Update Methods
		14-16	Added device to folder structure
		21	Added FWUP_CFG_CF_W_UNIT_SIZE and FWUP_CFG_FWUPV1_COMPATIBLE to configuration settings
		23-24	Added device in ROM/RAM/Stack
		28	Added parameter to R_FWUP_EraseArea function
		28	Added description to R_FWUP_GetImageSize function
		28	Added description to R_FWUP_GetImageSize function
		29	Added parameter to R_FWUP_WriteImageProgram function
		29	Added return value to R_FWUP_WriteImage function
		30	Added return value to R_FWUP_VerifyImage function
		64,65	Added board used for operation check environment
		66-68	added device to related FIT module version
		88-111	Added device to Operation check environment
		113	Added note

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

   A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.
2. Processing at power-on

   The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.
3. Input of signal during power-off state

   Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.
4. Handling of unused pins

   Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.
5. Clock signals

   After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.
6. Voltage application waveform at input pin

   Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).
7. Prohibition of access to reserved addresses

   Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.
8. Differences between products

   Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

Corporate Headquarters

TOYOSU FORESIA, 3-2-24 Toyosu,
Koto-ku, Tokyo 135-0061, Japan
www.renesas.com

Contact information

For further information on a product, technology, the most up-to-date version of a document, or your nearest sales office, please visit: www.renesas.com/contact/.