[Notes]

CS+ Code Generator for RL78 (CS+ for CC),

CS+ Code Generator for RL78 (CS+ for CA,CX),

e^2 studio Code Generator Plug-in,

AP4 Coding Assistance Tool for RL78

Outline

When using the products in the title, note the following point.

1. Writing to port-related registers for unused pins

1. Writing to Port-Related Registers for Unused Pins

1.1 Applicable Products

➢ V2.05.00 and later versions of CS+ Code Generator for RL78 (CS+ for CC V3.00)

➢ V2.05.00 and later versions of CS+ Code Generator for RL78 (CS+ for CA,CX V3.00)

➢ V2.0.2 and later versions of the Code Generator plug-in (e^2 studio V3.0.1.9)

➢ V1.04.00 and later versions of the AP4 coding assistance tool for RL78
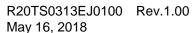
1.2 Applicable MCUs

➢ RL78 family: RL78/I1D group

1.3 Details

When using the applicable products to generate code for the port function, the following unnecessary register settings are made for the unused pins in a port when the mode (input or output) of each pin in the port is set. Because of this, the unused pins in the port output unintended data.
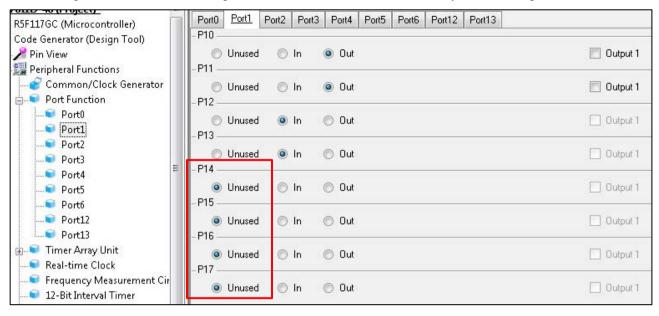
➢ Unnecessary register settings

- Port Mode Register (PMxx): Output mode (0)

- Port Mode Control Register (PMCxx): Digital I/O (0)

This note does not apply to the pins that are used for the peripheral functions other than the port function.

➢ Example: When configuring Port 1

Although "Unused" is selected for pins P14 to P17 (within the red frame), they are set as outputs.

1.4 Workaround

When you are generating code for the port function, if you have already selected "Unused" for the unused pins, change the setting to "In (input)" or "Out (output)".

The settings of the pins that are not used for the port function but used for other peripheral functions can remain the same ("Unused").

➢ Example: Setting the pins used for peripheral functions other than the port function

You cannot change the setting from "Unused" to "In" or "Out" for the pins that are not used for the port function but used for other peripheral functions.

For example, if pins P14 to P17 are used as an analog input port for the A/D converter, their setting can remain the same ("Unused").

1.5   Schedule for Fixing the Problem

This problem will be fixed in a later version. A revised version will be available in July 2018.

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	May 16, 2018	-	First edition issued

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