

[Released on the Web]

R20TS0303EJ0100

Rev.1.00

Parameter File for the PG-FP5 Flash Memory Programmer

Apr. 16, 2018

Outline

We will revise a parameter file for the PG-FP5 flash memory programmer.

This parameter file is a text file that contains information on the target controller required to use the PG-FP5 for programming. For an overview of the PG-FP5, see the following URL.

<https://www.renesas.com/pg-fp5>

1. Parameter File to be Revised

1.1 Addition of Supported MCUs

- Parameter file PR5-RH850P1X for the RH850/P1x series will be revised from V1.05 to V1.06.

Added MCUs: R7F701396A (RH850/P1H-C group)
R7F701397A (RH850/P1M-C group)

2. Obtaining the File

Download the required parameter file from the following URL.

The file will be available from April 20.

https://www.renesas.com/pg_fp5_download

For details about how to install the file, see Chapter 3 of PG-FP5 User's Manual: Common, at the following URL.

<https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ut2922>

PG-FP5 V2.15 Flash Memory Programmer User's Manual: Common

3. Required Firmware, FPGA Data, and Programming GUI

To confirm that you have the firmware, FPGA data, and programming GUI versions required to use the parameter file, see the release note for the parameter file at the following URL.

https://www.renesas.com/pg_fp5_documentation

Parameter Files for PG-FP5 Release Note

This document will be available from April 20.

4. Remarks

The PG-FP5 is to be discontinued, with final orders being accepted until May 2018.

Addition of supported MCUs for the PG-FP5 will be made available until July 2018.

Refer to the following tool news for details about the PG-FP5 and its successor, the PG-FP6.

<https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ts0289>

[Notification]

Acceptance for Final Orders for PG-FP5 Flash Memory Programmer, Introducing PG-FP6 as Successor

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Apr. 16, 2018	-	First edition issued

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