

[New Release]

R20TS0884EJ0100

RH850 Model-Based Environment

Rev.1.00

Oct. 16, 2022

Embedded Target for RH850 Virtual Platform V1.00.00

Outline

RH850 Model-Based Environment Embedded Target for RH850 Virtual Platform V1.00.00 has been released.

Product web page: www.renesas.com/software-tool/rh850-model-based-environment-embedded-target-rh850-virtual-platform

1. Product Overview

Embedded Target for RH850 Virtual Platform (ET-VPF) is a development environment that generates peripheral code for target devices from Simulink® models and enables cooperative verification as Virtual Hardware In the Loop Simulation (vHILS) in a virtual environment with Simulink.

- Peripheral code is generated from Simulink models, along with the algorithm code, making it easy to check the operation and evaluate the performance of the application including peripheral functions for device selection and prototyping, even if you are not familiar with device specifications.
- The use of a virtual environment enables early verification, even before the WS of the device is completed, and verification can be performed without the need for actual devices.

2. Product Features

- Peripheral code is generated by adding blocks corresponding to the peripheral functions of the device to the model. The peripheral functions support Port, ADC, UART, CAN, and PWM (added sequentially).
- It automatically builds a vHILS verification environment using a virtual environment and enables verification of the cooperation between Simulink and the virtual environment.
- A comparison of vHILS and the Model In the Loop Simulation (MILS) execution results enables back-to-back testing as recommended by ISO 26262.

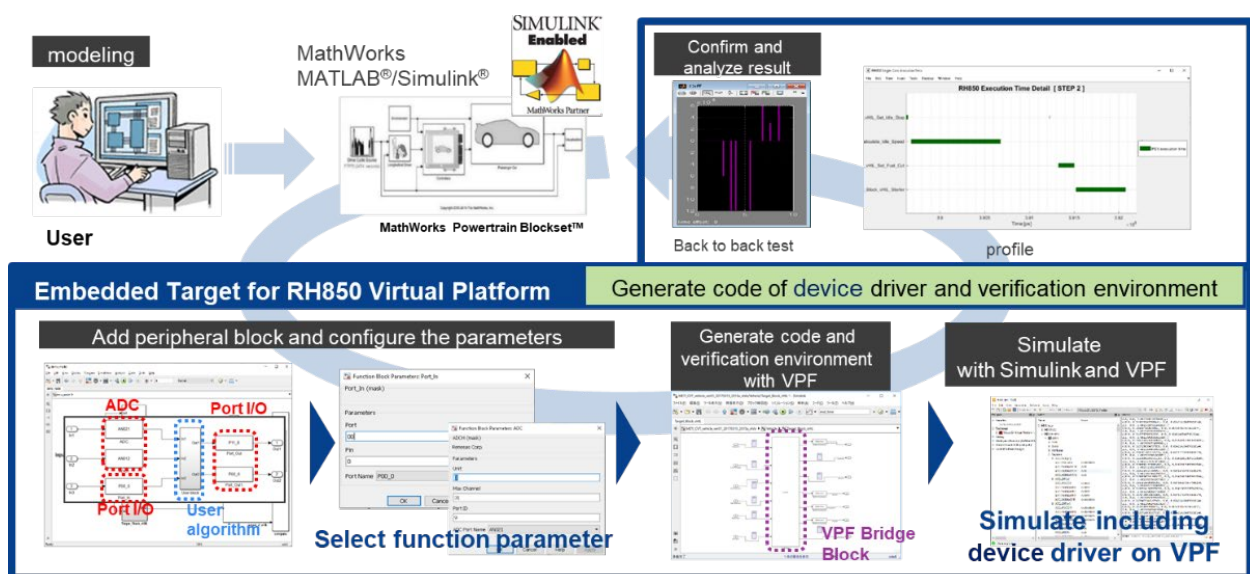


Figure 1: Product overview

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Oct.16.22	-	First edition issued

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