[Notes]

e² studio Smart Configurator Plug-in,

Smart Configurator for RX

Outline

When using the products in the title, note the following point.

1. When using the PLL frequency synthesizer of the clock

1. When Using the PLL Frequency Synthesizer of the Clock

1.1 Applicable Products

➤ e² studio V5.2.0 (Smart Configurator Plug-in V1.0.0) or later

➤ Smart Configurator for RX V1.0.0 or later

1.2 Applicable Devices

➤ RX family:
RX64M, RX651, RX65N, RX66T, RX71M, and RX72T groups

1.3 Details

When PLL frequency synthesizer uses main clock as input, the input frequency range for PLL frequency synthesizer of the applicable products is incorrect and the input frequency can be set lower than 8MHz.
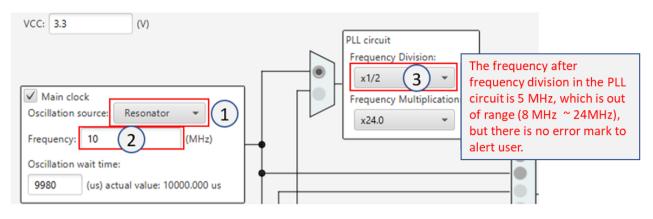
(a) External oscillator is used as the oscillation source of main clock. (See Figure 1.1)

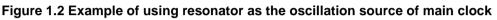(b) Resonator is used as the oscillation source of main clock. (See Figure 1.2)

Figure 1.1 Example of using external oscillator as the oscillation source of main clock

Figure 1.2 Example of using resonator as the oscillation source of main clock

1.4　Workaround

Set the input frequency for PLL frequency synthesizer [Note] within the correct range (8 MHz ~ 24 MHz).

Note: Main clock frequency after frequency division in the PLL circuit.

1.5　Schedule for Fixing the Problem

This problem will be fixed in the Smart Configurator for RX V2.5.0 / e² studio V7.8.0 (Scheduled to be released in March 2020.)

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	Feb.01.20	-	First edition issued

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