

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU	Document No.	TN-RL*-A0092A/E	Rev.	1.00
Title	Correction for Incorrect Description Notice RL78/G11 Descriptions in the User's Manual: Hardware Rev. 2.20 Changed		Information Category	Technical Notification	
Applicable Product	RL78/G11 Group	Lot No.	Reference Document	RL78/G11 User's Manual: Hardware Rev. 2.20 R01UH0637EJ0220 (Jan. 2020)	
		All lots			

This document describes misstatements found in the RL78/G11 User's Manual: Hardware Rev. 2.20 (R01UH0637EJ0220).

Corrections

Applicable Item	Applicable Page	Contents
35.3.2 Supply current characteristics	Page 1007	Incorrect descriptions revised

Document Improvement

The above corrections will be made for the next revision of the User's Manual: Hardware.

Corrections in the User's Manual: Hardware

No.	Corrections and Applicable Items			Pages in this document for corrections
	Document No.	English	R01UH0637EJ0220	
1	35.3.2 Supply current characteristics		Page 1007	Page 3

Incorrect: Bold with underline; Correct: Gray hatched

Revision History

RL78/G11 Correction for incorrect description notice

Document Number	Issue Date	Description
TN-RL*-A0092A/E	Apr. 3, 2020	First edition issued Corrections No.1 revised (this document)

1. CHAPTER 35 ELECTRICAL SPECIFICATIONS(TA=-40 to +85°C)

35.3.2 Supply current characteristics

Incorrect:

(TA = -40 to +85°C, 1.6 V ≤ EVDD ≤ VDD ≤ 5.5 V, VSS = 0 V) (3/4)

Parameter	Symbol	Conditions		MIN.	TYP.	MAX.	Unit		
Supply current Note 1	I _{DD2} Note 2	HALT mode	HS (high-speed main) mode	f _{HOCO} = 48 MHz ^{Note 4} V _{DD} = 5.0 V		0.59	2.43	mA	
				f _{HI} = 24 MHz ^{Note 4} V _{DD} = 3.0 V		0.59	2.43		
				f _{HOCO} = 24 MHz ^{Note 4} V _{DD} = 5.0 V		0.41	1.83		
				f _{HI} = 24 MHz ^{Note 4} V _{DD} = 3.0 V		0.41	1.83		
				f _{HOCO} = 16 MHz ^{Note 4} V _{DD} = 5.0 V		0.39	1.38		
				f _{HI} = 16 MHz ^{Note 4} V _{DD} = 3.0 V		0.39	1.38		
			LS (low-speed main) mode (MCSEL = 0)	f _{HI} = 8 MHz ^{Note 4} V _{DD} = 3.0 V		250	710	μA	
				V _{DD} = 2.0 V		250	710		
			LS (low-speed main) mode (MCSEL = 1)	f _{HI} = 4 MHz ^{Note 4}	V _{DD} = 3.0 V		204	400	μA
					V _{DD} = 2.0 V		204	400	
				f _{IM} = 4 MHz ^{Note 6}	V _{DD} = 3.0 V		43	250	
					V _{DD} = 2.0 V		43	250	
			LV (low-voltage main) mode	f _{HI} = 4 MHz ^{Note 4}	V _{DD} = 3.0 V		450	700	mA
					V _{DD} = 2.0 V		450	700	

Correct:

(TA = -40 to +85°C, 1.6 V ≤ EVDD ≤ VDD ≤ 5.5 V, VSS = 0 V) (3/4)

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