## Old Company Name in Catalogs and Other Documents

On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

Send any inquiries to http://www.renesas.com/inquiry.

# **RENESAS TECHNICAL UPDATE**

Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Renesas Technology Corp.

Product Category	MPU&MCU		Document No.	TN-SH7-A659A/E	Rev.	1.00
Title	Correction of errors regarding the TADCR register of MTU2		Information Category	Technical Notification		
Applicable Product	SH7125 Group SH7124 Group	Lot No.				
		All	Reference Document	SH7125 Group, SH7124 Group Hardware Manual REJ09B0243-0300 Rev. 3.00		

We would like to inform you of the correction of errors in the above listed hardware manuals. Please refer to the following for details.

<Corrections>

Page 319, Section 9 Multi-Function Timer Pulse Unit 2 (MTU2)

9.5.2 A/D Converter Activation, AD Converter Activation by AD Converter Start Request Delaying Function

### [Before Change]

### A/D Converter Activation by A/D Converter Start Request Delaying Function:

The A/D converter can be activated by generating A/D converter start request signal TRG4AN or TRG4BN when the TCNT\_4 count matches the TADCORA or TADCORB value if the TAD4AE or TAD4BE bit in the A/D converters start request control register (TADCR) is set to 1.

### [After Change]

### A/D Converter Activation by A/D Converter Start Request Delaying Function:

The A/D converter can be activated by generating A/D converter start request signal TRG4AN or TRG4BN when the TCNT\_4 count matches the TADCORA or TADCORB value if the UT4AE, DT4AE, UT4BE, or DT4BE bit in the A/D converters start request control register (TADCR) is set to 1.

