

DA14531 Module Development Kit

Design Name	DA14531-Module-Devkit
Ref. Number	376-25-C
Version	C1.3
Date	02 JULY 2020
Designer	KL

History Table

Version	Date	Comments
A0.0	07/JAN/2020	Draft Release
B0.0	14/FEB/2020	J6 connector pin assignment modification (Pin1:VBUS, Pin5=GND) Signals P0_6, P0_7, P0_8, P0_9, P0_10, P0_11 exposed to PCI connector 4-wire UART assigned on signals P0_6 (UTX), P0_5(URX), P0_8(CTS), P0_7(RTS) Double jumper header J5 added for LED1, LED 2 P0_6 (UTX), P0_5 (URX) and RESET signals added on debug connector J2 J1 pad of DA14531 Module connected to GND R7, R8 changed to 100 Ohm
C0.0	25/FEB/2020	Added resistor R38 for OVP testing Added test point T27, TP28, TP29, TP30 for 4-wire UART signals Change assigment of MikroBus SCL signal to P0_8 from P0_6 Add resistor R39, R40 in parallel to jumper header J5 OVP circuit not populated: J6, SW4, U2, D5, D6, Q2, Q1, C1, C2, C3, C4, R18, R19, R20, R38, R42 LED/ BUTTON section-no populated parts: J5, SW1, D1, R1, R39, R7 Add R41, R42 resistors
C1.0	03/MAR/2020	Changed part number for D1, D2 LEDs
C1.1	27/MAR/2020	Delete part number for non populated parts (BOM change)
C1.2	07/MAY/2020	D3, D4 changed to no populated parts (BOM change)
C1.3	02/JUL/2020	Replace BT1 part number (BOM change)

Configuration note

NOTE box

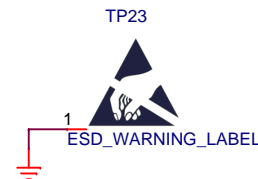
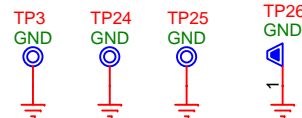
Changes in
this Version.

TP1 FIDUCIAL

TP2 FIDUCIAL



T1 Label_Dialog_L6008



<Variant Name>



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Title : DA14531-Module-Devkit_vC

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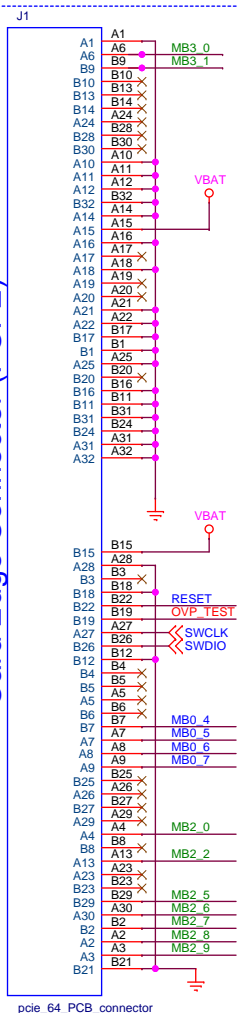
Designer: Kalliopi Liantinioti

Rev:C

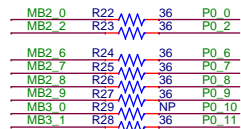
Date: Thursday, July 02, 2020

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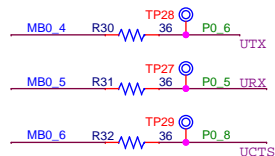
Card Edge Connector (PCI-E)



BREAKOUT HEADERS



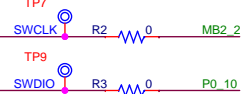
(4-PIN UART)



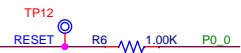
(1-WIRE UART)



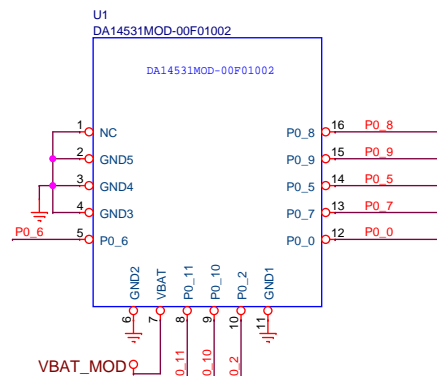
JTAG



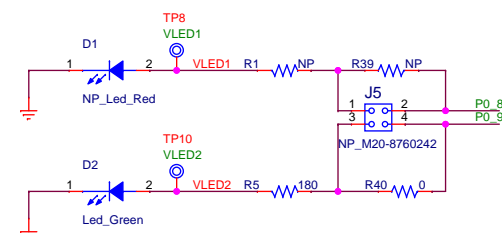
RESET



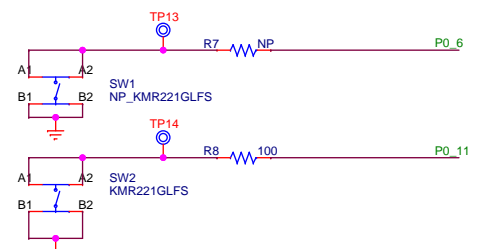
DA14531_QFN24_MODULE



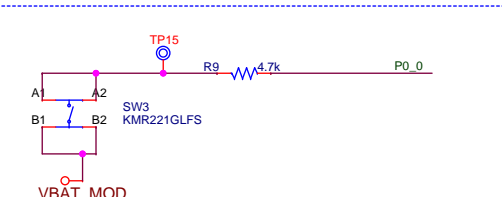
NOTE: Pin #1 (NC) has no internal connection and is recommended to be connected to GND



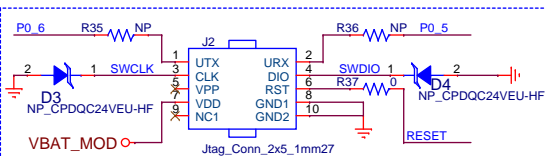
USER LED



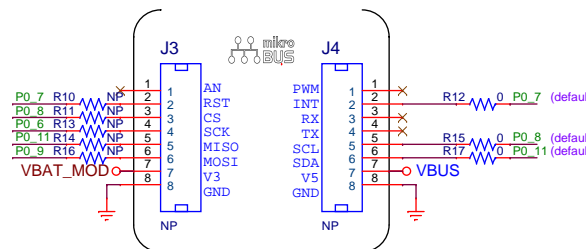
GENERAL PURPOSE SWITCHES/BUTTONS



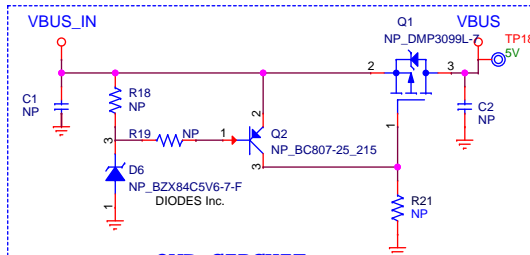
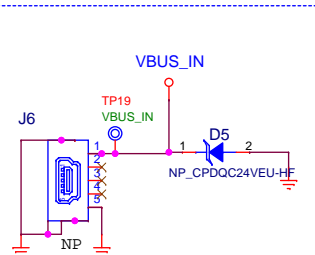
POR/RESET BUTTON



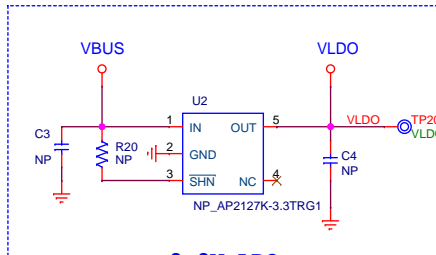
DEBUGGING OPTION (NOT POPULATED)



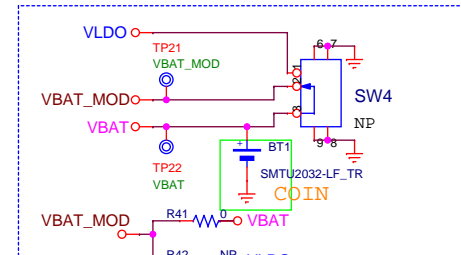
MikroBUS SOCKET (NOT POPULATED)



OVP CIRCUIT

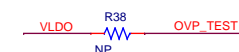


3.3V LDO



POWER OPTIONS

MikroBus GPIO multiplexing					
MikroBus					
	Peripherals	I2C	SPI	Other	
P0_6	SW1		CSK		
P0_11	SW2	SDA (default)	MISO		
P0_8	LED D1	SCL (default)	CS		
P0_9	LED D2		MOSI		
P0_7				INT RST	(default)



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