

QE for BLE[RA] V1.0.0

Release Note

Thank you very much for your using the QE for BLE[RA] V1.0.0.

This release documentation, we have indicated this product installation, restrictions and so on.

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1. About QE for BLE[RA]

1.1 Summary

QE for BLE[RA] is a dedicated tool for developing Bluetooth® low energy-based embedded systems. This solution tool kit runs in the e² studio integrated development environment. The combination of e² studio and QE for BLE[RA] makes it easy to test the communications features of RA4W1 group of RA family that supports BLE5, thus reducing the time required in developing a product prior to its release on the market.

1.2 Functions

Adding Bluetooth functions to a product requires the addition and customization of complex Bluetooth profiles. The QE for BLE[RA] supports Bluetooth development for RA family devices with the following functions.

1. Designation of a profile provided by Bluetooth SIG

Using R_BLE Custom Profile RA (QE) View, you can generate code by designating a Bluetooth profile provided by Bluetooth SIG Inc. (<https://www.bluetooth.com/>).

2. Definition of a proprietary profile

If you want to perform unique data communication (not through profiles provided by Bluetooth SIG), you must define a profile. Using R_BLE Custom Profile RA (QE) View, you can modify a profile easily and generate code.

3. Configuration of advertising and scanning

Supports the configuration of advertising and scanning, a feature of the BLE. Using R_BLE Custom Profile RA (QE) View, you can easily configure the timing and frequency of advertising and scanning communication with the GUI.

4. Communication check by using a profile

The QE for BLE[RA] allows you to check profile-based communication by creating and executing a script using the API for the Bluetooth protocol stack

1.3 Supported Environment

Microsoft Windows 8.1, Windows 10

Renesas e² studio V7.8.0 (or later)

* If you are using a version earlier than e² studio V 7.8.0, please update to V 7.8.0 or later.

1.4 Supported Microcontroller

RA4W1

1.5 Supported FSP Version

FSP V1.1.0 (or later)

2. Installation and Uninstallation

2.1 Installing This Product

Use the following procedure to install this product.

1. Activate e² studio.
2. Click [Help], and then click the [Install New Software...] menu to open the [Install] dialog box.
3. Click the [Add...] button to open the [Add Repository] dialog box.
4. Click the [Archive...] button, select the installation file (zip file) in the opened file selection dialog box, and then click the [Open] button.
5. Click the [OK] button in the [Add Repository] dialog box.
6. Expand the [Renesas QE] item shown in the [Install] dialog box, select the [Renesas QE for BLE[RA]] check box and the [Renesas QE for BLE[RA] Utility] check box, and then click the [Next>] button.
* If you check off the [Contact all update sites during install to find required software] checkbox, you can shorten the installation time.
7. Confirm that the installation target is correct, and then click the [Next>] button.
8. After checking the license, select the [I accept the terms of the license agreements] radio button, and then click the [Finish] button.
9. When a dialog box for selecting a trusted certificate appears, check the displayed certificate, and then click the [OK] button to continue installation.
10. Restart e² studio according to the instruction on the screen.
11. Start this product from the [Renesas Views] - [Renesas QE] menu of e² studio.
For details about how to use this product, see the [Help] menu of e² studio.

2.2 Uninstalling This Product

Use the following procedure to uninstall this product.

1. Start e² studio.
2. Select [Help -> About e² studio] to open the [About e² studio] dialog box.
3. Click the [Installation Details] button to open the [e² studio Installation Details] dialog box.
4. Select [Renesas QE for BLE[RA]] and [Renesas QE for BLE[RA] Utility] displayed on the [Installed Software] tabbed page and click the [Uninstall...] button to open the [Uninstall] dialog box.
5. Check the displayed information and click the [Finish] button.
6. When prompted to restart e² studio, restart it.

3. Notes / Restrictions

3.1 Usage Considerations

Please pay attention to the following items.

3.1.1 Notes of [Working with views]

1. Floating an R_BLE main (QE) view may not display the contents of the floating view.

Workaround:

Do not float the R_BLE main (QE) view.

3.1.2 Notes of [Creating custom profiles]

1. When creating services for a custom profile in the R_BLE Custom Profile RA (QE) View, if you create multiple services with the same UUID, the GUI may behave incorrectly.

Workaround:

Do not duplicate UUIDs.

2. When creating services for a custom profile in the R_BLE Custom Profile RA (QE) View, if you create multiple services with the same abbreviation, the GUI may behave incorrectly.

Workaround:

Do not duplicate abbreviations.

3.2 Functional Restrictions

There are no functional restrictions for this product.

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	May.07.20	-	First edition issued.

General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity.

Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power is supplied until the power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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