PRODUCT CHANGE NOTICE

Data Sheet Specification Change for Intersil Product ISL6719ARZ*

Refer to:
PCN14041

Date: July 15, 2014

intersil™

July 15, 2014

To: Our Valued Intersil Customers

Subject: **Data Sheet Specification Change for Intersil Product ISL6719ARZ***

This notice is to inform you that Intersil has changed the data sheet specification for the ISL6719* products. The change to the Electrical Specification Maximum VOUT, Faulted VSW_FB minimum limit changed from 22V to 21V – aligns the data sheet with the product characteristics and is necessary to maintain product manufacturability in support of customer delivery requirements. Details regarding the change are contained on the following page. The updated data sheet is available is available on the Intersil web site at:

http://www.intersil.com/content/dam/Intersil/documents/fn65/fn6555.pdf.

Products affected: ISL6719ARZ ISL6719ARZ-T

There have been no changes to the die/silicon or product itself. There will be no change in the external marking of the packaged parts.

Intersil will take all necessary actions to conform to agreed upon customer requirements and to ensure the continued high quality and reliability of Intersil products being supplied. Customers may expect to receive product electrically screened to the revised data sheet beginning *ninety* days from the date of this notification or earlier with approval.

If you have concerns with this advisory, Intersil must hear from you promptly. Please contact the nearest Intersil Sales Office or call the Intersil Corporate line at 1-888-468-3774, in the United States, or 1-321-724-7143 outside of the United States.

Regards,

Jeffrey Touvell

Jeffrey Touvell

Intersil Corporation PCN14041

CC: T. Linder D. Singh D. DeCrosta

PCN14041 Data Sheet Change

From:

Electrical Specifications Recommended operating conditions unless otherwise noted. Refer to "Functional Block Diagram" on page 2 and "Typical Application" on page 3. 17V < VPWR < 100V, C_{VSW} = 1µF, I_{VSW} = -3mA, VSW Enabled, T_A = -40°C to +105°C (Note 4), Typical values are at T_A = +25°C. **(Continued)**

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Maximum VOUT, Faulted VSW_FB	VSW_FB = 0V, VPWR = 100V, AUXIN = 40V	22		25	V

To:

Electrical Specifications Recommended operating conditions unless otherwise noted. Refer to "Functional Block Diagram" on page 2 and "Typical Application" on page 3. 17V < VPWR < 100V, C_{VSW} = 1µF, I_{VSW} = -3mA, VSW Enabled, T_A = -40°C to +105°C (Note 7), Typical values are at T_A = +25°C. **(Continued)**

PARAMETER	TEST CONDITIONS	MIN (Note 8)	TYP	MAX (Note 8)	UNITS
Source Voltage Headroom (VPWR - VSW)	VSW = 20V, AUXIN = 0V				
	I_{VSW} = -100mA			6.2	V
	I_{VSW} = -50mA			5.2	V
Minimum Required Load				-3	mA
Maximum VOUT, Faulted VSW_FB	VSW_FB = 0V, VPWR = 100V, AUXIN = 40V	21		25	V

Note: Changes are shaded in yellow