## RENESAS

FOR EVERY SPACE

# HEALTHCARE SOLUTIONS WITH RENESAS SYNERGY<sup>™</sup> PLATFORM



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#### **Speeding-up Medical Device Development**

Developing medical products is a long process that not only involves all the necessary steps of modern product design in times of ever shorter product lifecycles. In addition, the need for patient / user safety is a prime concern and therefore medical devices are tightly regulated when it comes to development and putting them onto the market.

In these times, medical device manufacturers are looking for opportunities to reduce the actual development time as indicated by a recent survey by the PA Consulting Group. In light of the Medical Device Regulation that came into effect in May 2017 in Europe which puts higher requirements on device makers to achieve certification of their products, companies developing products for the medical industry might be even more occupied with work on non-differentiating aspects of their device, and expectations are that overall development times are likely even increasing.



#### **Renesas Synergy™ Platform Benefits for Medical Devices**

In order to address these two topics – certification and long development times – makers of medical devices can look to the Renesas Synergy Platform that provides developers with immediate access to a complete embedded platform starting with an Application Programming Interface (API) to leverage a full software framework beneath which is built around the best-in-class Express Logic X-Ware<sup>™</sup>. This software framework is the Synergy Software Package (SSP) – qualified, maintained, supported, and warranted by Renesas. The platform also integrates a wide range of scalable Arm<sup>®</sup> Cortex<sup>®</sup>-M based MCUs fully accessible through the software APIs, powerful features of the industry-leading development toolchain IAR Embedded Workbench<sup>®</sup> with intuitive configuration assistance, and hardware kits for development and solutions. Developers gain access to all these elements with simple click-through licensing on the Synergy Gallery web site. Since everything has been pre-integrated, tested, and maintained by Renesas, developers can save months of work and extensive investment, freeing them to focus developing solid, reliable, well performing medical applications.



#### Addressing Security Concerns with the Synergy Platform

To address the rising level of concern regarding product security, the Synergy Platform by default includes a rich set of security functionality that addresses confidentiality, trust, non-repudiation and availability of data through mechanisms like key protection and storage, true random number generation, symmetric and asymmetric cipher algorithms, hash functions, secure boot and secure memory segments.



From the certification side, the ThreadX® RTOS included in the SSP has been developed according to IEC62304 – the harmonized international standard that specifies life cycle requirements for the development of medical software and software within medical devices. This "precertification" will make it much easier for device manufacturers to acquire their product certification in the end. As is evident, using the Synergy Platform for your medical device development will provide you with significant advantages to compete in your market.



#### **Synergy S1 Series MCUs for Medical Sensor Applications**

| 32-MHz                                    | Arm® C  | ortex®-M0+ CPU                 | NVIC   SWD   MTB                                |                            |
|---|---------|--------------------------------|---|----------------------------|
| <b>N</b> emory                            |         | Analog                         | Timing & Control                                | нмі                        |
| Code Flash (up to                         | 256 KB) | 14-Bit A/D Converter           | General PWM Timer 32-Bit                        | Capacitive Touch           |
| Data Flash (4 KB) 12-Bit D/A Converter    |         | 12-Bit D/A Converter           | General PWM Timer 16-Bit                        | Sensing Unit               |
| SRAM (up to 24 KB)                        |         | Low-Power Analog<br>Comparator | Asynchronous General<br>Purpose Timer           |                            |
|   |         | Temperature Sensor             | WDT   |                            |
| Connectivity                              |         | System & Power<br>Management   | Safety  | Security & F<br>Encryption |
| USBFS                                     |         | Data Transfer Controller       | SRAM Parity Error Check                         | 128-Bit Unique ID          |
| CAN                                       |         | Event Link Controller          | Flash Area Protection                           | TRNG                       |
| Serial Communications<br>Interface<br>SPI |         | Low Power Modes                | ADC Diagnostics                                 | AES (128/256)              |
|   |         | Multiple Clocks                | Clock Frequency Accuracy<br>Measurement Circuit |                            |
| 110                                       |         | Port Function Select           | CRC Calculator                                  |                            |
|   |         | RTC                            |   |                            |
| DALI Lighting Int                         | ertace  | SysTick                        | Data Operation Circuit                          |                            |
|   |         |                                | Port Output Enable for GPT                      |                            |
|   |         |                                | IWDT  |                            |

Entry-level Synergy S1 Series MCUs use a Cortex M0+ core and focus on ultra-low-power applications with active power consumption of  $77\mu A$  per MHz. Typical medical use cases for S1 Series MCUs include sensor devices (e.g. respiratory sensors) or simple house-keeping functionality in a multi-chip environment like for an AED.



## Synergy S3 Series MCUs for Activity Monitors and Host Applications

| 48-N                     | /IHz Arm® ( | Cortex®-M4 (                | CPU S      | 3 FPU   Arm N<br>JTAG   SWD                     |       |                                     |            |
|--------------------------|-------------|-----------------------------|------------|---|-------|-------------------------------------|------------|
| Memory                   |             | Analog                      | •••        | Timing & Control                                | Ö     | нмі                                 |            |
| Code Flash (up to 1 MB)  |             | 14-Bit A/D Converter        |            | General PWM Timer 32-Bit                        |       | Capacitive Touch                    |            |
| Data Flash (up to 16 KB) |             | 12-Bit D/A Converter        |            | Asynchronous General                            |       | Sensing Unit Segment LCD Controller |            |
| SRAM (up to 192 KB)      |             | Low-Power Analog            |            | Purpose Timer<br>WDT                            |       | Segment LCD                         | Controller |
| Flash Cache              |             | Compara                     |            | WDI   |       |                                     |            |
| MPUs                     |             | High-Speed<br>Compara       |            |   |       |                                     |            |
| Memory Mirror Function   |             | OPAM                        | Р          |   |       |                                     |            |
|                          |             | Temperature                 | Sensor     |   |       |                                     |            |
| Connectivity             |             | System & Powe<br>Management |            | Safety  | Ø     | Security &<br>Encryption            | 6          |
| USBFS                    |             | DMA Controller              |            | ECC in SRAM                                     |       | 128-Bit Unique ID                   |            |
| CAN                      | SDHI/MMC    | Data Transfer               | Controller | SRAM Parity Error                               | Check | TRNG                                |            |
| Serial Communications    |             | Event Link Controller       |            | Flash Area Protection                           |       | AES (128/256)                       |            |
| Interface                |             | Low Power Modes             |            | ADC Diagnostics                                 |       | GHASH                               |            |
| IrDA Interface           |             | Multiple Clocks             |            | Clock Frequency Accuracy<br>Measurement Circuit |       |                                     |            |
| IIC                      | SSI         | Port Function Select        |            | CRC Calculato                                   |       |                                     |            |
| External Memory Bus      |             | RTC                         |            | Data Operation Ci                               |       |                                     |            |
|                          |             | SysTick                     |            | Port Output Enable for GPT                      |       |                                     |            |
|                          |             | Low Voltage D               | etection   | IWDT  |       |                                     |            |

Synergy S3 Series MCUs use Cortex M4 and operate from 32 to 100MHz. For example, the S3A7 MCU Group within the S3 series contains 1MB of flash and operates at 48MHz. The primary focus of the S3 Series MCUs is on high efficiency and typical use cases are sports watches, insulin pumps and simple host applications.



#### Synergy S5 Series MCUs for Hospital Devices and Patient Monitors

| 120-1        | MHz Arm®                 | Cortex®-M4                 | CPU S                                    | 5 FPU   Arm N<br>JTAG   SWD |   |  |                            |  |
|--------------|--------------------------|----------------------------|--|-----------------------------|---|--|----------------------------|--|
| Memory       | Memory                   |                            | Analog                                   |                             | Ġ   | нмі  |                            |  |
| Code Flash   | (up to 2 MB)             | 12-Bit A/D 0               | Converter                                | General PWM Time            |   | Capacitive To<br>Sensing U                   |                            |  |
| Data Flash ( | Data Flash (up to 64 KB) |                            | 12-Bit D/A Converter                     |                             | Enhanced High Resolution  |  |                            |  |
| SRAM (up     | to 640 KB)               | High-Speed<br>Compa        |  | Enhanced                    |   | Graphics LCD Controller<br>2D Drawing Engine |                            |  |
| Flash        | Flash Cache              |                            | PGA                                      |                             | General PWM Timer<br>32-Bit   |  | ec                         |  |
|              | MPUs                     |                            | Temperature Sensor                       |                             | Asynchronous General<br>Purpose Timer   |  | Parallel Data Capture Unit |  |
| Memory Mir   | rrror Function           |                            |  | WDT                         | 21  |  |                            |  |
| Connectivity |                          | System & Pow<br>Management | er 🙀                                     | Safety                      | Ø   | Security &<br>Encryption                     | Ô                          |  |
| Ethernet M/  | AC Controller            | DMA Controller             |  | ECC in SRAM                 |   | 128-Bit Unique ID                            |                            |  |
|              | Ethernet DMA Controller  |                            | Data Transfer Controller                 |                             | SRAM Parity Error Check   |  |                            |  |
| Ethernet PT  | Ethernet PTP Controller  |                            | Event Link Controller                    |                             | Flash Area Protection   |  | /256)                      |  |
| CAN          | SDHI                     | Low Power                  | Modes                                    | ADC Diagnostics             |   | 3DES/ARC4                                    |                            |  |
|              | Serial Communications    |                            | Multiple Clocks Port Function Select RTC |                             | Clock Frequency Accuracy<br>Measurement Circuit<br>CRC Calculator<br>Data Operation Circuit |  | l I                        |  |
|              | Interface                |                            |  |                             |   |  | SHA256                     |  |
| OSPI         | IrDA Interface           |                            |  |                             |   |  |                            |  |
| lic          | SSI                      | SysTick                    |  | Port Output Enable for GPT  |   |  |                            |  |
| Sampling Ra  | ate Converter            |                            |  | IWDT                        | _   |  |                            |  |
| External N   | lemory Bus               |                            |  |                             |   |  |                            |  |

### Synergy S7 Series MCUs for High-end Patient Monitors and Gateways

| 240-MHz Arm® Cortex®-M4 CPU S7 FPU   Arm MPU   NVIC   ETM<br>JTAG   SWD   Boundary Scan |                  |                                     |                   |                  |  |   |   |            |
|---|------------------|-------------------------------------|-------------------|------------------|--|---|---|------------|
| Memory  |                  | Analog                              | 44                | I                | Timing & Control   | Ö | НМІ                                       | R          |
| Code Flash (4 MB)   |                  | 12-Bit A/D Converter ×2<br>(25 ch.) |                   |                  | General PWM Timer 32-Bit<br>Enhanced High Resolution ×4          |   | Capacitive Touch<br>Sensing Unit (18 ch.) |            |
| Data Flas   | sh (64 KB)       | 12-Bit D/A Converter ×2             |                   |                  | General PWM Timer 32-Bit   |   | Graphics LCD C                            | Controller |
| SRAM (640 KB)   |                  |                                     | High-Speed Analog |                  | Enhanced ×4  | _ | 2D Drawing                                | Engine     |
| Flash Cache   |                  | Compara                             |                   |                  | General PWM Timer<br>32-Bit ×6                                   |   | JPEG Codec                                |            |
| MPUs  |                  | PGA ×6                              |                   |                  | Asynchronous General   |   | Parallel Data Capture Unit                |            |
| Memory Mirror Function  |                  | Temperatur                          | e Sensor          | Purpose Timer ×2 |  |   |   |            |
|   |                  |                                     |                   | L                | WDT  |   |   |            |
| Connectivity  |                  | System & Pow<br>Management          | er 🔅              |                  | Safety   | Ø | Security &<br>Encryption                  | Ô          |
| Ethernet MAC  | Controller ×2    | DMA Controller (8 ch.)              |                   |                  | SRAM Parity Error Check  |   | 128-Bit Unique ID                         |            |
| Ethernet DN   | A Controller     | Data Transfer Controller            |                   |                  | Flash Area Protection  |   | TRNG                                      |            |
| Ethernet PTP Controller   |                  | Event Link Controller               |                   |                  | ADC Diagnostics  |   | AES (128/192/256)                         |            |
| USBHS   | USBFS            | Low Powe                            | r Modes           |                  | Clock Frequency Accuracy   |   | 3DES/ARC4                                 |            |
| CAN ×2 SDHI ×2<br>Serial Communications   |                  | Multiple Clocks                     |                   |                  | Measurement Circuit  |   | RSA/DS                                    | A          |
| Interface ×10   |                  | Port Function Select                |                   |                  | CRC Calculator Data Operation Circuit Port Output Enable for GPT |   | SHA1/SHA224                               | /SHA256    |
| IrDA Ir   | IrDA Interface   |                                     | RTC<br>SysTick    |                  |  |   | GHASH                                     | 1          |
| QSPI  | QSPI SPI ×2      |                                     |                   |                  |  |   |   |            |
|   | UT TAL           | SucT                                | ick               |                  |  |   |   |            |
| IIC ×3  | SPI ×2<br>SSI ×2 | SysT                                | ick               |                  | IWDT   |   |   |            |
|   | UT TAL           | SysT                                | ick               |                  | IWDT   |   |   |            |

MCU devices in the Synergy S5 Series also use Cortex M4 and operate from 100 to 200MHz. They are designed for high integration with a rich set of HMI functionality and security. Typically, you can use the S5 Series MCUs for applications like infusion pumps or other hospital equipment with a need for Ethernet connectivity.



Synergy S7 Series devices represent the high end MCUs in the Synergy Platform Devices from the S7G2 MCU Group for example run a Cortex M4 at 240MHz and include 4MB of flash and 640KB of RAM. Typical medical use cases are high-end patient monitors, hospital grade blood analysis devices or gateways.







#### Wireless Frameworks for Enhanced Connectivity

In the time of the Internet of Things, more and more medical device are becoming connected: most of them wirelessly. To address the need for flexibility in design and choice of communication protocols, Renesas has developed a set of Wireless Frameworks for the Synergy Platform Supported wireless technologies in the Synergy ecosystem will be Wi-Fi, BLE and cellular in the first step. One advantage of this approach is that the application is no longer tied to a particular vendor's wireless component. Also, it simplifies migration to additional vendors due to wireless product obsolescence, lack of availability, or manufacturability problems. Finally, application code can be re-used across regional requirements (e.g. allowed frequency bands), and over varying performance requirements. The frameworks will be a part of the SSP covering an initial set of wireless module device drivers. However, it is possible – and desired – to enhance the set of supported modules in any of three ways: third party publication on the Renesas Rulz forum or the Renesas Synergy Gallery or updates of the SSP itself.



#### **Rich Synergy Ecosystem to Make Your Life Easy**

The Synergy Platform was started in 2015 and is growing now with more MCU devices and software providing you with more options to choose from to identify the ideal solution for your medical device development. All existing and future Synergy MCU devices will include SSP support and all other benefits like:

- Synergy Gallery
- •IAR Embedded Workbench development environment for Synergy with no extra fee
- •Development tools such as TraceX and GUIX Studio with no extra fee
- Extensive Support Model (including 24/5 chat)

•Software Ecosystem Enhancements with qualified or verified software add-ons •Synergy Design House Network helping you in the design of your medical device In summary, the Synergy Platform consisting of MCUs, Software, Tools, Support and Gallery will reduce your development time and investment of your resources due to its scalability, integration, qualification, and lifetime maintenance allowing you to focus on your next innovation.



Accelerate. Innovate. Differentiate.



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Renesas Electronics Hong Kong Limited Renesas Electronics America Inc. 2801 Scott Boulevard Santa Clara, CA 95050-2549, U.S.A. Tel: +852-2265-6688, Fax: +852 2886-9022 Tel: +1-408-588-6000, Fax: +1-408-588-6130 9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3 Tel: +1-905-237-2004 Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670 Renesas Electronics Singapore Pte. Ltd. Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949 Tel: +44-1628-585-100, Fax: +44-1628-585-900 Tel: +65-6213-0200, Fax: +65-6213-0300 Renesas Electronics Europe GmbH Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-6503-0, Fax: +49-211-6503-1327 Renesas Electronics (China) Co., Ltd. Room 1709, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100191, P.R.China Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HAL II Stage, Indiranagar, Bangalore, India Tel: +91-80-67208700, Fax: +91-80-67208777 Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd. Unit 301, Tower A, Cantral Towers, 555 Langao Road, Putuo District, Shanghai, P. R. China 200333 Tel: +86-21-2226-0888, Fax: +86-21-2226-0999

Unit 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong

Renesas Electronics Malaysia Sdn.Bhd. Unit 1207, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd. 12F., 234 Teheran-ro, Gangnam-Gu, Seoul, 135-080, Korea Tel: +82-2-558-3737, Fax: +82-2-558-5141

### **Renesas Electronics Corporation**