Renesas RA Family

# Secure Crypto Engine Operational Modes

## Introduction

The Secure Crypto Engine 7 (SCE7) and Secure Crypto Engine 9 (SCE9) on Renesas RA Family MCUs can operate in two different modes, called Compatibility mode and Protected mode. This Application Note describes the two modes, highlights the advantages and disadvantages of each, and provides guidance for using the two modes. In addition, reference links to existing Renesas RA Family Application Projects demonstrating these two modes are provided so the user can refer to them for details on the corresponding FSP module usage.

## Prerequisites and Intended Audience

This application note assumes you have some knowledge about cryptography. The reader is recommended to read the Secure Crypto Engine chapter of the Hardware User's Manual to understand the basics of the hardware features of the SCE.

The intended audience are product developers, product manufacturers, product support, or end users who are involved with designing application systems involving usage of the Renesas RA Family MCU Secure Crypto Engine.

## Contents

# 1. Overview of RA Family MCU Secure Crypto Engine

The Renesas RA Family RA4 and RA6 MCU Series include a Secure Crypto Engine (SCE), which consists of an access management circuit, encryption engine, and random number generator. There are three type of Secure Crypto Engines (SCE) that reside on the different MCU Groups.

- **SCE9**: RA4M2, RA4M3, RA6M4, RA6M5
- **SCE7:** RA6M1, RA6M2, RA6M3, RA6T1
- **SCE5:** RA4M1, RA4W1

This section introduces the general structure and cryptographic capabilities of the Secure Crypto Engines.

## 1.1 General Structure of the Secure Crypto Engine

The Secure Crypto Engines are isolated subsystems on the MCU. The internal cryptographic operations are isolated from a CPU-accessible bus. Renesas's unique secure key handling capabilities enable the creation of solutions that have no plaintext key exposure outside the crypto engine.

Figure 1 is the SCE9 structural feature representation. Different versions of the SCE offer different security feature sets, but the structural features are common. See section 1.2 for details on the differences.

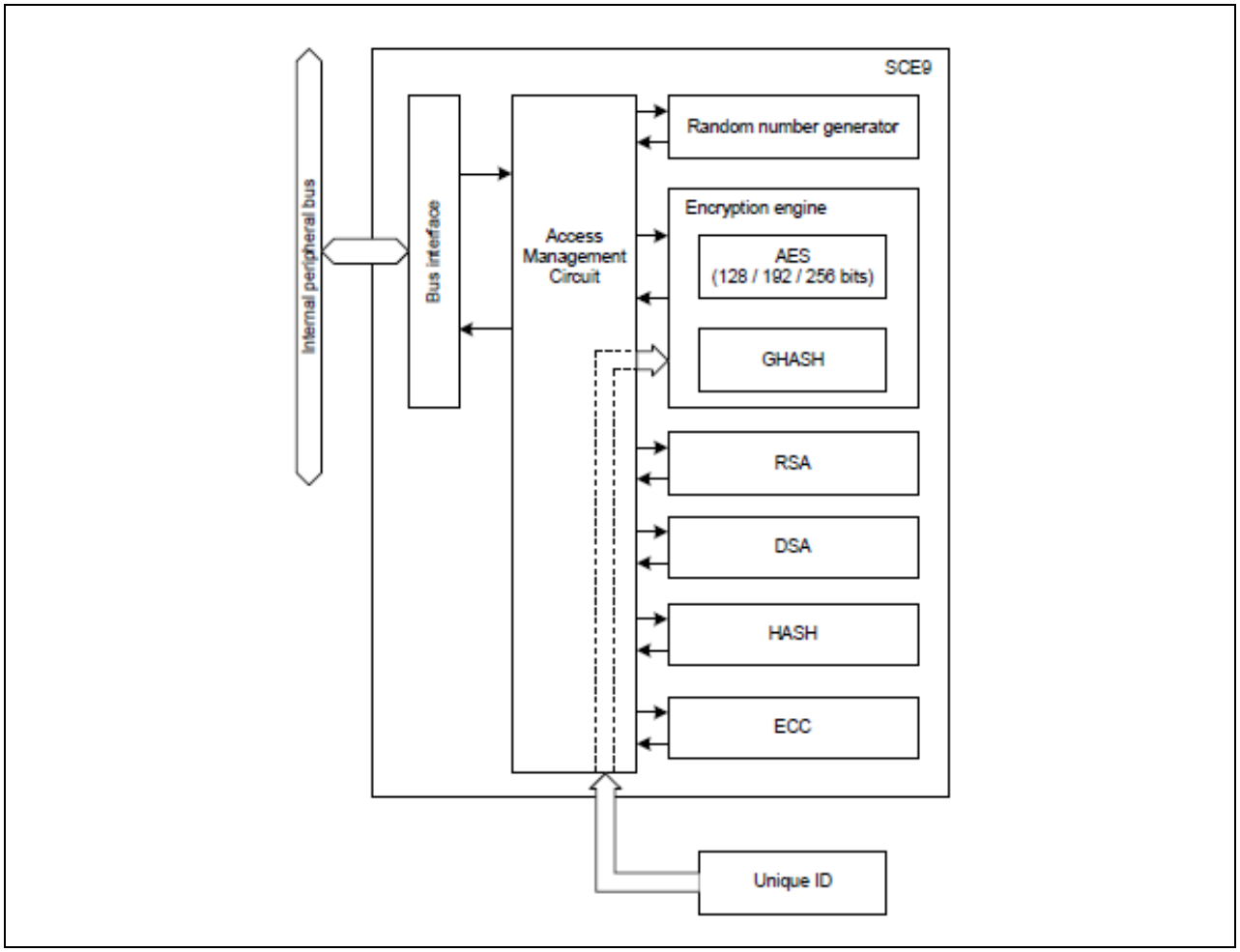


**Figure 1. SCE9 Structural Features**

## 1.2 Cryptographic Capabilities of the Secure Crypto Engine

The following table provides a summary of the cryptographic capabilities of the Secure Crypto Engines found in the RA Family MCUs.

**Table 1. SCE Cryptographic Capabilities**

| Functions | | RA6 and RA4<br>M33 Core, SCE9 | RA6<br>M4 Core, SCE7 | RA4<br>M4 Core, SCE5 |
|---|---|---|---|---|
| RSA | Key Generation, Sign/Verify | Up to 4K<br>(RSA 3K/4K - Verify only) | Up to 2K | - |
| ECC | Key Generation, ECDSA, ECDH | Up to 512 bit | Up to 384 bit | - |
| DSA | Sign/Verify | - | Y | - |
| AES | ECB, CBC, CTR | 128/192/256 | 128/192/256 | 128/256 |
| | GCM, CMAC HMAC | 128/192/256 | 128/192/256 | 128/192/256 |
| | CCM | Y | - | Y |
| Hash | GHASH | Y | Y | - |
| | SHA-1 | - | Y | - |
| | SHA2 (224/256) | Y | Y | - |
| TRNG | HW Entropy, DRBG-AES-128 | Y | Y | Y |
| Wrapped | Key confidentiality, authenticity | Y | Y | Y |
| Plaintext | Legacy compatibility | Y | Y | Y |

Following are some highlights of the features of each of the SCE modules:

- SCE5 provides hardware-accelerated symmetric encryption for confidentiality.
- SCE7 adds hardware-accelerated asymmetric encryption and advanced hash functions for integrity and authentication. SCE7 AES, SHA, and random number generation DRBG are NIST CAVP certified.
- SCE9 extends asymmetric encryption support for RSA up to 4K and enhanced key storage capability with a Hardware Unique Key (HUK). Renesas also intends to certify a full complement of algorithms under NIST CAVP certification.

## 1.3 Key Handling Capabilities of the Secure Crypto Engine

### 1.3.1 Support for Wrapped Keys

Renesas RA Family MCUs have the unique ability to store and use cryptographic keys in wrapped format. Wrapping involves encrypting and signing the key with either the MCU's Hardware Unique Key (HUK) or a derived key based on the MCU's Hardware Root Key and MCU's Unique ID. Since these encryption keys are unique for each individual MCU, even if an attacker were able to extract the wrapped key, another MCU will not be able to use it.

In Compatibility mode, plaintext keys can be wrapped by application software. Wrapped keys can also be generated by the Secure Crypto Engine. The application software can then use the wrapped keys via the PSA Crypto APIs.

In Protected mode, only wrapped key can be used by application software. Wrapped keys can be generated by the Secure Crypto Engine, and known keys can be securely installed via a device programmer. Application software can install new keys by using a previously installed Key-Update Key, which must be installed via a device programmer. Refer to the *Renesas RA Family Installing and Updating Secure Keys* Application Project for more information about this process.

### 1.3.2 Support for Plaintext Keys

Plaintext keys are often required to provide legacy system support or to integrate with various software stacks and libraries. SCE Compatibility mode supports plaintext key usage.

SCE Protected mode does not support plaintext keys. Having plaintext keys present in the application is inherently a security risk, because it is possible that malicious code could exploit system weaknesses and obtain the plaintext key data. This risk may be determined to be low enough to be acceptable, but the risk does exist. Protected mode protects against this risk by not supporting plaintext key usage.

## 2. Compatibility Mode of the Secure Crypto Engine

Compatibility mode provides straight-forward integration with legacy systems and third-party software and solutions, while offering optimised performance and unlimited secure key storage.

## 2.1 Advantages of Compatibility Mode

Following are some advantages when using Compatibility mode.

- Plaintext keys are allowed. This provides compatibility with legacy systems and simplifies software development. It can also be necessary to integrate with existing software and infrastructure. Many existing programming systems support plaintext key installation, often using application code to securely store the key on chip.
- Wrapped keys for secure key storage are supported but not required. Generation of wrapped keys is also supported.

## 2.2 Disadvantages of Compatibility Mode

Following are some disadvantages when using Compatibility mode.

- No Simple Power Analysis (SPA) and Differential Power Analysis (DPA) protections.
- Potential user key exposure if plaintext keys are used. The user must evaluate the potential threats and risk of this exposure and implement their design accordingly.
- Secure key installation is not available
- Secure key update is limited, as there will be plaintext exposure outside SCE. For more information about this process, refer to the Renesas RA Family MCU *Installing and Utilizing Cryptographic User Keys using SCE9* Application Project.

## 2.3 Compatibility Mode Support with Renesas RA FSP

The SCE Compatibility mode is supported by all Renesas RA Family RA6 and RA4 MCUs which have a Secure Crypto Engine. This mode can be accessed using FSP MbedCrypto module or the Network connection stacks in FSP v2.0.0 or later. There are several application projects that demonstrate the SCE operating in Compatibility mode. Refer to the Reference section items 3 to 6.

## 3. The Protected Mode of the Secure Crypto Engine

Protected mode provides optimum protection against security attacks by providing SPA/DPA resistance and secure key installation and update, with a usage model that enforces secure best practices key handling.

## 3.1 Advantages of Protected Mode

Protected mode has many security advantages listed as follows:

- No plaintext key exposure on any CPU- or externally accessible bus.
- Secure key installation using the serial programming interface simplifies secure key provisioning.
- Secure key update via user-installed Key-Update Keys allows secure key update in the field.
- SPA/DPA side-channel attack resistance is included. Side-channel attack using power analysis is one of the most frequent attacks used to extract sensitive information from a chip. Renesas RA Family SCE Protected mode implements countermeasures against such attacks.
- Countermeasures for timing attacks are implemented. The ECC and RSA implementation on SCE9 are constant time when dealing with sensitive key material.
- The implemented API is designed to be compatible with the RX Family TSIP Library, facilitating porting software between Renesas MCU families.

## 3.2 Disadvantages of Protected Mode

Following are the potential disadvantages of using Protected mode:

- Plaintext keys are not allowed, which can introduce difficulties integrating with legacy systems and software.
- For cryptographic protocols that needs key calculation, for example ECDH and ECIES, key calculation must be done within the SCE. This functionality is currently not supported in FSP v3.0.0, however, a selection of these algorithms will be added in later FSP releases.

## 3.3 Protected Mode Support with Renesas RA FSP and Renesas Flash Programmer

The SCE Protected mode can be accessed using the FSP Crypto module (`r_sce_protected`) in a standalone format with FSP v3.0.0 or later versions. Support for other libraries (for example, TLS) will be integrated in later FSP releases.

Protected mode supports wrapped user key generation via FSP Crypto API calls and key installation via the serial programming interface using RFP.

Field update of user keys can be achieved by installing one or more Key-Update Keys via the serial programming interface. New keys are then installed using one of the previously installed Key-Update Keys and the FSP Crypto APIs.

To get hands-on experience using the SCE Protected mode with FSP Crypto APIs, user can reference Renesas RA Family MCU Installing and Updating Secure Keys Application Project. This Application Project includes an Application Note which provides step by step instructions on how to perform user key and Key-Update Key installation. In addition, a reference example software project is provided in this Application Project that implements new user key update via the previously installed Key-Update Key and FSP Crypto APIs. See the Reference section for information on this Application Project.

# 4. Secure Crypto Engine Operational Modes Summary

**The PSA Crypto API implementation uses SCE Compatibility mode. The FSP Crypto API implementation uses SCE Protected mode.** The following table provides a side-by-side comparison of the two modes regarding the key formats, key installation in terms of FSP support and inoperability between the two different operation modes. **Note that keys installed via a device programmer (that is, the factory bootloader) cannot be used in Compatibility mode.**

| Capability | Compatibility Mode PSA Crypto API | Protected Mode FSP Crypto API |
|---|---|---|
| **Plaintext Symmetric and Private Keys** | | |
| Installation via factory bootloader | No | No |
| Installation via FSP | Yes | |
| Creation via key generation | No | |
| Usage within FSP | Yes | |
| **Standard Format Public Keys** | | |
| Installation via factory bootloader | No | Yes |
| Installation via FSP | Yes | No |
| **MAC-tagged Public Keys** | | |
| Usage within FSP | Yes | Yes |
| Cross-mode compatibility | No | No |
| **Factory Wrapped Symmetric and Private Keys (DLM Server)** | | |
| Installation of DLM-wrapped key via factory bootloader | No | Yes |
| Installation of DLM-wrapped key via FSP | | No |
| **MCU-wrapped Symmetric and Private Keys** | | |
| Creation by factory bootloader | No | No |
| **Installed via key installation in factory bootloader** | **No** | **Yes** |
| Installed via key installation in FSP | Yes | Yes |
| Creation via key generation | Yes | Yes |
| Usage within FSP | Yes | Yes |
| **Cross-mode compatibility** | **No** | **No** |

Ensures optimal key storage protection

Enables legacy system and software support

Provides authenticity check.

Simplifies secure provisioning

Removes Renesas DLM server dependency. Protects against malicious key installation.

# 5. Mode Selection based on Application Use Cases

This section introduces some of the common cryptographic application use cases. Information on the SCE operational modes support status for these uses cases are provided for user's reference.

## 5.1 Trusted Firmware M (TF-M)

Trusted Firmware-M (TF-M) implements the Secure Processing Environment (SPE) for Armv8-M, Armv8.1-M architectures (for example, the Arm® Cortex®-M33, Cortex-M23, Cortex-M55 processors) or dual-core platforms. Renesas RA Family FSP integrated TF-M support starting with FSP v2.0.0 for use on TrustZone-enabled MCUs.

TF-M uses PSA Crypto APIs and SCE Compatibility mode for cryptographic operations. This support allows the customer to benefit from the Arm PSA Ecosystem software.

## 5.2 Internet Connectivity

FSP has integrated Amazon FreeRTOS and MbedTLS support. Compatibility mode is used when integrating with this software combination.

FSP also integrated Azure RTOS and NetX Duo support from FSP v3.0.0. Compatibility mode is used for this software combination.

Internet connectivity solutions using Protected mode are currently under development to provide optimum secure key storage.

## 5.3 Private Infrastructure Connectivity

For private infrastructure in industry or networking applications, it is recommended, if possible, to use Protected mode with FSP Crypto APIs for increased security considerations.

If plaintext keys must be used, for example to interface with existing infrastructure, then Compatibility mode with PSA Crypto APIs must be used.

## 5.4 Production Support and Supply Chain Considerations

Protected mode provides the following benefits for customers who are concerned with protecting their supply chain:

- Secure key installation can be conveniently performed in production for all MCUs.
- With RA Family MCUs, OEM can further lock down the secure key storage and IP region for enhanced security control prior to deliver the hardware downstream.

# 6. References

1. Renesas RA Family MCU RA6M4 Group User's Manual: Hardware
2. Renesas RA Family MCU RA6M3 Group User's Manual: Hardware
3. Renesas RA Family MCU Establishing and Protecting the Device Identity using SCE7 and Security MPU
4. Renesas RA Family MCU Establishing and Protecting the Device Identity using SCE9 and TrustZone
5. Renesas RA Family MCU Installing and Utilizing Cryptographic User Keys using SCE9
6. Using Trusted Firmware M (TF-M) with FSP v2.03
7. Renesas RA Family Installing and Updating Secure Keys.
   The Application Note document number is r11an0496 and user can search this application note number under Renesas RA RA6M4 or EK-RA6M4 product page to location this application project.

# 7. Website and Support

Visit the following URLs to learn about the RA family of microcontrollers, download tools and documentation, and get support.

| | |
|---|---|
| RA Product Information | renesas.com/ra |
| Flexible Software Package (FSP) | renesas.com/ra/fsp |
| RA Product Support Forum | renesas.com/ra/forum |
| Renesas Support | renesas.com/support |

## Revision History

| Rev. | Date | Description | |
|---|---|---|---|
| | | Page | Summary |
| 1.00 | May.18.21 | - | First release |

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