

## Renesas Synergy™ Platform

# I<sup>2</sup>C Framework Module Guide

## Introduction

This module guide will enable you to effectively use a module in your own design. Upon completion of this guide, you will be able to add this module to your own design, configure it correctly for the target application and write code, using the included application project code as a reference and efficient starting point. References to more detailed API descriptions and suggestions of other application projects that illustrate more advanced uses of the module are available on the Renesas Synergy Knowledge Base (as described in the References section at the end of this document) and should be valuable resources for creating more complex designs.

The I<sup>2</sup>C Framework module provides a ThreadX®-aware high-level API for I2C industry standard serial device communications and configures the I<sup>2</sup>C peripheral in order to enable serial communication to be used by the framework. The I<sup>2</sup>C Framework module uses the I<sup>2</sup>C and SCI peripherals on the Synergy MCU.

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### 1. I<sup>2</sup>C Framework Module Features

- ThreadX-aware framework
- Handles integration and synchronization of multiple I<sup>2</sup>C peripherals on the I<sup>2</sup>C bus
- Provides a single interface to access both SCI I<sup>2</sup>C and RIIC drivers
- The I<sup>2</sup>C framework module configures I<sup>2</sup>C communication in master mode
- The I<sup>2</sup>C framework module supports three data rates: 100 kHz, 400 kHz, and 1 MHz
- The I<sup>2</sup>C framework module supports both 7-bit addressing and 10-bit addressing
- The I<sup>2</sup>C framework module also provides support for callbacks internally. User defined callback is not used. The callback functions are called with the following events i2c\_event\_t:
  - Transfer aborted
  - Transmit complete
  - Receive complete.
  - The callback structure i2c\_callback\_args\_t also provides the number of bytes that were sent or received
  - Implemented by:
    - Simple I<sup>2</sup>C on SCI
    - RIIC.

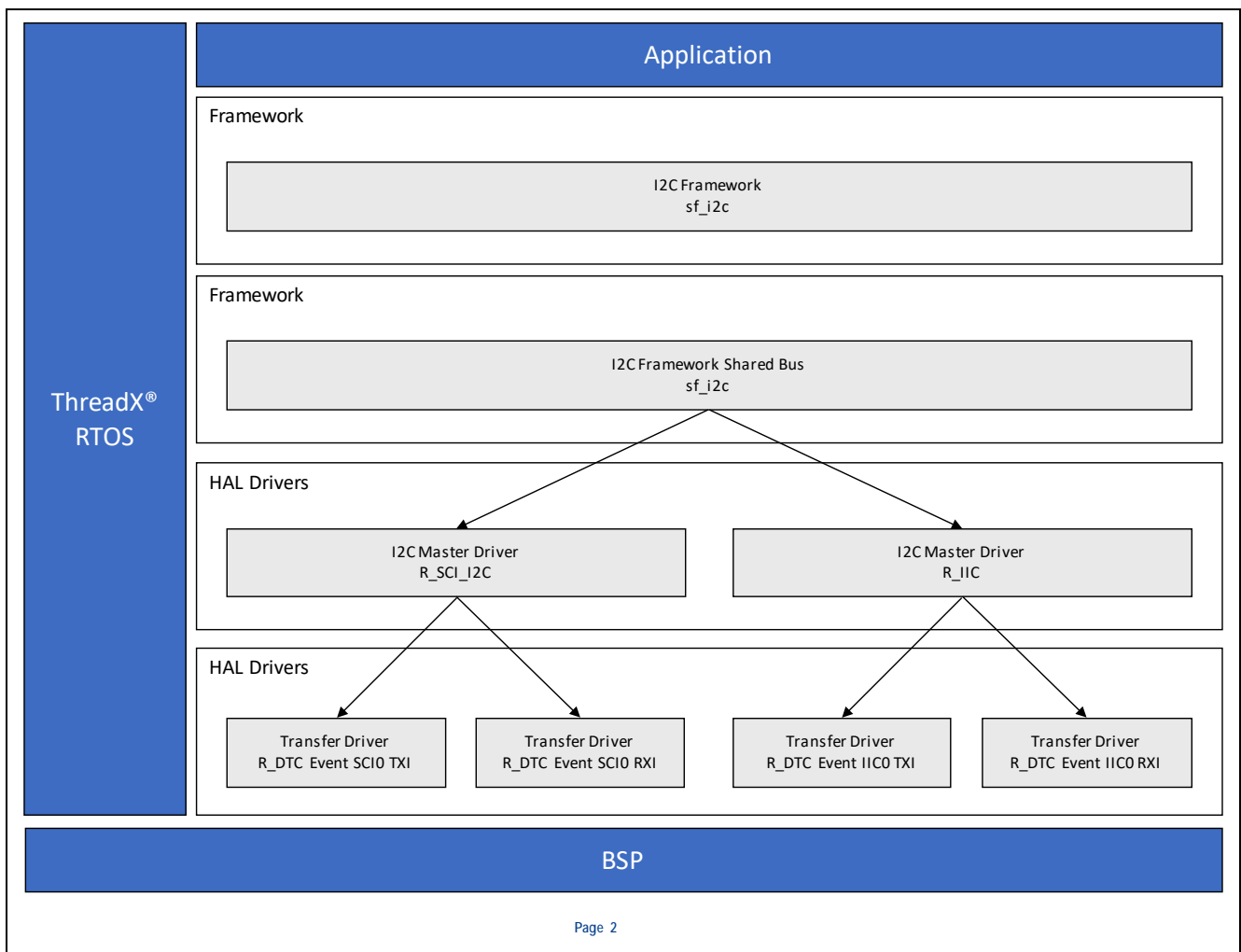


Figure 1. I<sup>2</sup>C Framework Module Block Diagram

## 2. I<sup>2</sup>C Framework Module APIs Overview

The I<sup>2</sup>C Framework interface defines APIs for opening, closing, reading, writing, locking, unlocking, and resetting the bus using the I<sup>2</sup>C Framework. A complete list of the available APIs, an example API call and a short description of each can be found in the following table. A table of status return values follows the API summary table.

**Table 1. I<sup>2</sup>C Framework Module API Summary**

Function Name	Example API Call and Definition
open	<code>g_sf_i2c_device.p_api-&gt;open (g_sf_i2c_device.p_ctrl, g_sf_i2c_device.p_cfg)</code> Opens a designated I <sup>2</sup> C device on the bus.
close	<code>g_sf_i2c_device.p_api-&gt;close (g_sf_i2c_device.p_ctrl);</code> Disables the I <sup>2</sup> C device designated by control handle. Closes the RTOS services used by the bus if no devices are connected to the bus.
read	<code>g_sf_i2c_device.p_api-&gt;read (g_sf_i2c_device.p_ctrl, &amp;destination, no_of_bytes_to_read, restart, timeout);</code> Receives data from I <sup>2</sup> C device.
write	<code>g_sf_i2c_device.p_api-&gt;write (g_sf_i2c_device.p_ctrl, &amp;source, no_of_bytes_to_write, restart, timeout);</code> Transmits data to I <sup>2</sup> C device.
lock	<code>g_sf_i2c_device.p_api-&gt;lock (g_sf_i2c_device.p_ctrl);</code> Locks the bus for a device. Locking the bus reserves it until unlocking and allows several reads and writes without interrupt.
unlock	<code>g_sf_i2c_device.p_api-&gt;unlock (g_sf_i2c_device.p_ctrl);</code> Unlocks the bus from a particular device and makes it available for other devices.
reset	<code>g_sf_i2c_device.p_api-&gt;reset (g_sf_i2c_device.p_ctrl, timeout);</code> Aborts any in-progress transfer and forces the I <sup>2</sup> C peripheral into ready state.
versionGet	<code>g_sf_i2c_device.p_api-&gt;version(version);</code> Retrieves the version information using the version pointer.
lockWait	<code>g_sf_i2c_device.p_api-&gt;lockWait(g_sf_i2c_device.p_ctrl, timeout);</code> Locks the bus for a device. Locking the bus reserves it until unlocking and allows several reads and writes without intervention from other devices on the same I <sup>2</sup> C bus. Timeout value is user configurable.

**Note:** For more complete descriptions of operation and definitions for the function data structures, typedefs, defines, API data, API structures, and function variables, review the *SSP User's Manual API References* for the associated module.

**Table 2. Status Return Values**

Name	Description
SSP_SUCCESS	I <sup>2</sup> C function performed successfully
SSP_ERR_INVALID_MODE	Illegal I <sup>2</sup> C mode is specified
SSP_ERR_IP_CHANNEL_NOT_PRESENT	Omitted I <sup>2</sup> C channel is specified
SSP_ERR_IN_USE	I <sup>2</sup> C channel has already been opened
SSP_ERR_INVALID_ARGUMENT	Argument is not one of the predefined values
SSP_ERR_INTERNAL	Internal error has occurred
SSP_ERR_ASSERTION	A critical assertion has failed, or Null pointer(s) is(are) given
SSP_ERR_NOT_OPEN	Device instance not opened
SSP_ERR_TRANSFER_ABORTED	The data transfer was aborted
SSP_ERR_INVALID_RATE	The requested rate cannot be set
SSP_ERR_TIMEOUT	Timeout error occurs

Note: Lower-level drivers may return common error codes. Refer to the *SSP User's Manual API References* for the associated module for a definition of all relevant status return values.

### 3. I<sup>2</sup>C Framework Module Operational Overview

The I<sup>2</sup>C Framework module complies with the layered driver architecture of the SSP. It uses the lower-level I<sup>2</sup>C HAL modules to communicate with the I<sup>2</sup>C peripherals and controls the I<sup>2</sup>C-capable peripherals on a Synergy microcontroller, as configured by a user. With the I<sup>2</sup>C Framework module, one or more I<sup>2</sup>C buses can be created and multiple I<sup>2</sup>C peripherals can be connected to each I<sup>2</sup>C bus. The I<sup>2</sup>C Framework module APIs use a ThreadX-mutex to acquire and release the shared bus for I<sup>2</sup>C slave devices. In the I<sup>2</sup>C Framework module, acquire and release are implemented by `lock` (or `lockWait`) and `unlock` APIs, respectively.

As the I<sup>2</sup>C framework module configures I<sup>2</sup>C communication in master mode, this allows the user to:

- Initialize the driver
- Read from a slave device
- Write to a slave device
- Reset the I<sup>2</sup>C peripheral
- Lock the I<sup>2</sup>C bus
- Unlock the I<sup>2</sup>C bus.

The I<sup>2</sup>C Framework module works with the Synergy MCU I<sup>2</sup>C hardware modules, the RIIC and SCI HAL modules. Both I<sup>2</sup>C modules support the I<sup>2</sup>C fast-mode with bit rates of up to 400 kHz. The IIC peripheral and the RIIC HAL module support fast-mode plus with 1-MHz bit-rates. The module supports only master mode for both implementations.

#### Multiple Slave Devices on the Same Bus

The I<sup>2</sup>C Framework module uses a bus and device on bus architecture. If multiple slaves are connected to the I<sup>2</sup>C bus, each slave communicates with an associated and separate SF\_I2C module instance. Each SF\_I2C instance is created in a separate thread. Every slave device is linked to the bus to which it will be connected and share the bus with all other slave devices. The user must configure the framework shared-bus and the lower-level I<sup>2</sup>C HAL layer for each framework device connecting to the bus. The user can add the existing framework shared-bus when configuring multiple devices on the same bus. A common start and stop procedure is used for all I<sup>2</sup>C data-transfer operations. Only one device is configured to the lower level and the remaining devices perform read or write operations by switching the device address within the framework.

All I<sup>2</sup>C Framework devices on the same bus must use the same lower-level configuration settings (for example, the I<sup>2</sup>C HAL module), except for the slave address and addressing mode. The framework uses the configuration of the first device that it opens in the application to configure the bus. This means that all I<sup>2</sup>C Framework devices on the same bus must have the same lower-level configuration settings (except for the slave address and addressing mode). If different configurations are used, proper operation cannot be guaranteed.

#### Bus Locking

The I<sup>2</sup>C Framework supports bus-locking functionality, meaning that the bus can be locked for a given peripheral. The locking allows devices to reserve a bus to themselves for a given time period (between lock and unlock). This allows devices to complete several reads and writes on the bus without interruption (which is required in some instances).

The I<sup>2</sup>C bus is locked when `lock` or `lockWait` APIs are called. This API locks the I<sup>2</sup>C bus by acquiring the mutex for the thread in which the I<sup>2</sup>C Framework device is used. Once locked, the I<sup>2</sup>C bus can only be utilized by the associated device. The other I<sup>2</sup>C Framework device, or same I<sup>2</sup>C framework device from other threads, cannot acquire the mutex, so they won't be able to access the bus. Once the bus is unlocked by calling the `unlock` API from the `sf_i2c` device that locked it, the mutex is released and the bus becomes available for other `sf_i2c` devices. The `lockWait` and `lock` API are similar, except the `lock` API provides the user with an option to set timeout value. The `lockWait` API waits for the specified timeout period if the I<sup>2</sup>C bus is already locked by another device. In case of the `lock` API, the thread waits forever, if the I<sup>2</sup>C bus is not released by the other device.

### 3.1 I<sup>2</sup>C Framework Module Important Operational Notes and Limitations

#### 3.1.1 I<sup>2</sup>C Framework Module Operational Notes

- The closest possible baud rate that can be achieved (less than or equal to the requested rate) at the current PCLKB settings is calculated and used. If a valid clock rate could not be calculated, an error is returned.
- The I<sup>2</sup>C can trigger the start of other peripherals available from the ELC. See the ELC Module Guide for further information.
- The I<sup>2</sup>C Framework can support multiple I<sup>2</sup>C devices on the same bus if the clock rate remains the same for all the devices. That means multiple devices can be opened in the same bus if they are of the same clock rate. If devices have different clock rates, only one device can be opened at a time.
- SDA and SCL output pin type should be n-channel open drain when using I<sup>2</sup>C on SCI.
- Shared bus can be used by multiple slave devices with the respective configuration. The framework also handles mutual exclusion in lock and unlock APIs when multiple devices are using the same I<sup>2</sup>C channel.
- To configure multiple I<sup>2</sup>C devices on the same bus, add and configure the following modules for each device to be connected to the bus:
  - I<sup>2</sup>C Framework device module
  - Configure the I<sup>2</sup>C shared bus module for the first device being configured, then use the same bus for the remaining devices
  - I<sup>2</sup>C HAL module
  - DTC module (optional)
- Lock functionality will be effective for devices from different threads. If multiple devices connected to the bus are from the same thread, the I<sup>2</sup>C bus will be locked for all devices from that thread. In such cases, even if the bus is locked, all devices from the same thread can access the bus.
- In case a device is being used from multiple threads, and the device locks the I<sup>2</sup>C bus from one thread, the same device cannot access the I<sup>2</sup>C bus from other threads.

Note: Configure each I<sup>2</sup>C Framework device with a unique name in the ISDE configurator. Provide the same configuration settings for devices connected on the same bus (except the slave address and addressing modes).

#### 3.1.2 I<sup>2</sup>C Framework Module Limitations

The I<sup>2</sup>C framework module does not currently support the use of DMA. Refer to the latest SSP Release Notes for any additional operational limitations for this module.

## 4. Including the I<sup>2</sup>C Framework Module in an Application

This section describes how to include the I<sup>2</sup>C Framework module in an application using the SSP configurator.

Note: It is assumed you are familiar with creating a project, adding threads, adding a stack to a thread, and configuring a block within the stack. If you are unfamiliar with any of these items, refer to the first few chapters of the *SSP User's Manual* to learn how to manage each of these important steps in creating SSP-based applications.

To add the I<sup>2</sup>C Framework module to an application, simply add it to a thread using the stacks selection sequence given in the following table. (The default name for the I<sup>2</sup>C Framework Module is `g_sf_i2c_device0`. This name can be changed in the associated Properties window.)

**Table 3. I<sup>2</sup>C Framework Module Selection Sequence**

Resource	ISDE Tab	Stacks Selection Sequence
g_sf_i2c_device0 I2C Framework on sf_i2c	Threads	New Stack> Framework> Connectivity> I <sup>2</sup> C Framework Device on sf_i2c

The following figure shows when the I<sup>2</sup>C Framework module on sf\_i2c is added to the thread stack, the configurator automatically adds any needed lower-level modules. Any drivers that need additional configuration information are box text highlighted in **Red**. Modules with a **Gray** band are individual modules

that stand alone. Modules with a Blue band are shared or common and need only be added once and can be used by multiple stacks.

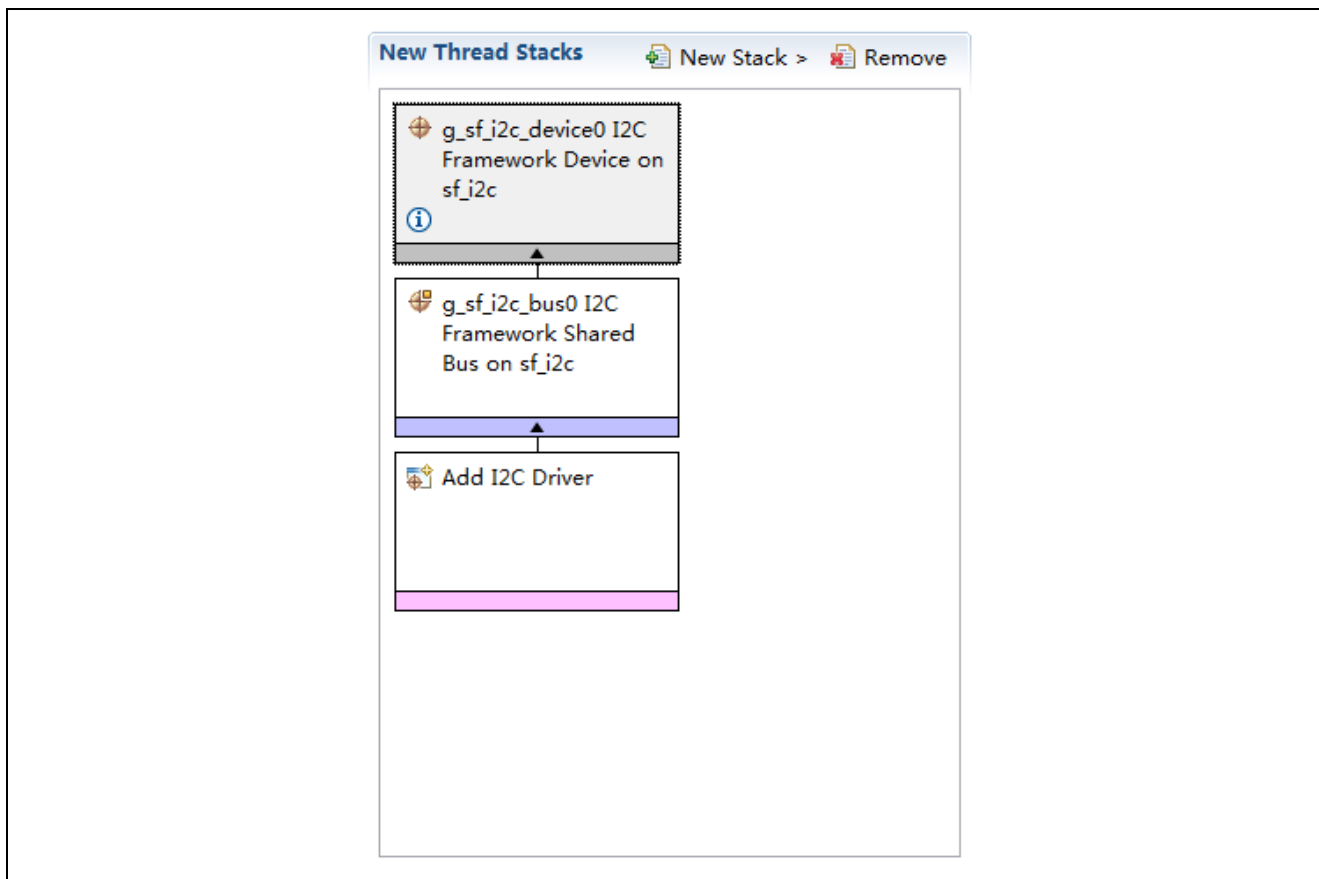


Figure 2. I<sup>2</sup>C Framework Module Stack

### 5. Configuring the I<sup>2</sup>C Framework Module

The I<sup>2</sup>C Framework module must be configured by you for the desired operation. The SSP configuration window automatically identifies (by highlighting the block in red) any required configuration selections, such as interrupts or operating modes, which must be configured for lower-level modules for successful operation. Only those properties that can be changed without causing conflicts are available for modification. Other properties are ‘locked’ and unavailable for changes. They are identified with a lock icon for the ‘locked’ property in the Properties window in the ISDE. This approach simplifies the configuration process and makes it much less error-prone than previous ‘manual’ approaches to configuration. The available configuration settings and defaults for all the user-accessible properties are given in the Properties tab within the SSP configurator and are shown in the following tables for easy reference.

Note: You may want to open your ISDE, create the module, and explore the property settings in parallel with reviewing the following configuration table settings. This help to orient you and can be a useful hands-on approach to learning the ins and outs of developing with SSP.

Table 4. Configuration Settings for the I<sup>2</sup>C Framework Module Device on sf\_i2c

Parameter	Value	Description
Parameter Checking	BSP, Enabled, Disabled (Default: Enabled)	Enable or disable parameter error checking.
Name	g_sf_i2c_device0	Give a name to identify the I <sup>2</sup> C Framework device. API, Config and Control instances will be created based on this name.
Slave Address	0x00	Specify the address of the I <sup>2</sup> C slave device.

Parameter	Value	Description
Address Mode	7-Bit, 10-Bit (Default: 7-Bit)	Select the I <sup>2</sup> C address mode.

Note: The example values and defaults are for a project using the Synergy S7G2 MCU. Other MCUs may have different default values and available configuration settings.

In some cases, settings other than the defaults for lower-level modules can be desirable. For example, it might be useful to select different address mode or set different slave address. The configurable properties for the lower-level modules are given in the following sections for completeness and as a reference.

Note: Most of the property settings for lower-level modules are intuitive and can usually be determined by inspection of the associated Properties window with the SSP configurator.

## 5.1 Configuration Settings for the I<sup>2</sup>C Framework Lower-Level Modules

Typically, only a small number of settings must be modified from the default for lower-level drivers as indicated by the red text in the thread stack block. Notice that some of the configuration properties must be set to a certain value for proper framework operation and will be locked to prevent user modification. The following table identifies all the settings within the properties section for the module:

**Table 5. Configuration Settings for the I<sup>2</sup>C Framework Shared Bus on sf\_i2c**

ISDE Property	Value	Description
Name	g_sf_i2c_bus0	Module name.

Note: The example values and defaults are for a project using the Synergy S7G2 MCU. Other MCUs may have different default values and available configuration settings.

**Table 6. Configuration Settings for the I<sup>2</sup>C Master Driver on r\_sci\_i2c**

ISDE Property	Value	Description
Parameter Checking	BSP, Enabled, Disabled (Default: BSP)	Enable or disable parameter error checking.
Name	g_i2c0	Module name.
Channel	0	Specify the SCI channel to be used with this configuration.
Rate	Standard, Fast-mode (Default: Standard)	Select the I <sup>2</sup> C data rate.
Slave Address	0	Address of the slave device.
Address Mode	7-Bit, 10-Bit (Default: 7-Bit)	Only 7-bit addresses are currently supported.
SDA Output Delay (nano seconds)	300	SDA output delay in nanoseconds.
Bit Rate Modulation Enable	Enable, Disable (Default: Enable)	Enables bitrate modulation function.
Callback	NULL	A user callback function can be registered in <code>i2c_api_master_t::open</code> . If this callback function is provided, it will be called from the interrupt service routine (ISR) for each of the conditions defined in <code>i2c_event_t</code> . <b>Warning:</b> Since the callback is called from an ISR, do not use blocking calls or lengthy processing. Spending excessive time in an ISR can affect the responsiveness of the system.
Receive Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX) (Default: Priority 12)	Receive interrupt priority selection.



ISDE Property	Value	Description
Transmit Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX) (Default: Priority 12)	Transmit interrupt priority selection.
Transmit End Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX) (Default: Priority 12)	Transmit end interrupt priority selection.

Note: The example values and defaults are for a project using the Synergy S7G2 MCU. Other MCUs may have different default values and available configuration settings.

**Table 7. Configuration Settings for the I<sup>2</sup>C Master Driver on r\_riic**

ISDE Property	Value	Description
Parameter Checking	BSP, Enabled, Disabled (Default: BSP)	Enable or disable parameter error checking
Name	g_i2c0	Module name
Channel	0	Specify the IIC channel to be used with this configuration.
Rate	Standard, Fast-mode, Fast-mode plus Default: Standard	Standard, Fast and Fast-plus. (See IIC Rate Calculation.)
Slave Address	0	Set the address of the slave device the I <sup>2</sup> C master will be communicating with.
Address Mode	7-Bit, 10-Bit (Default: 7-Bit)	Only 7-bit addresses are currently supported.
Timeout Mode	Short Mode, Long Mode (Default: Short Mode)	Select the timeout mode.
Callback	NULL	A user callback function can be registered in <code>i2c_api_master_t::open</code> . If this callback function is provided, it will be called from the interrupt service routine (ISR) for each of the conditions defined in <code>i2c_event_t</code> . <b>Warning:</b> Since the callback is called from an ISR, do not use blocking calls or lengthy processing. Spending excessive time in an ISR can affect the responsiveness of the system.
Receive Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX) (Default: Priority 12)	Receive interrupt priority selection
Transmit Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX) (Default: Priority 12)	Transmit interrupt priority selection
Transmit End Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX) (Default: Priority 12)	Transmit end interrupt priority selection
Error Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX) (Default: Priority 12)	Error interrupt priority selection



Note: The example values and defaults are for a project using the Synergy S7G2 MCU. Other MCUs may have different default values and available configuration settings.

**Table 8. Configuration Settings for the Transfer Driver on r\_dtc Event SCI0 TXI**

ISDE Property	Value	Description
Parameter Checking	BSP, Enabled, Disabled (Default: BSP)	Selects if code for parameter checking is to be included in the build
Software Start	Enabled, Disabled (Default: Disabled)	Software start selection.
Linker section to keep DTC vector table	.ssp_dtc_vector_table	Linker section to keep DTC vector table.
Name	g_transfer0	Module name
Mode	Normal	Mode selection
Transfer Size	1 Byte	Transfer size selection
Destination Address Mode	Fixed	Destination address mode selection
Source Address Mode	Incremented	Source address mode selection
Repeat Area (Unused in Normal Mode)	Source	Repeat area selection
Interrupt Frequency	After all transfers have completed	Interrupt frequency selection
Destination Pointer	NULL	Destination pointer selection
Source Pointer	NULL	Source pointer selection
Number of Transfers	0	Number of transfers selection
Number of Blocks (Valid only in Block Mode)	0	Number of blocks selection
Activation Source (Must enable IRQ)	Event SCI0 TXI	Activation source selection
Auto Enable	FALSE	Auto enable selection
Callback (Only valid with Software start)	NULL	Callback selection
ELC Software Event Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX), Disabled (Default: Disabled)	ELC software event interrupt priority selection.

Note: The example values and defaults are for a project using the Synergy S7G2 MCU. Other MCUs may have different default values and available configuration settings.

**Table 9. Configuration Settings for the Transfer Driver on r\_dtc Event SCI0 RXI**

ISDE Property	Value	Description
Parameter Checking	BSP, Enabled, Disabled (Default: BSP)	Selects if code for parameter checking is to be included in the build
Software Start	Enabled, Disabled (Default: Disabled)	Software start selection
Linker section to keep DTC vector table	.ssp_dtc_vector_table	Linker section to keep DTC vector table.
Name	g_transfer1	Module name
Mode	Normal	Mode selection
Transfer Size	1 Byte	Transfer size selection
Destination Address Mode	Incremented	Destination address mode selection
Source Address Mode	Fixed	Source address mode selection
Repeat Area (Unused in Normal Mode)	Destination	Repeat area selection

ISDE Property	Value	Description
Interrupt Frequency	After all transfers have completed	Interrupt frequency selection
Destination Pointer	NULL	Destination pointer selection
Source Pointer	NULL	Source pointer selection
Number of Transfers	0	Number of transfers selection
Number of Blocks (Valid only in Block Mode)	0	Number of blocks selection
Activation Source (Must enable IRQ)	Event SCI0 RXI	Activation source selection
Auto Enable	FALSE	Auto enable selection
Callback (Only valid with Software start)	NULL	Callback selection
ELC Software Event Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX), Disabled (Default: Disabled)	ELC software event interrupt priority selection

Note: The example values and defaults are for a project using the Synergy S7G2 MCU. Other MCUs may have different default values and available configuration settings.

**Table 10. Configuration Settings for the Transfer Driver on r\_dtc Event IIC0 TXI**

ISDE Property	Value	Description
Parameter Checking	BSP, Enabled, Disabled (Default: BSP)	Selects if code for parameter checking is to be included in the build
Software Start	Enabled, Disabled (Default: Disabled)	Software start selection
Linker section to keep DTC vector table	.ssp_dtc_vector_table	Linker section to keep DTC vector table
Name	g_transfer0	Module name
Mode	Normal	Mode selection
Transfer Size	1 Byte	Transfer size selection
Destination Address Mode	Fixed	Destination address mode selection
Source Address Mode	Incremented	Source address mode selection
Repeat Area (Unused in Normal Mode)	Source	Repeat area selection
Interrupt Frequency	After all transfers have completed	Interrupt frequency selection
Destination Pointer	NULL	Destination pointer selection
Source Pointer	NULL	Source pointer selection
Number of Transfers	0	Number of transfers selection
Number of Blocks (Valid only in Block Mode)	0	Number of blocks selection
Activation Source (Must enable IRQ)	Event IIC0 TXI	Activation source selection
Auto Enable	FALSE	Auto enable selection
Callback (Only valid with Software start)	NULL	Callback selection
ELC Software Event Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX), Disabled (Default: Disabled)	ELC software event interrupt priority selection

**Table 11. Configuration Settings for the Transfer Driver on r\_dtc Event IIC0 RXI**

ISDE Property	Value	Description
Parameter Checking	BSP, Enabled, Disabled Default: BSP	Selects if code for parameter checking is to be included in the build
Software Start	Enabled, Disabled Default: Disabled	Software start selection.
Linker section to keep DTC vector table	.ssp_dtc_vector_table	Linker section to keep DTC vector table.
Name	g_transfer1	Module name
Mode	Normal	Mode selection
Transfer Size	1 Byte	Transfer size selection
Destination Address Mode	Incremented	Destination address mode selection
Source Address Mode	Fixed	Source address mode selection
Repeat Area (Unused in Normal Mode)	Destination	Repeat area selection
Interrupt Frequency	After all transfers have completed	Interrupt frequency selection
Destination Pointer	NULL	Destination pointer selection
Source Pointer	NULL	Source pointer selection
Number of Transfers	0	Number of transfers selection
Number of Blocks (Valid only in Block Mode)	0	Number of blocks selection
Activation Source (Must enable IRQ)	Event IIC0 RXI	Activation source selection
Auto Enable	FALSE	Auto enable selection
Callback (Only valid with Software start)	NULL	Callback selection
ELC Software Event Interrupt Priority	Priority 0 (highest), Priority 1:14, Priority 15 (lowest - not valid if using ThreadX), Disabled (Default: Disabled)	ELC software event interrupt priority selection.

Note: The example values and defaults are for a project using the Synergy S7G2 MCU. Other MCUs may have different default values and available configuration settings.

## 5.2 I<sup>2</sup>C Framework Module Clock Configuration

The SCI peripheral module uses PCLKB as its clock source. The PCLKB frequency is set using the SSP configurator clock tab prior to a build or by using the CGC Interface at run-time. During configuration, the I<sup>2</sup>C transfer rate is calculated and set internally by the driver, based on the user-selected PCLKB rate and the user-selected transfer rate. If the PCLKB is configured in such a manner that the user-selected rate cannot be achieved, an error is returned when initializing the driver.

### 5.3 I<sup>2</sup>C Framework Module Pin Configuration

The SCI and I<sup>2</sup>C peripheral module uses pins on the MCU to communicate to external devices. I/O pins must be selected and configured as required by the external device. The following table illustrates the method for selecting the pins within the SSP configuration window and the subsequent table illustrates an example selection for the pins.

Note: For some peripherals, the operation-mode selection determines what peripheral signals are available and what MCU pins are required.

**Table 12. Pin Selection Sequence for SCI0**

Resource	ISDE Tab	Pin Selection Sequence
SCI	Pins	Select <b>Peripherals &gt; Connectivity: SCI &gt; SCI0</b>

Note: The selection sequence assumes SCI0 is the desired hardware target for the driver.

**Table 13. Pin Configuration Settings for the I<sup>2</sup>C Framework Module on SCI**

Pin Configuration Property	Value	Description
Pin Group Selection	Mixed, _A only, _B only (Default: Mixed)	Pin group selection
Operation Mode	Disabled, Custom, Asynchronous UART, Simple SPI, Simple I <sup>2</sup> C, Synchronous UART, Smart Card (Default: Simple SPI)	Select Simple I <sup>2</sup> C as the Operation Mode for I <sup>2</sup> C on SCI
SDA	None, P411, P101 (Default: None)	SDA Pin
SCL	None, P410, P100 (Default: None)	SCL Pin

Note: The example values are for a project using the Synergy S7G2 MCU and the SK-S7G2 Kit. Other Synergy Kits and other Synergy MCUs may have different available pin configuration settings.

**Table 14. Pin Selection Sequence for IIC1**

Resource	ISDE Tab	Pin Selection Sequence
IIC	Pins	Select <b>Peripherals &gt; Connectivity: IIC &gt; IIC1</b>

Note: The selection sequence assumes IIC1 is the desired hardware target for the driver.

**Table 15. Pin Configuration Settings for the I<sup>2</sup>C Framework Module on IIC**

Pin Configuration Property	Value	Description
Pin Group Selection	Mixed, _A only, _B only (Default: _A only)	Pin group selection
Operation Mode	Disabled, Enabled (Default: Disabled)	Select operation mode
SDA	None, P206, P101 (Default: None)	SDA Pin
SCL	None, P205, P100 (Default: None)	SCL Pin

Note: The example values are for a project using the Synergy S7G2 MCU and the SK-S7G2 Kit. Other Synergy Kits and other Synergy MCUs may have different available pin configuration settings.

### 5.4 I<sup>2</sup>C Framework Module Other Settings

In addition to the SCL and SDA pins, an I<sup>2</sup>C RESET signal may be required to reset the I<sup>2</sup>C slave device. If this is the case, the RESET signal can be added using a GPIO pin and must be controlled directly by the application program. The external device reset function is not supported within the `r_sci_i2c` or `r_iic` module.

## 6. Using the I<sup>2</sup>C Framework Module in an Application

A common application for the I<sup>2</sup>C framework module requires multiple slave devices on a single bus. The implementation for this common application is described below. (For an application where multiple busses are required, just duplicate the single bus example as needed for each separate bus.)

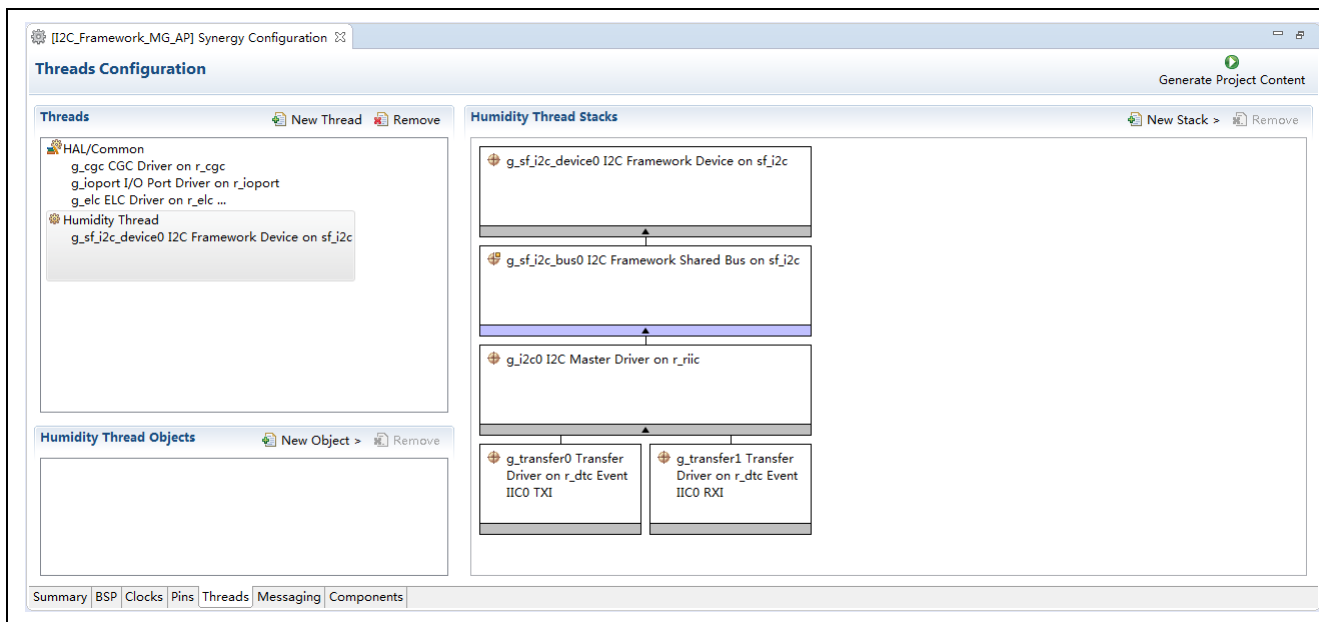
### 6.1 Implementation Steps for Two Slave Devices on the Same Shared Bus

When using the I<sup>2</sup>C framework module to create a single bus with multiple slave devices create two thread stacks each with an I<sup>2</sup>C framework instances. These instances each use the same shared bus instance and the following steps instruct you in how to do this within the SSP Configurator.

Notes:

1. The following example puts both `sf_i2c` module instances in the same thread. If the bus locking function is needed, the `sf_i2c` modules should be put in different threads. Locking applies to all the devices within the locked thread.
2. The following steps assume some familiarity with the use of the SSP development environment. If any of the following steps are confusing, read over the first few chapters of the SSP User's Manual to become familiar with the SSP development environment.
3. Add the first I<sup>2</sup>C framework device to a new or existing thread. This creates the I<sup>2</sup>C master stack. A shared bus on `sf_i2c` is added along with the I<sup>2</sup>C driver. The I<sup>2</sup>C driver can be selected for implementation on `r_iic` or `r_sci_i2c`. The DTC transfer driver is also added by default. This can be removed if the CPU transfer mode is needed instead.

The resulting module stack is shown in the following figure. Example configuration settings are given in the tables that follow the figure:



**Figure 3. Example module stack — both `sf_i2c` module instances in the same thread**

The following table lists an example configuration setting for the first thread and thread stack.

**Table 16. Special Configuration Settings for Example Thread 1 (Slave #1)**

Property	Value	Description
Parameter Checking	Default (BSP)	Enable or Disable Parameter Checking.
Name	g_sf_i2c_device0	Give a name to identify the I <sup>2</sup> C Framework device. API, Config and Control instances will be created based on this name.
Slave Address	0x48	Specify the address of I <sup>2</sup> C slave 1.
Address Mode	7-bit	Select the I <sup>2</sup> C address mode.

**Table 17. Configuration Settings for the I<sup>2</sup>C Framework Shared Bus on sf\_i2c**

Property	Value	Description
Name	g_sf_i2c_bus0	Give a name to identify the I <sup>2</sup> C Framework shared bus. This shared bus is shared by multiple I <sup>2</sup> C Framework Devices.

**Table 18. Configuration Settings for the I<sup>2</sup>C Master Driver on r\_riic**

Property	Value	Description
Parameter Checking	Default (BSP)	Enable or Disable Parameter Checking.
Name	g_i2c0	Give a name to identify the I <sup>2</sup> C Driver device. This will be used internally by Framework.
Channel	0	Specify the I <sup>2</sup> C channel.
Rate	Standard	Select the speed of the I <sup>2</sup> C bus.
Slave Address	0	This field will be locked as slave address already configured in the I <sup>2</sup> C Framework Device on sf_i2c.
Address Mode	7-bit	This field will be locked as address mode already configured in the I <sup>2</sup> C Framework Device on sf_i2c.
Timeout Mode	Short Mode	Select Timeout mode: Short mode or Long mode
Callback	NULL	This field will be locked as Framework does not provide callback handling to the user.
Receive Interrupt Priority	Priority 2	Receive interrupt priority selection.
Transmit Interrupt Priority	Priority 2	Transmit interrupt priority selection.
Transmit End Interrupt Priority	Priority 2	Transmit end interrupt priority selection.
Error Interrupt Priority	Priority 2	Error interrupt priority selection.

**Table 19. Configuration Settings for the I<sup>2</sup>C Master Driver on r\_sci\_i2c**

Property	Value	Description
Parameter Checking	Default(BSP)	Enable Or Disable Parameter Checking.
Name	g_i2c0	Give a name to identify the I <sup>2</sup> C Driver device. This will be used by Framework internally.
Channel	0	Specify the address of I <sup>2</sup> C slave.
Rate	Standard	Select the speed of the I <sup>2</sup> C bus.
Slave Address	0	This field will be locked as slave address already configured in the I <sup>2</sup> C Framework Device on sf_i2c.
Address Mode	7-bit	This field will be locked as address mode already configured in the I <sup>2</sup> C Framework Device on sf_i2c.
Slave Output Delay	300	SDA output delay in nanoseconds.
Bit Rate Modulation Enable	Enable	Enables bitrate modulation function.
Callback	NULL	This field will be locked as Framework does not provide callback handling to the user.

Property	Value	Description
Receive Interrupt Priority	Priority 2	Receive interrupt priority selection.
Transmit Interrupt Priority	Priority 2	Transmit interrupt priority selection.
Transmit End Interrupt Priority	Priority 2	Transmit end interrupt priority selection.

**Table 20. Configuration Settings for the Transfer Driver on r\_dtc Event IIC0 TXI**

Property	Value	Description
Parameter Checking	Default (BSP)	Enable Or Disable Parameter Checking.
Software Start	Disabled	
Linker section to keep DTC vector table	.ssp_dtc_vector_table	Linker section to keep DTC vector table.
Name	g_transfer0	Module name.
Mode	Normal	Mode selection. This field is locked.
Transfer Size	1 Byte	Transfer size selection. This field is locked.
Destination Address Mode	Fixed	Destination address mode selection. This field is locked.
Source Address Mode	Incremented	Source address mode selection. This field is locked.
Repeat Area (Unused in Normal Mode)	Source	Repeat area selection. This field is locked.
Interrupt Frequency	After all transfers have completed	This field is locked.
Destination Pointer	NULL	Destination pointer selection. This field is locked.
Source Pointer	NULL	Source pointer selection. This field is locked.
Number of Transfers	0	Number of transfer selection. This field is locked.
Number of Blocks (Valid only in Block Mode)	0	Number of blocks selection. This field is locked.
Activation Source (Must enable IRQ)	Event IIC0 TXI	Activation source selection. This field is locked.
Auto Enable	False	Auto enable selection. This field is locked.
Callback (Only valid with Software start)	NULL	Callback selection. This field is locked.
ELC Software Event Interrupt Priority	Disabled	ELC software event interrupt priority selection.

**Table 21. Configuration Settings for the Transfer Driver on r\_dtc Event IIC0 RXI**

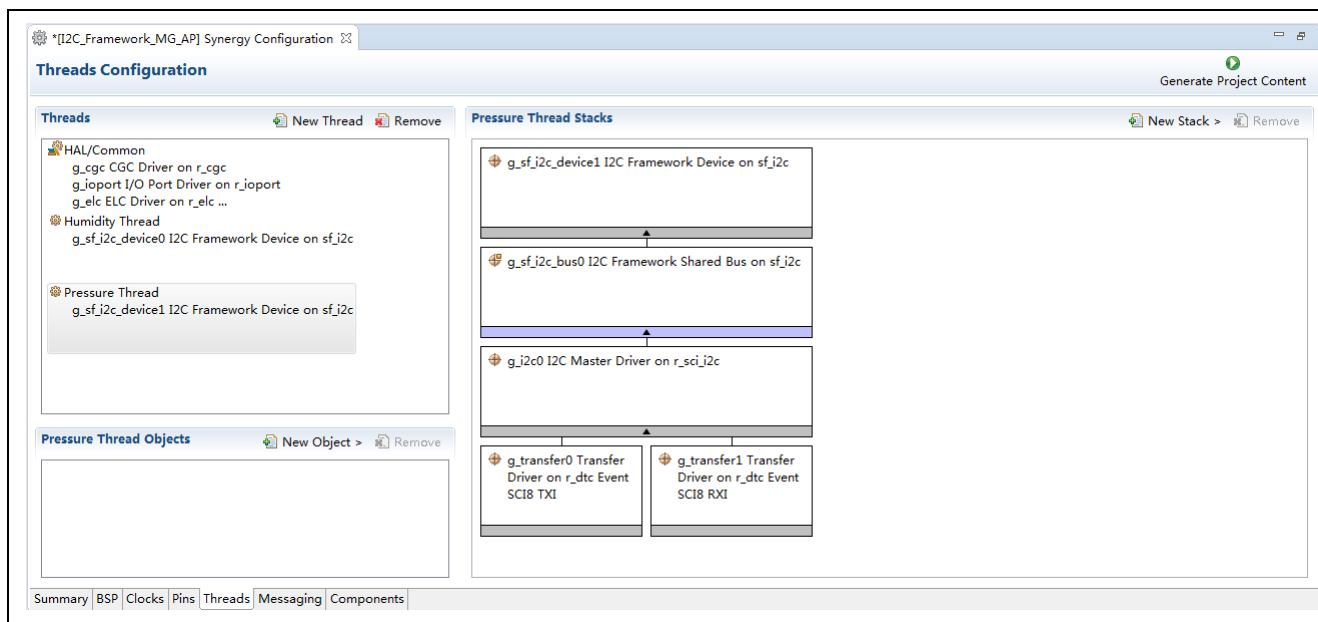
Property	Value	Description
Parameter Checking	Default(BSP)	Enable or Disable Parameter Checking.
Software Start	Disabled	Software Start Selection.
Linker section to keep DTC vector table	.ssp_dtc_vector_table	Linker section to keep DTC vector table.
Name	g_transfer0	Module name.
Mode	Normal	Mode selection. This field is locked.
Transfer Size	1 Byte	Transfer size selection. This field is locked.
Destination Address Mode	Incremented	Destination address mode selection. This field is locked.
Source Address Mode	Fixed	Source address mode selection. This field is locked.
Repeat Area (Unused in Normal Mode)	Destination	Repeat area selection. This field is locked.



Property	Value	Description
Interrupt Frequency	After all transfers have completed	This field is locked.
Destination Pointer	NULL	Destination pointer selection. This field is locked.
Source Pointer	NULL	Source pointer selection. This field is locked.
Number of Transfers	0	Number of transfer selection. This field is locked.
Number of Blocks (Valid only in Block Mode)	0	Number of blocks selection. This field is locked.
Activation Source (Must enable IRQ)	Event IIC0 RXI	Activation source selection. This field is locked.
Auto Enable	False	Auto enable selection. This field is locked.
Callback (Only valid with Software start)	NULL	Callback selection. This field is locked.
ELC Software Event Interrupt Priority	Disabled	ELC software event interrupt priority selection.

Add the second I<sup>2</sup>C Framework Device to the second thread. The I<sup>2</sup>C Framework Shared Bus on sf\_i2c is not added automatically. Select the option to use the existing shared bus. The Configurator then automatically adds the I<sup>2</sup>C Framework Shared Bus on sf\_i2c and remaining modules. The lower level modules are added and configured automatically to be consistent with the previously defined settings from the first I<sup>2</sup>C framework instance. In fact, if any lower level settings are changed in on stack, they are automatically updated in the other.

The resulting module stack is shown in the following figure:



**Figure 4. Example module stack — Second I<sup>2</sup>C Framework Device on second thread**

The following table lists the example configuration setting for the second thread and thread stack.

**Table 22. Special Configuration Settings for Example Thread 2 (Slave #2)**

Property	Value	Description
Parameter Checking	Default(BSP)	Enable Or Disable Parameter Checking
Name	g_sf_i2c_device1	Give a name to identify the I <sup>2</sup> C Framework device. API, Config and Control instances will be created based on this name.
Slave Address	0x28	Specify the address of I <sup>2</sup> C slave2
Address Mode	7-bit	Select the I <sup>2</sup> C address mode.

Notes:

1. The listed configuration settings can be repeated to add more slave devices to the same bus — if they share the same low-level settings.
2. Because existing shared bus of I<sup>2</sup>C Framework Shared Bus on sf\_i2c is used, the setting for lower level modules is the same as the first thread. No additional settings are needed.
3. If a set of slave devices have different lower-level settings than another set, they must use a different bus and can be implemented by repeating the two steps outlined above — defining a different bus and the different lower-level characteristics for the set of slave devices.

### 6.2 Adding Another Shared Bus

The I<sup>2</sup>C framework module which will use a second shared bus can be added to any thread. Starting with the previous example, if it is added to the I<sup>2</sup>C\_Device1 thread, then the module stack would appear (see figure). Available options for the shared bus are New or Use.

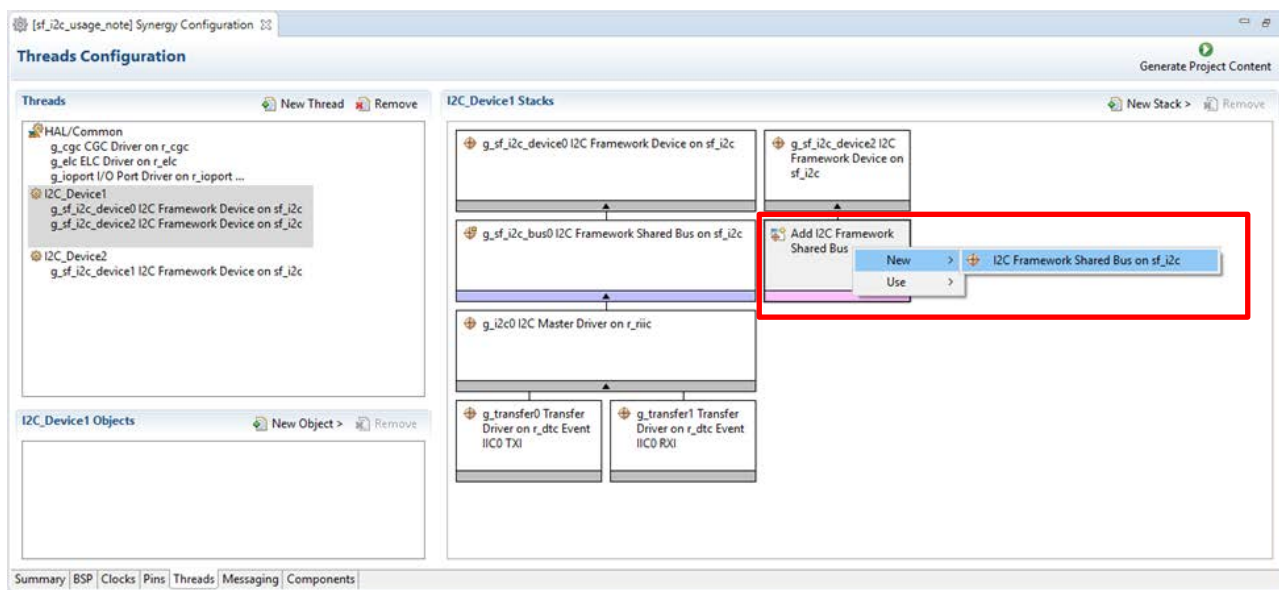


Figure 5. Example module stack — adding another shared bus

Select **New** to add another I<sup>2</sup>C Framework Shared Bus on sf\_i2c module. Configure the shared bus properties as needed for the application. Select the desired low-level I<sup>2</sup>C driver. The channel number for the g\_i2c1 I<sup>2</sup>C driver module, must be different from the channel number for the g\_i2c0 I<sup>2</sup>C driver module. The following figure shows the resulting thread stack:

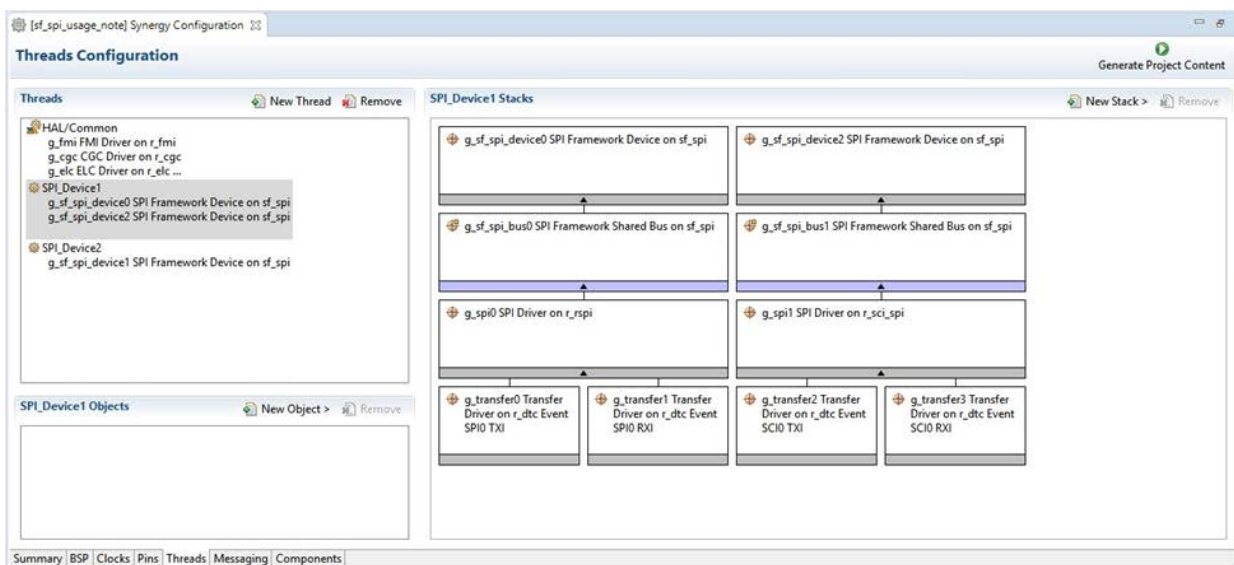


Figure 6. Example thread stack on I<sup>2</sup>C Framework Shared Bus

A second device can be added in the I2C\_Device2 thread using the same information provided in this section. The following figure shows the resulting thread stack:

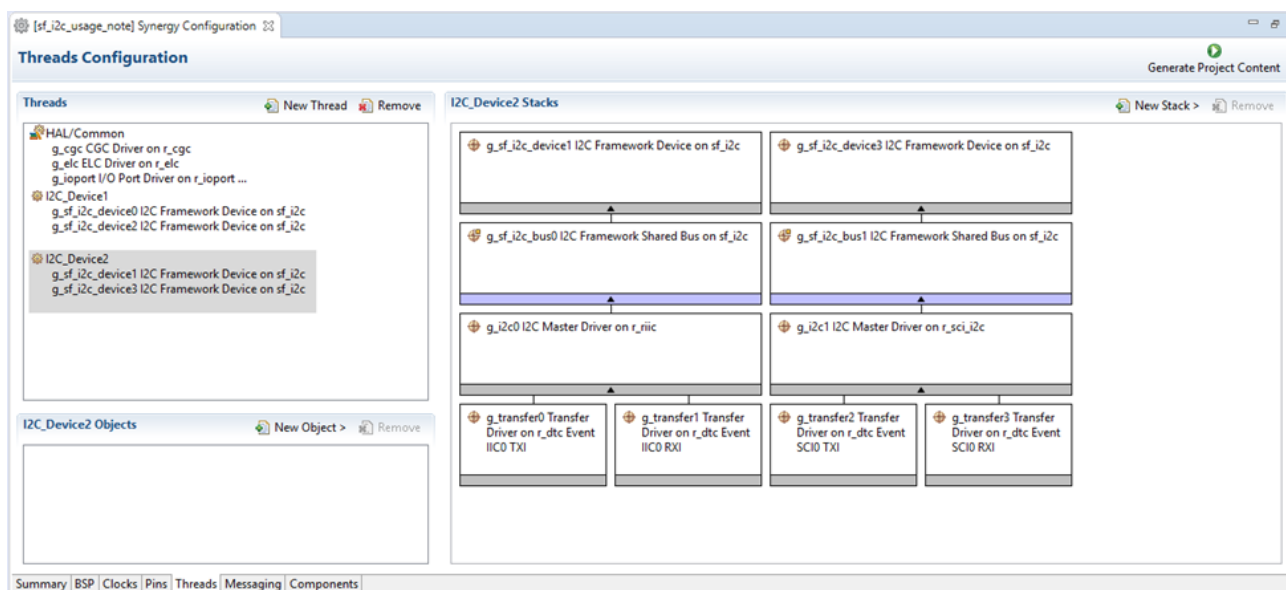


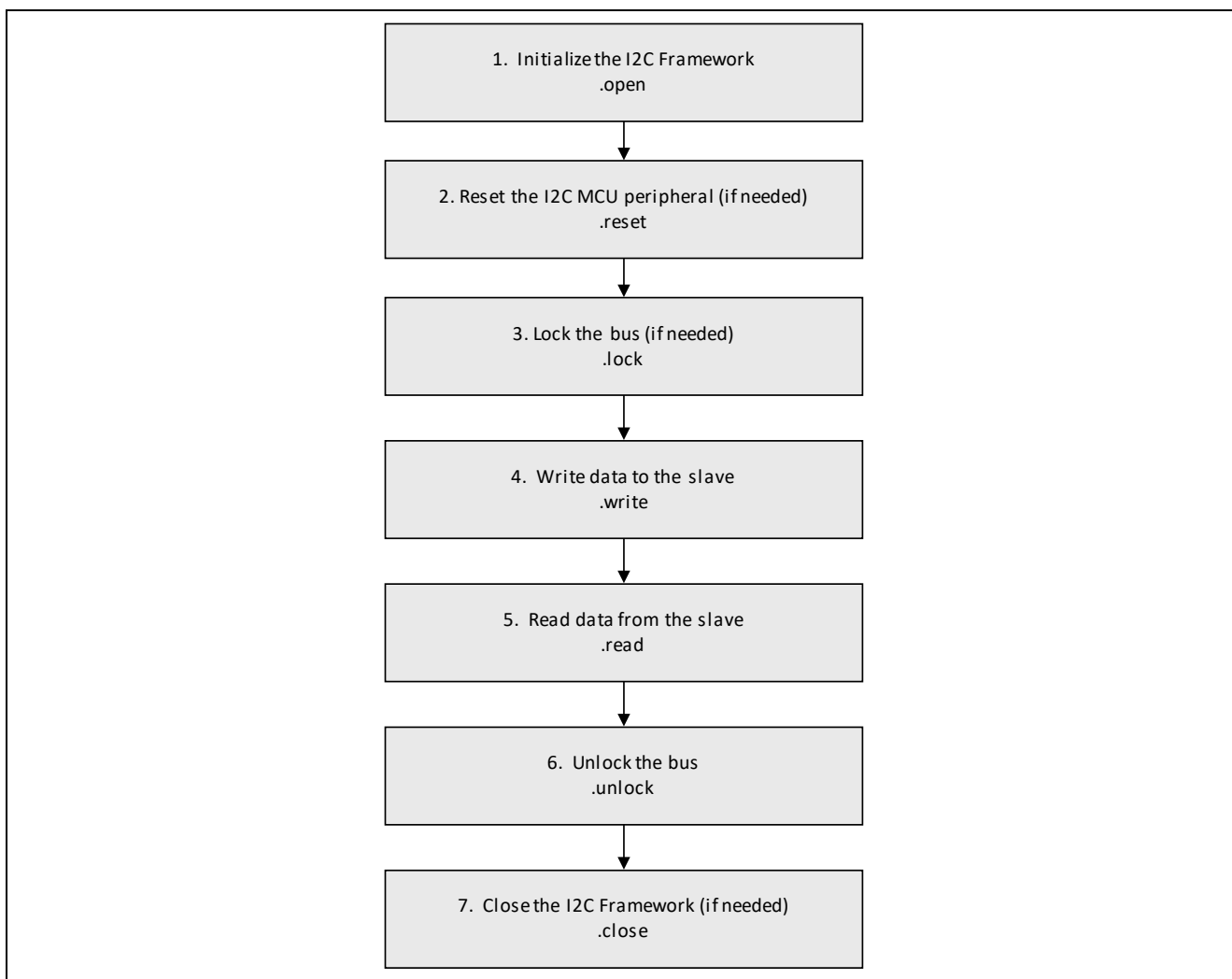
Figure 7. Example thread stack — adding a second device

### 6.3 The typical steps in using the Framework module in an application

The typical steps in using the I<sup>2</sup>C Framework module in an application are:

1. Initialize the I<sup>2</sup>C Framework module using the open API. Each I<sup>2</sup>C framework module needs to call the open API at least once before performing any operations on the bus.
2. Reset the I<sup>2</sup>C MCU peripheral using the reset API — if needed.
3. Lock the bus using the lock or lockWait API for a framework module. Once the bus is locked by an I<sup>2</sup>C framework module it cannot be used by any other I<sup>2</sup>C framework module on the same bus. Locking ensures ownership of the bus remains with the I<sup>2</sup>C framework module until it is unlocked. Operations initiated from other I<sup>2</sup>C framework modules on the bus will fail while the bus is locked. It is not mandatory to lock the bus before any read/write operations on the bus — locking is optional, if needed. If thread is not supposed to wait forever when locking the I<sup>2</sup>C bus, call the lockWait API with desired timeout value.
4. Write data to the slave using the write API. The write operation will not be successful if the bus is already locked by any other I<sup>2</sup>C framework module.
5. Read data from the slave using the read API. The read operation will not be successful if the bus is already locked by any other I<sup>2</sup>C framework module.
6. Unlock the bus using the unlock API if it is already locked by the same I<sup>2</sup>C framework module. Once the bus is unlocked, other I<sup>2</sup>C framework modules can use it. It is necessary to unlock the locked bus after the protected read or write operations are over. (if needed)
7. Close the I<sup>2</sup>C framework module using the close API. Each I<sup>2</sup>C framework module can call the close API after all its read and write operations on the bus are completed — if needed.

The following diagram illustrates common steps in a typical operational flow:



**Figure 8. Flow Diagram of a Typical I<sup>2</sup>C Framework Module Application**

## 7. The I<sup>2</sup>C Framework Module Application Project

The application project associated with this module guide demonstrates a typical example. You may want to import and open the application project within the ISDE and view the configuration settings for the I<sup>2</sup>C Framework module. You can also read over the code (in `humidity_thread_entry.c` and `pressure_thread_entry.c`) that is used to illustrate the I<sup>2</sup>C Framework module APIs in a complete design.

The application project demonstrates the typical use of the I<sup>2</sup>C Framework module APIs. The application project creates two threads, one is for sampling humidity value from slave device 0, and the other is the sampling pressure value from slave device 1. These two devices use the same low-level layer that consists of the I<sup>2</sup>C Framework shared bus and the I<sup>2</sup>C Master Driver. The difference between these two devices is slave address that is set in properties of I<sup>2</sup>C Framework Device. Each device access starts with read or write APIs with particular slave address.

**Table 23. Software and Hardware Resources Used by the Application Project**

Resource	Revision	Description
e <sup>2</sup> studio	v6.2.1	Integrated Solution Development Environment
SSP	v1.5.0	Synergy Software Platform
IAR EW for Renesas Synergy	v8.23.1	IAR Embedded Workbench® for Renesas Synergy™
SSC	v6.2.1	Synergy Standalone Configurator



Resource	Revision	Description
SK-S7G2	v3.0, v3.1, v3.3	Starter Kit
Sensor Shield board (ZR-9500-ASY)	REV B	Accessory in PK-Cloud1

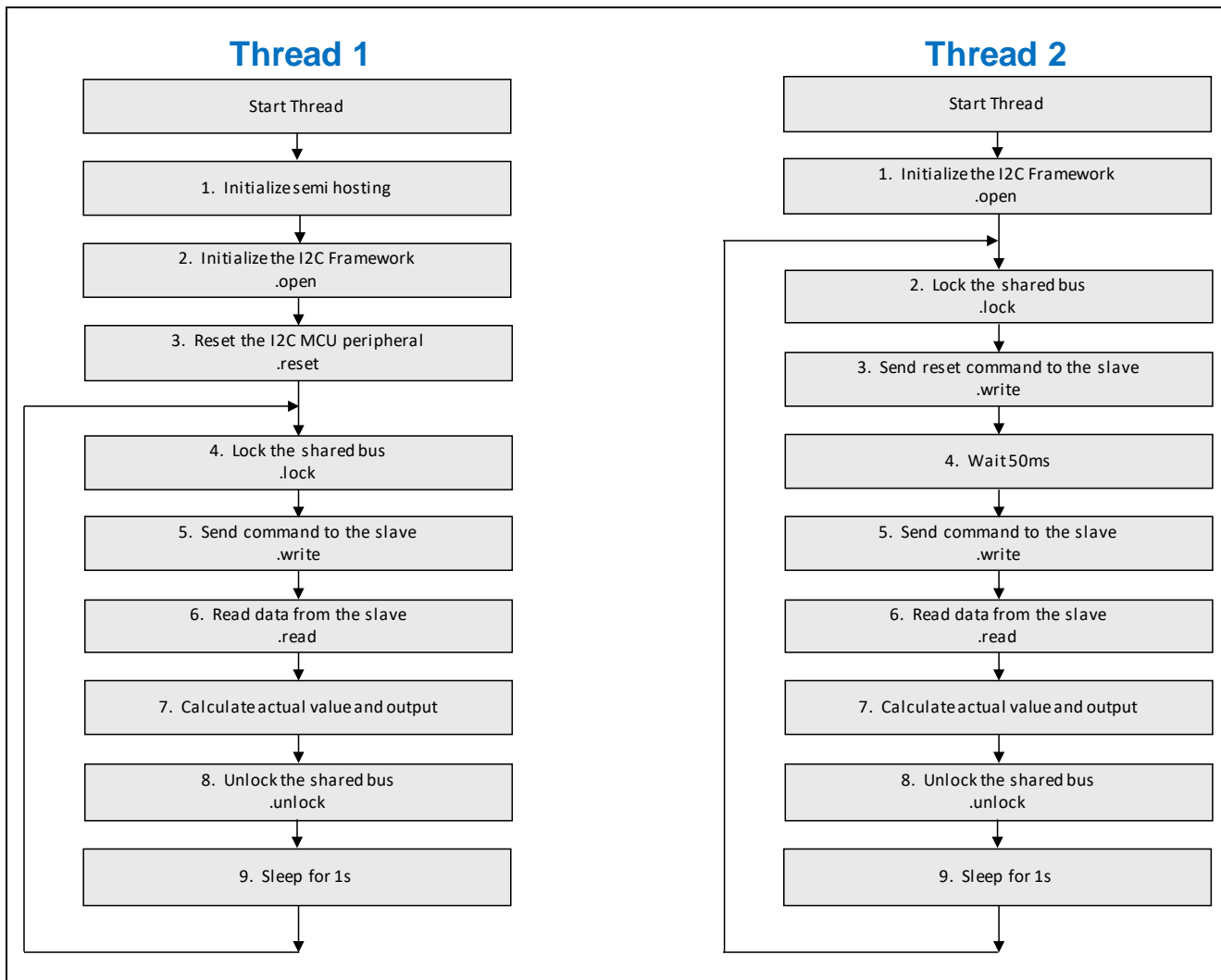
Note: The application project assumes that you are familiar with using printf(), semi-hosting, and the Debug Console in your ISDE. If you are unfamiliar with these techniques, refer to the “How do I Use Printf() with the Debug Console in the Synergy Software Package” Knowledge Base article given in the References section at the end of this document.

The following figure shows an actual hardware connection:



Note: Channel 8 I<sup>2</sup>C signals can also be led out to connect with a specified sensor board using the Dupont Line.

The following diagram shows a simple flow diagram of the application project:



**Figure 10. I<sup>2</sup>C Framework Module Application Project Flow Diagram**

The configuration settings in the application project need to be customized for the specifics of the target kit and MCU. The I<sup>2</sup>C Framework module in the application project uses the I<sup>2</sup>C Framework which uses the I<sup>2</sup>C shared bus and the I<sup>2</sup>C master driver as low-level support. I<sup>2</sup>C master driver is based on the `r_sci_i2c` module and uses channel 8 for I<sup>2</sup>C communication. The output pins for I<sup>2</sup>C communication are selected to conform with the signal connections from the slave device. These are P104 for SCL and P105 for SDA. It can be helpful to open the application project in the ISDE and locate these settings in the Pin configuration tab. These signals can also be located on the schematic for the SK-S7G2 as a check on the validity of the selected pins for the I<sup>2</sup>C signals. The I<sup>2</sup>C signals on Arduino Shield Uno interface are used to connect with an external sensor shield board which is plugged into. This external sensor shield board is an accessory of PK-Cloud1. It can also be replaced with any board customized by the user if the humidity and temperature sensor HIH6030 and barometric pressure sensor MS5637 are designed on the board. The location for the schematic is provided in the References section at the end of this document.

See section 6.1 for the application project configuration setting of two threads and threads stack. The following table list port configuration settings for I<sup>2</sup>C interface:

**Table 24. Pin Configuration Settings for SCI channel 8**

Pin Configuration Property	Setting
Pin Group Selection	A only

Pin Configuration Property	Setting
Operation Mode	Simple I <sup>2</sup> C
SDA	P105
SCL	P104

Once the I<sup>2</sup>C Framework application project has been successfully added and configured, it can be used by the application program. The I<sup>2</sup>C Framework application project implements steps in a similar manner to those shown for the general case as described in the preceding chapter. There is a key point of read and write function that is the restart parameter deciding to send slave address or not.

Refer to the code in `humidity_thread_entry.c` and `pressure_thread_entry.c`, as you follow along with the flow outlined in the previous Project Flow Diagram. The first section of the `humidity_thread_entry.c` has the header files which are referred to in the I<sup>2</sup>C instance structure to allow semi-hosting support to display results using `printf()`. The following section calls `open` and `reset` APIs for initializing I<sup>2</sup>C framework device, then enter an infinite loop for getting raw data from slave device. In this infinite loop, before using `write` and `read` APIs, `lock` API is called first to prevent the shared bus from use of other I<sup>2</sup>C framework device. Locking successfully leads to use of `write` and `read` APIs without interrupt. After getting raw data, calculate actual value and output it on Debug Console using `printf()`. The second thread has almost the same process of the first thread except call of `reset` API, because resetting the I<sup>2</sup>C MCU peripheral has been done by the first thread.

Note: It is assumed that you are familiar with using `printf()` and the Debug Console in the Synergy Software Package. If you are unfamiliar with this, refer to the “*How do I Use Printf() with the Debug Console in the Synergy Software Package*” Knowledge Base article, available as described in the References section at the end of this document. Alternatively, you can see results using the watch variables in the debug mode.

A few key properties are configured in this application project to support the required operations and the physical properties of the target board and MCU. The properties with the values set for this specific project are listed in the following table. You can also open the application project and view these settings in the Properties window as a hands-on exercise.

## 8. Customizing the I<sup>2</sup>C Framework Module for a Target Application

Some configuration settings are normally changed by the developer from those shown in the application project. For example, the user can easily change the configuration settings for the SCI clock by updating the PCLKB in the Clocks tab. The user can also change the slave address in I<sup>2</sup>C Framework Drive to connect with other different sensors. Besides, DTC transfer is an optional item for I<sup>2</sup>C Master Driver, so you can remove the default option from I<sup>2</sup>C master driver according to your application.

## 9. Running the I<sup>2</sup>C Framework Module Application Project

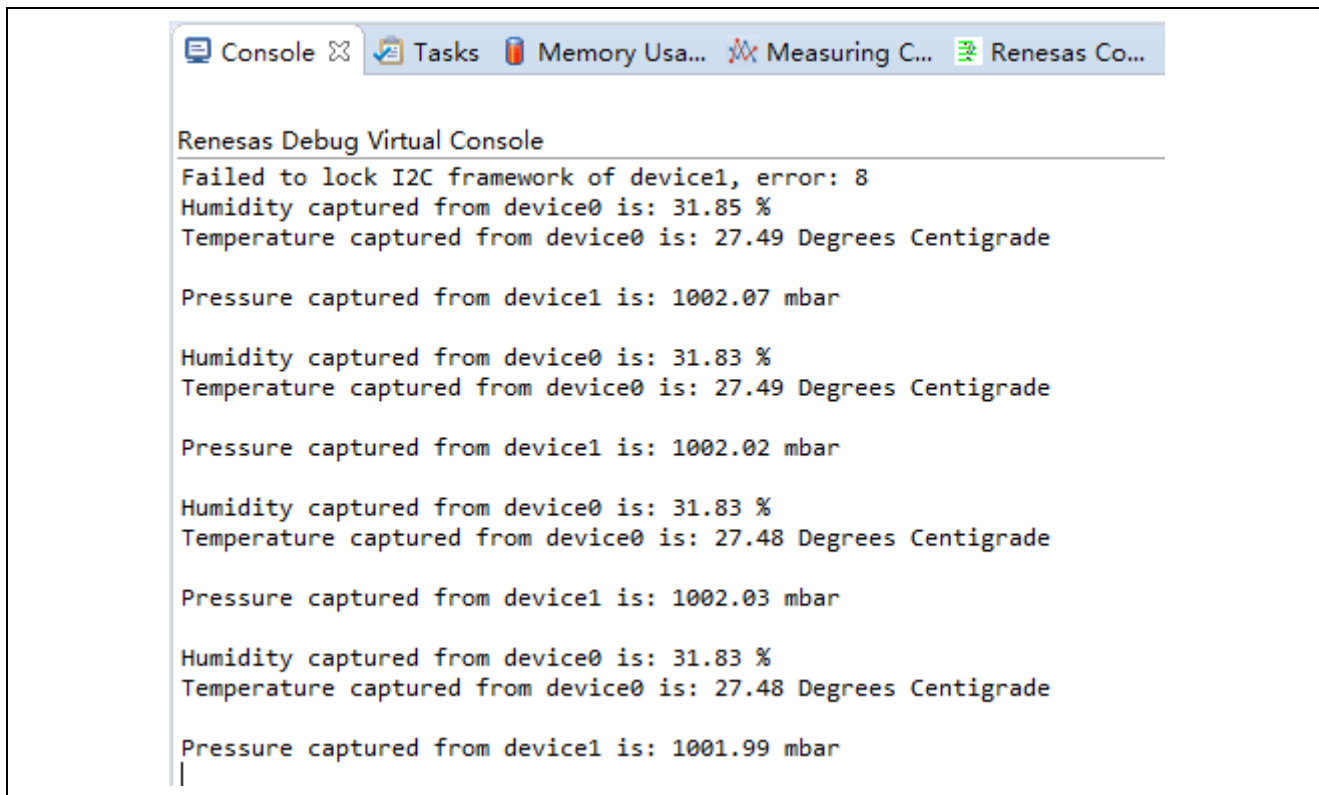
To run the I<sup>2</sup>C Framework application project and to see it executed on a target kit, you can simply import it into your ISDE, compile, and run debug.

Note: The following steps are sufficiently detailed for someone experienced with the basic flow through the Synergy development process. If these steps are unfamiliar, refer to the first few chapters of the *SSP User's Manual* for a description of how to accomplish these steps.

To create and run the application project simply follow these steps:

1. Import and build the example project included with this package according to the *Renesas Synergy™ Project Import Guide* (r11an0023eu0121-synergy-ssp-import-guide.pdf).
2. Plug Sensor Shield board into Arduino interface.
3. Connect to the host PC using the USB cable (use J19 DEBUG\_USB connector).
4. Start to debug the application.
5. The output can be viewed in the Renesas Debug Console.



The image shows a screenshot of the Renesas Debug Virtual Console window. The window title bar includes 'Console', 'Tasks', 'Memory Usa...', 'Measuring C...', and 'Renesas Co...'. The main content area displays the following text:

```
Renesas Debug Virtual Console
Failed to lock I2C framework of device1, error: 8
Humidity captured from device0 is: 31.85 %
Temperature captured from device0 is: 27.49 Degrees Centigrade

Pressure captured from device1 is: 1002.07 mbar

Humidity captured from device0 is: 31.83 %
Temperature captured from device0 is: 27.49 Degrees Centigrade

Pressure captured from device1 is: 1002.02 mbar

Humidity captured from device0 is: 31.83 %
Temperature captured from device0 is: 27.48 Degrees Centigrade

Pressure captured from device1 is: 1002.03 mbar

Humidity captured from device0 is: 31.83 %
Temperature captured from device0 is: 27.48 Degrees Centigrade

Pressure captured from device1 is: 1001.99 mbar
```

**Figure 11. Example Output from I<sup>2</sup>C Framework Application Project**

Notes:

1. Output value may differ based on environment.
2. The first lock failure (Failed to lock I<sup>2</sup>C framework of device1) occurs before unlocking the shared bus of device 0.
3. To output the float value on the Debug Console in e<sup>2</sup> studio, find the project-> **properties** -> **C/C++ Build** -> **Settings** -> **Tool Settings** -> **Cross ARM C Linker** -> **Miscellaneous**, and check the item "Use float with nano printf (-u \_printf\_float)" shown in the following figure shows.
4. For output degree centigrade symbol on Terminal I/O in IAR, in debug status, find **Tools** -> **Options** -> **Terminal I/O** -> **Encoding** -> **Select UTF-8** instead of System which the following figure shows.

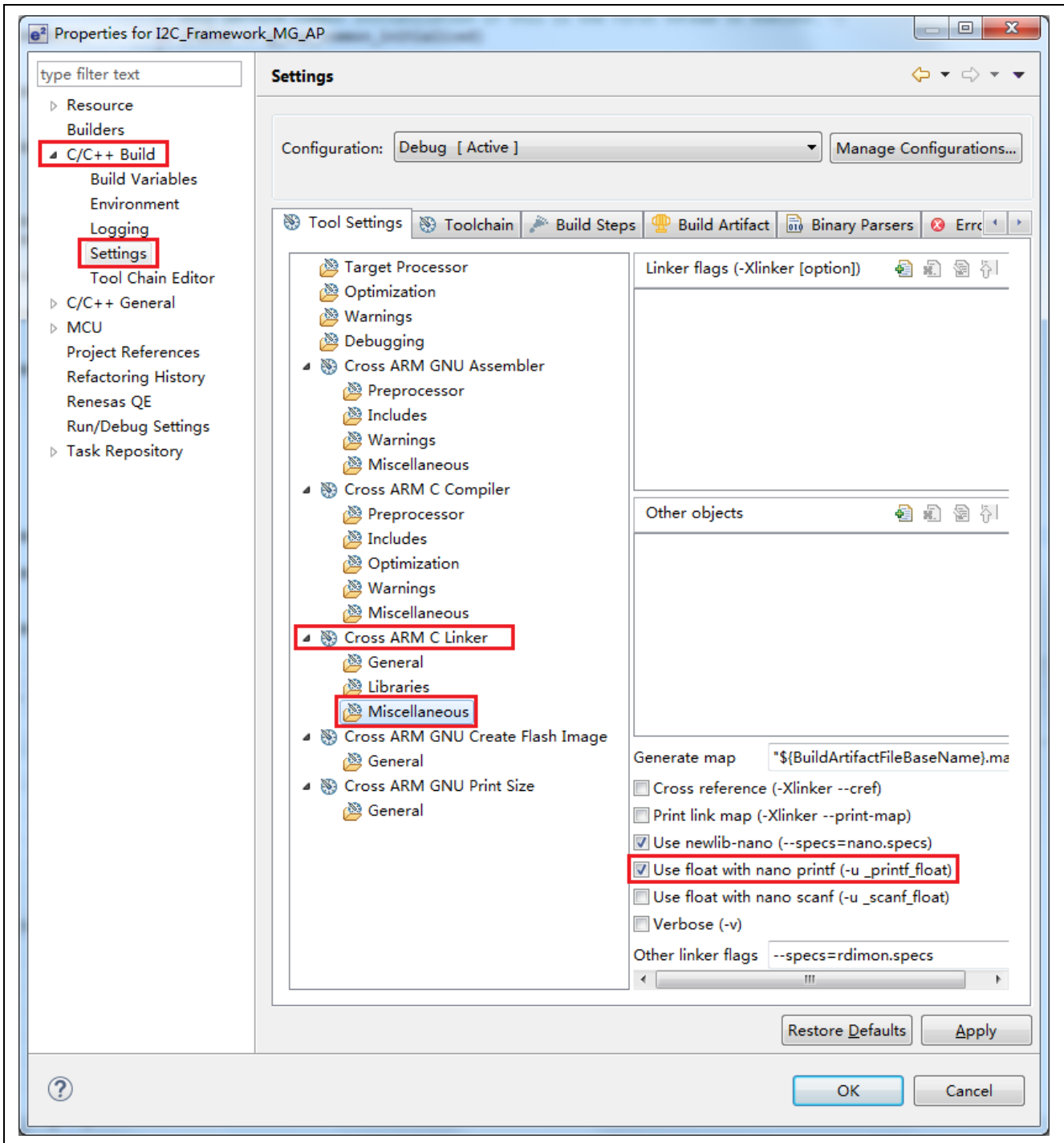


Figure 12. Enable the output of float value using printf() in e<sup>2</sup> studio

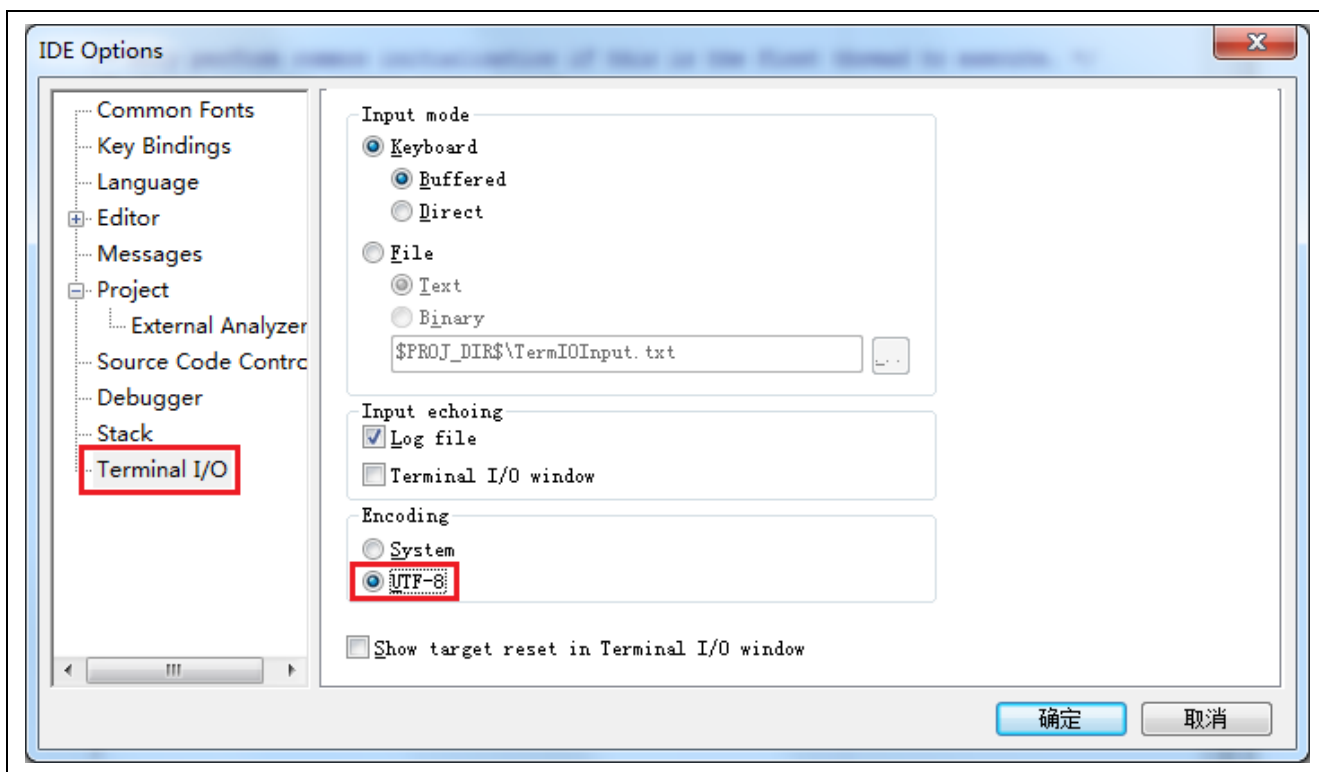


Figure 13. Enable the output of degree centigrade symbol in IAR Terminal I/O

## 10. I<sup>2</sup>C Framework Module Conclusion

This module guide has provided all the background information needed to select, add, configure and use the module in an example project. Many of these steps were time consuming and error-prone activities in previous generations of embedded systems. The Renesas Synergy Platform makes these steps much less time consuming and removes the common errors, like conflicting configuration settings or the incorrect selection of lower-level modules. The use of high-level APIs (as demonstrated in the application project) illustrate additional development time savings by allowing work to begin at a high level, avoiding the time required in older development environments to use, or in some cases, create, lower-level drivers.

## 11. I<sup>2</sup>C Framework Module Next Steps

After you have mastered a simple I<sup>2</sup>C Framework application project, you may want to review a more complex example. The GUIX for the SK-S7G2 application example demonstrates the use of the I<sup>2</sup>C Framework to implement the touch controller. This project is available through the link shown in the References section at the end of this document.

## 12. I<sup>2</sup>C Framework Module Reference Information

*SSP User Manual*: Available in html format in the SSP distribution package and as a pdf from the Synergy Gallery.

Links to all the most up-to-date sf\_i2c module reference materials and resources are available on the Synergy Knowledge Base: [https://en-support.renesas.com/search/sf\\_i2c%20Module%20Guide%20Resources](https://en-support.renesas.com/search/sf_i2c%20Module%20Guide%20Resources).

## Website and Support

Visit the following vanity URLs to learn about key elements of the Synergy Platform, download components and related documentation, and get support.

Synergy Software	<a href="http://www.renesas.com/synergy/software">www.renesas.com/synergy/software</a>
Synergy Software Package	<a href="http://www.renesas.com/synergy/ssp">www.renesas.com/synergy/ssp</a>
Software add-ons	<a href="http://www.renesas.com/synergy/addons">www.renesas.com/synergy/addons</a>
Software glossary	<a href="http://www.renesas.com/synergy/softwareglossary">www.renesas.com/synergy/softwareglossary</a>
Development tools	<a href="http://www.renesas.com/synergy/tools">www.renesas.com/synergy/tools</a>
Synergy Hardware	<a href="http://www.renesas.com/synergy/hardware">www.renesas.com/synergy/hardware</a>
Microcontrollers	<a href="http://www.renesas.com/synergy/mcus">www.renesas.com/synergy/mcus</a>
MCU glossary	<a href="http://www.renesas.com/synergy/mcuglossary">www.renesas.com/synergy/mcuglossary</a>
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**Revision History**

Rev.	Date	Description	
		Page	Summary
1.00	May.15.17	–	Initial Release
1.01	Aug.07.17	16	Update to Hardware and Software Resource table
1.02	Jan.07.19	–	Updated for v1.5.0

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