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April 1st, 2010 Renesas Electronics Corporation

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R8C/Tiny Series

General-purpose Program for Changing Blocks

1. Abstract

This program changes memory contents consisting of the same number of bytes with each other memory location.

2. Introduction

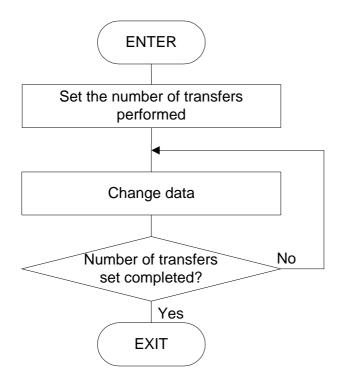
This program changes memory contents consisting of the same number of bytes with each other memory location. An add and conditional branch instruction (ADJNZ) is used to count the number of transfers performed. In this program, memory contents basically are changed in bytes. However, if the memory contents to be changed consist of even bytes, they can be changed in words for increased speed of processing.

| Subroutine name : - | ROM capacity : 17 bytes |
|---------------------------------------|------------------------------|
| Interrupt during execution : Accepted | Number of stacks used : None |

| Register/memory | r/memory Input Out | | Usage condition | |
|---------------------------------------|------------------------|---------------------|---------------------|--|
| R0L | - | Last data of BLOCK2 | Register used for | |
| | | | change | |
| R0H | - | - | Unused | |
| R1 | - | - | Unused | |
| R2 | - | - | Unused | |
| R3 | - | - | Unused | |
| A0 | - | 0000 ₁₆ | Number of transfers | |
| | | | performed | |
| A1 | - | - | Unused | |
| BLOCK1 | Content of BLOCK1 | Content of BLOCK2 | ← | |
| BLOCK2 | Content of BLOCK2 | Content of BLOCK1 | ~ | |
| | | | | |
| Usage precautions | Momery contents are ab | anged in bytee | | |
| Memory contents are changed in bytes. | | | | |



3. Flowchart





4. The example of a reference program

| •************************************* | ***** | ********** | ************ |
|--|----------------|---------------------|--|
| ; | | | * |
| ; M16C Pro | gram Collectic | on No. 3 | * |
| ; CPU | : R8C/Tin | | * |
| | | , | * |
| , .************************************ | ***** | ***** | ******* |
| , VramTOP | .EQU | 000400H | ; USER PROGRAM RAM START ADDRESS |
| VromTOP | .EQU | 00D000H | ; 12Kbyte Flash version |
| | .200 | 0000000 | |
| , | SECTION RA | M,DATA | , |
| .ORG VramTOP | | | ; ; RAM area |
| LENGTH | .EQU | 10 | ; Length of area |
| BLOCK1: | .EQU | LENGTH | ; Area 1 |
| BLOCK2: | .BLKB | LENGTH | ; Area 2 |
| | .DERD | LENGTH | |
| , | | | , |
| : Title: Cha | nging blocks | | |
| | | n units of blocks. | |
| ; Input: | | > Output: | |
| ; R0L() | | R0L (Indeterminate) | |
| ; R0H() | | R0H (Unused) | |
| ; R1L() | | R1L (Unused) | |
| ; R1E() | | R1H (Unused) | |
| ; R2 () | | R2 (Unused) | |
| ; R3 () | | R3 (Unused) | |
| ; A0 () | | A0 (Indeterminate) | |
| ; A1 () | | A1 (Unused) | |
| | ount used: Nor | | |
| : Notes: | | | |
| · | | | |
| , | SECTION PR | ROGRAM,CODE | · · · · |
| | | omTOP | , ; ROM area |
| MOV.B | #LENGTH, | | ; Sets number of transfers performed |
| LOOP: | <i>"</i> , | | |
| MOV.B | BLOCK1-1 | [A0] R0[| , |
| XCHG.B | R0L,BLOC | | , ; Changes data |
| MOV.B R0L,BLOCK1-1[A0] | | | |
| ADJNZ.W | | | , ;> Looped for the number of transfers performed |
| | | , # • | : |
| , F | END | | , |
| | | | 3 |



5. Reference

SOFTWARE MANUAL R8C/Tiny Series SOFTWARE MANUAL (Acquire the most current version from Renesas web-site)

6. Web-site and contact for support

Renesas Web-site

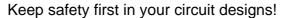
http://www.renesas.com

Contact for Renesas technical support Mail to : <u>support_apl@renesas.com</u>



REVISION HISTORY

| Rev. | Date | Description | |
|------|--------------|-------------|----------------------|
| | | Page | Summary |
| 1.00 | Dec 24, 2003 | - | First edition issued |
| | | | |
| | | | |



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