

輸出令 (export order)	貨物等省令(Ordinance of the Ministry Specifying Goods and Technologies Pursuant to Provisions of the Appended Table 1 of the Export Control Order and the Appended Table of the Foreign Exchange Order)				EU規制番号(EU regulation number)		EU規制文言(英文)(EU regulatory wording)	EU規制の差異注意(要)(Attention to differences in EU regulations (required))	I Area Group A	Ri Area Group B	To(2) Area Group C	Chi Area Group D
	条 (articles)	項 (sections)	号 (issues)	イ出入 号 (Japanese alphabet)	5桁(5 digits)	細番(fine number)						
7	6	1	1				3A001, 3A002, 3A003, 3B001, 3B002, 3C001, 3C002, 3C003, 3C004, 3C005, 3C006		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1				3A001 Electronic items as follows: a. General purpose integrated circuits, as follows: 1. Integrated circuits designed or rated as radiation hardened to withstand any of the following: a. A total dose of 5 x 10 ³ Gy (silicon) or higher; b. A dose rate upset of 5 x 10 ⁶ Gy (silicon)/s or higher; or c. A fluence (integrated flux) of neutrons (1 MeV equivalent) of 5 x 10 ¹³ n/cm ² or higher on silicon, or its equivalent for other materials;		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1				3A001 a 1		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(一)		3A001 a 1 a		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(二)		3A001 a 1 b		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(三)		3A001 a 1 c		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1				3A001 a 2 2. "Microprocessor microcircuits", "microcomputer microcircuits", microcontroller microcircuits, storage integrated circuits manufactured from a compound semiconductor, analogue-to-digital converters, integrated circuits that contain analogue-to-digital converters and store or process the digitised data, digital-to-analogue converters, electro-optical or "optical integrated circuits" designed for "signal processing", field programmable logic devices, custom integrated circuits for which either the function is unknown or the control status of the equipment in which the integrated circuit will be used is unknown, Fast Fourier Transform (FFT) processors, Static Random-Access Memories (SRAMs), or 'non-volatile memories', having any of the following: Note: 3A001.a.2. does not control integrated circuits designed for civil automobiles or railway train applications. Technical Note: Non-volatile memories' are memories with data retention over a period of time after a power shutdown. "Microprocessor microcircuit" (3) means a "monolithic integrated circuit" or "multichip integrated circuit" containing an arithmetic logic unit (ALU) capable of executing a series of general purpose instructions from an external storage. N.B. 1: The "microprocessor microcircuit" normally does not contain integral user-accessible storage, although storage present on-the-chip may be used in performing its logic function. N.B. 2: This includes chip sets which are designed to operate together to provide the function of a "microprocessor microcircuit". "Microcomputer microcircuit" (3) means a "monolithic integrated circuit" or "multichip integrated circuit" containing an arithmetic logic unit (ALU) capable of executing general purpose instructions from an internal storage, on data contained in the internal storage. N.B.: The internal storage may be augmented by an external storage.		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(一)		3A001 a 2 a		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(二)		3A001 a 2 b		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(三)		3A001 a 2 c		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1				3A001 a 3 3. "Microprocessor microcircuits", "microcomputer microcircuits" and microcontroller microcircuits, manufactured from a compound semiconductor and operating at a clock frequency exceeding 40 MHz;		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1				3A001 a 5 5. Analogue-to-Digital Converter (ADC) and Digital-to-Analogue Converter (DAC) integrated circuits, as follows:		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(一)		3A001 a 5 a		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(一)		3A001 a 5 a 1		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(一)	2	3A001 a 5 a 2		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(一)	3	3A001 a 5 a 3		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(一)	4	3A001 a 5 a 4		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(一)	5	3A001 a 5 a 5		GBL SGBL	SGBL	SGBL	-
7(1)	6	1	1		(二)		3A001 a 5 b		GBL SGBL	SGBL	SGBL	-

Group A	Ireland, United States, Argentina, Italy, United Kingdom, Australia, Austria, Netherlands, Canada, Greece, Switzerland, Sweden, Spain, Czech Republic, Denmark, Germany, New Zealand, Norway, Hungary, Finland, France, Bulgaria, Belgium, Poland, Portugal, Luxembourg
Group B	South Korea
Group C	Other than Group A,B,D
Group D	Afghanistan, Iraq, Iran, North Korea, Democratic Republic of the Congo, Sudan, Somalia, Central Africa, South Sudan, Libya, Lebanon
GBL	General Bulk license
SGBL	Special General Bulk Export License(Renesas has it)
SBL	Special Bulk Export License

7(i)	6	1	—	ホ	(一)	3A001	a	5	b	1	1. A resolution of 10 bit or more but less than 12 bit, with an 'adjusted update rate' exceeding 3 500 MSPS; or	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	ホ	(一)	3A001	a	5	b	2	2. A resolution of 12 bit or more and having any of the following:	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	ホ	(一)	3A001	a	5	b	2	a a. An 'adjusted update rate' exceeding 1 250 MSPS but not exceeding 3 500 MSPS, and having any of the following: 1. A settling time less than 9 ns to arrive at or within 0.024% of full scale from a full scale step; or 2. A 'Spurious Free Dynamic Range' (SFDR) greater than 68 dBc (carrier) when synthesising a full scale analogue signal of 100 MHz; or the highest full scale analogue signal frequency specified below 100 MHz; or b. An 'adjusted update rate' exceeding 3 500 MSPS;	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	ホ	(一)	3A001	a	5	b	2	b	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	ハ		3A001	a	6			6. Electro-optical and 'optical integrated circuits', designed for 'signal processing' and having all of the following: a. One or more than one internal 'laser' diode; b. One or more than one internal light detecting element; and c. Optical waveguides; "Optical integrated circuit" (3) means a "monolithic integrated circuit" or a "hybrid integrated circuit", containing one or more parts designed to function as a photosensor or photoemitter or to perform (an) optical or (an) electro-optical function(s). "Signal processing" (3 4 5 6) means the processing of externally derived information-bearing signals by algorithms such as time compression, filtering, extraction, selection, correlation, convolution or transformations between domains (e.g., fast Fourier transform or Walsh transform). "Laser" (0 1 2 3 5 6 7 8 9) is an item that produces spatially and temporally coherent light through amplification by stimulated emission of radiation.	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	ハ		3A001	a	7			7. Field programmable logic devices having any of the following: Note: 3A001.a.7. includes: - Complex Programmable Logic Devices (CPLDs) - Field Programmable Gate Arrays (FPGAs) - Field Programmable Logic Arrays (FPLAs) - Field Programmable Interconnects (FPICs) N.B. For integrated circuits having field programmable logic devices that are combined with an analogue-to-digital converter, see 3A001.a.14.	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	ハ	(一)	3A001	a	7	a		a. A maximum number of single-ended digital input/outputs of greater than 700; or	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	ハ	(二)	3A001	a	7	b		b. An 'aggregate one-way peak serial transceiver data rate' of 500 Gb/s or greater;	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ		3A001	a	9			9. Neural network integrated circuits;	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ		3A001	a	10			10. Custom integrated circuits for which the function is unknown, or the control status of the equipment in which the integrated circuits will be used is unknown to the manufacturer, having any of the following:	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ	(一)	3A001	a	10	a		a. More than 1 500 terminals;	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ	(二)	3A001	a	10	b		b. A typical 'basic gate propagation delay time' of less than 0.02 ns; or "Basic gate propagation delay time" (3) means the propagation delay time value corresponding to the basic gate used in a "monolithic integrated circuit". For a 'family' of "monolithic integrated circuits", this may be specified either as the propagation delay time per typical gate within the given 'family' or as the typical propagation delay time per gate within the given 'family'. N.B. 1: "Basic gate propagation delay time" is not to be confused with the input/output delay time of a complex "monolithic integrated circuit". N.B. 2: 'Family' consists of all integrated circuits to which all of the following are applied as their manufacturing methodology and specifications except their respective functions: a. The common hardware and software architecture; b. The common design and process technology; and c. The common basic characteristics.	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ	(三)	3A001	a	10	c		c. An operating frequency exceeding 3 GHz;	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ		3A001	a	11			11. Digital integrated circuits, other than those described in 3A001.a.3 to 3A001.a.10 and 3A001.a.12, based upon any compound semiconductor and having any of the following:	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ	(一)	3A001	a	11	a		a. An equivalent gate count of more than 3 000 (2 input gates); or	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ	(二)	3A001	a	11	b		b. A toggle frequency exceeding 1.2 GHz;	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ		3A001	a	12			12. Fast Fourier Transform (FFT) processors having a rated execution time for an N-point complex FFT of less than $(N \log_2 N) / 20$ 480 ms, where N is the number of points;	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ		3A001	a	13			13. Direct Digital Synthesizer (DDS) integrated circuits having any of the following:	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ	(一)	3A001	a	13	a		a. A Digital-to-Analogue Converter (DAC) clock frequency of 3.5 GHz or more and a DAC resolution of 10 bit or more, but less than 12 bit; or	GBL SGBL	SGBL	SGBL	-
7(i)	6	1	—	チ	(二)	3A001	a	13	b		b. A DAC clock frequency of 1.25 GHz or more and a DAC resolution of 12 bit or more;	GBL SGBL	SGBL	SGBL	-

7(1)	6	1	ワ	3A001	a	14	<p>3A001 Electronic items as follows:</p> <p>a. General purpose integrated circuits, as follows:</p> <p>14. Integrated circuits that perform or are programmable to perform all of the following:</p> <p>a. Analogue-to-digital conversions meeting any of the following:</p> <p>1. A resolution of 8 bit or more, but less than 10 bit, with a "sample rate" greater than 1.3 Giga Samples Per Second (GSPS);</p> <p>2. A resolution of 10 bit or more, but less than 12 bit, with a "sample rate" greater than 1.0 GSPS;</p> <p>3. A resolution of 12 bit or more, but less than 14 bit, with a "sample rate" greater than 1.0 GSPS;</p> <p>4. A resolution of 14 bit or more, but less than 16 bit, with a "sample rate" greater than 400 Mega Samples Per Second (MSPS); or</p> <p>5. A resolution of 16 bit or more with a "sample rate" greater than 180 MSPS; and</p> <p>b. Any of the following:</p> <p>1. Storage of digitised data; or</p> <p>2. Processing of digitised data;</p>	GBL SGBL	SGBL	SGBL	-
7(8の2)	6	1	八号の二	3A001	g		<p>g. Solid-state pulsed power switching thyristor devices and 'thyristor modules', using either electrically, optically, or electron radiation controlled switch methods and having any of the following:</p> <p>Note 1: 3A001.g. includes:</p> <ul style="list-style-type: none"> - Silicon Controlled Rectifiers (SCRs) - Electrical Triggering Thyristors (ETTs) - Light Triggering Thyristors (LTTs) - Integrated Gate Commutated Thyristors (IGCTs) - Gate Turn-off Thyristors (GTOs) - MOS Controlled Thyristors (MCTs) - Solidtrons <p>Note 2: 3A001.g. does not control thyristor devices and 'thyristor modules' incorporated into equipment designed for civil railway or "civil aircraft" applications.</p> <p>Technical Note: For the purposes of 3A001.g., a 'thyristor module' contains one or more thyristor devices.</p>	GBL SGBL	SGBL	SGBL	-
7(8の2)	6	1	八号の二	3A001	g	1	<p>1. A maximum turn-on current rate of rise (di/dt) greater than 30 000 A/us and off-state voltage greater than 1 100 V; or</p>	GBL SGBL	SGBL	SGBL	-
7(8の2)	6	1	八号の二	3A001	g	2	<p>2. A maximum turn-on current rate of rise (di/dt) greater than 2 000 A/us and having all of the following:</p> <p>a. An off-state peak voltage equal to or greater than 3 000 V; and</p> <p>b. A peak (surge) current equal to or greater than 3 000 A.</p>	GBL SGBL	SGBL	SGBL	-
7(8の3)	6	1	八号の三	3A001	h		<p>h. Solid-state power semiconductor switches, diodes, or 'modules', having all of the following:</p> <p>1. Rated for a maximum operating junction temperature greater than 488 K (215° C);</p> <p>2. Repetitive peak off-state voltage (blocking voltage) exceeding 300 V; and</p> <p>3. Continuous current greater than 1 A.</p> <p>Note 1: Repetitive peak off-state voltage in 3A001.h. includes drain to source voltage, collector to emitter voltage, repetitive peak reverse voltage and peak repetitive off-state blocking voltage.</p> <p>Note 2: 3A001.h. includes:</p> <ul style="list-style-type: none"> - Junction Field Effect Transistors (JFETs) - Vertical Junction Field Effect Transistors (VJFETs) - Metal Oxide Semiconductor Field Effect Transistors (MOSFETs) - Double Diffused Metal Oxide Semiconductor Field Effect Transistor (DMOSFET) - Insulated Gate Bipolar Transistor (IGBT) - High Electron Mobility Transistors (HEMTs) - Bipolar Junction Transistors (BJTs) - Thyristors and Silicon Controlled Rectifiers (SCRs) - Gate Turn-Off Thyristors (GTOs) - Emitter Turn-Off Thyristors (ETOs) - PIN Diodes - Schottky Diodes <p>Note 3: 3A001.h. does not control switches, diodes, or 'modules', incorporated into equipment designed for civil automobile, civil railway or "civil aircraft" applications.</p> <p>Technical Note: For the purposes of 3A001.h., 'modules' contain one or more solid-state power semiconductor switches or diodes.</p>	GBL SGBL	SGBL	SGBL	-
								GBL SGBL	SGBL	SGBL	-

2. Category 5 Part2 - Product As of 14/April,2021

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輸出令 (export order)	貨物等省令(Ordinance of the Ministry Specifying Goods and Technologies Pursuant to Provisions of the Appended Table 1 of the Export Control Order and the Appended Table of the Foreign Exchange Order)		EU規制番号(EU regulation number)		参考(Reference)	EU規制文言 (英文) (EU regulatory wording)	EU規制の差異注意 (脚) (Attention to differences in EU regulations (required))	I Area Group A	Ri Area Group B	To(2) Area Group C	Chi Area Group D
	条 (articles)	項 (sections)	号 (issues)	イロハ (Japanese alphabet)	細番(fine number)						

9(7)	8	1	九	イ	(十六)		5A002	a			5A002.a.Note2.f	f. Items, where the "information security" functionality is limited to wireless "personal area network" functionality, implementing only published or commercial cryptographic standards;	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	イ	(十七)		5A002	a			5A002.a.Note2.g	g. Mobile telecommunications Radio Access Network (RAN) equipment designed for civil use, which also meet the provisions of paragraphs a.2. to a.4. of the Cryptography Note (Note 3 in Category 5, Part 2), having an RF output power limited to 0.1W (20 dBm) or less, and supporting 16 or fewer concurrent users.	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	イ	(十八)		5A002	a			5A002.a.Note2.h	h. Routers, switches, gateways or relays, where the "information security" functionality is limited to the tasks of "Operations, Administration or Maintenance" ("OAM") implementing only published or commercial cryptographic standards; or	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	イ	(十九)		5A002	a			5A002.a.Note2.i	i. General purpose computing equipment or servers, where the "information security" functionality meets all of the following:	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	イ	(十九)	1	5A002	a			5A002.a.Note2.i.1	1. Uses only published or commercial cryptographic standards; and	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	イ	(十九)	2	5A002	a			5A002.a.Note2.i.2	2. Is any of the following: a. Integral to a CPU that meets the provisions of Note 3 to Category 5-Part 2; b. Integral to an operating system that is not specified in 5D002; or c. Limited to "OAM" of the equipment.	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	イ	(二十)		5A002	a			5A002.a.Note2.j	j. Items specially designed for a 'connected civil industry application', meeting all of the following:	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	イ	(二十)	1	5A002	a			5A002.a.Note2.j.1	1. Being any of the following:	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	イ	(二十)	1-	5A002	a			5A002.a.Note2.j.1.a	a. A network-capable endpoint device meeting any of the following: 1. The "information security" functionality is limited to securing 'non-arbitrary data' or the tasks of "Operations, Administration or Maintenance" ("OAM"); or 2. The device is limited to a specific 'connected civil industry	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	イ	(二十)	1=	5A002	a			5A002.a.Note2.j.1.b	b. Networking equipment meeting all of the following: 1. Being specially designed to communicate with the devices specified in paragraph j.1.a. above; and 2. The "information security" functionality is limited to supporting the 'connected civil industry application' of devices specified in paragraph j.1.a. above, or the tasks of "OAM" of this networking equipment or of other items specified in paragraph j. of this Note; and	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	イ	(二十)	2	5A002	a			5A002.a.Note2.j.2	2. Where the "information security" functionality implements only published or commercial cryptographic standards, and the cryptographic functionality cannot easily be changed by the user;	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ロ			5A002	b			5A002.a.Note2.j.1.a	b. Being a 'cryptographic activation token'; Technical Note: A 'cryptographic activation token' is an item designed or modified for any of the following:	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ロ	(一)	1	5A002	b	1			1. Converting, by means of "cryptographic activation", an item not specified in Category 5 - Part 2 into an item specified in 5A002.a. or 5D002.c.1., and not released by the Cryptography Note (Note 3 in Category 5 - Part 2); or	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ロ	(二)	2	5A002	b	2			2. Enabling, by means of "cryptographic activation", additional functionality specified in 5A002.a. of an item already specified in Category 5 - Part 2.	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ハ			5A002	c			5A002.a.Note2.j.2	c. Designed or modified to use or perform "quantum cryptography"; Technical Note: "Quantum cryptography" is also known as Quantum Key Distribution (QKD).	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ニ			5A002	d			5A002.a.Note2.j.2	d. Designed or modified to use cryptographic techniques to generate the channelising codes, scrambling codes or network identification codes, for systems using ultra-wideband modulation techniques and having any of the following:	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ニ	(一)	1	5A002	d	1			1. A bandwidth exceeding 500 MHz; or	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ニ	(二)	2	5A002	d	2			2. A "fractional bandwidth" of 20% or more;	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ホ			5A002	e			5A002.a.Note2.j.2	e. Designed or modified to use cryptographic techniques to generate the spreading code for "spread spectrum" systems, other than those specified in 5A002.d., including the hopping code for "frequency hopping" systems.	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ヘ			5A002				Cat5part2のノート3 Cryptography Note	CATEGORY 5 - TELECOMMUNICATIONS AND "INFORMATION SECURITY" Part 2 - "INFORMATION SECURITY" Note 3: Cryptography Note 5A002, 5D002.a.1., 5D002.b. and 5D002.c.1. do not control items as follows:	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ヘ	(一)		5A002				Note3a	a. Items that meet all of the following: 1. Generally available to the public by being sold, without restriction, from stock at retail selling points by means of any of the following: a. Over-the-counter transactions; b. Mail order transactions; c. Electronic transactions; or d. Telephone call transactions; 2. The cryptographic functionality cannot easily be changed by the user; 3. Designed for installation by the user without further substantial support by the supplier; and	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	九	ヘ	(二)		5A002				Note3b	b. Hardware components or 'executable software', of existing items described in paragraph a. of this Note, that have been designed for these existing items, meeting all of the following: 1. "Information security" is not the primary function or set of functions of the component or 'executable software'; 2. The component or 'executable software' does not change any cryptographic functionality of the existing items, or add new cryptographic functionality to the existing items;	GBL SGBL	SGBL	SGBL	-
9(7)	8	1	十				5A004				5A004	5A004 Systems, equipment and components for defeating, weakening or bypassing "information security", as follows:	GBL SGBL	SGBL	SGBL	-

8	1	十	イ			5A004	a													a. Designed or modified to perform 'cryptanalytic functions'. Note: 5A004.a. includes systems or equipment, designed or modified to perform 'cryptanalytic functions' by means of reverse engineering. Technical Note: Cryptanalytic functions' are functions designed to defeat cryptographic mechanisms in order to derive confidential variables or sensitive data, including clear text, passwords or cryptographic keys. b. Items, not specified in 4A005 or 5A004.a., designed to perform all of the following: 1. 'Extract raw data' from a computing or communications device; and 2. Circumvent "authentication" or authorisation controls of the device, in order to perform the function described in 5A004.b.1. Technical Note: Extract raw data' from a computing or communications device means to retrieve binary data from a storage medium (e.g., RAM, flash or hard disk) of the device without interpretation by the device's operating system or filesystem. Note1: 5A004.b. does not control systems or equipment specially designed for		GBL SGBL	SGBL	SGBL	-
8	1	十	ロ			5A004	b													b. Items limited to logical data extraction; c. Data extraction items using chip-off or JTAG; or d. Items specially designed and limited to jail-breaking or rooting.		GBL SGBL	SGBL	SGBL	-
8	1	十	ハ	(一)		5A004	b													a. Debuggers, hypervisors;		GBL SGBL	SGBL	SGBL	-
8	1	十	ニ	(二)		5A004	b													b. Items limited to logical data extraction;		GBL SGBL	SGBL	SGBL	-
8	1	十	三	(三)		5A004	b													c. Data extraction items using chip-off or JTAG; or		GBL SGBL	SGBL	SGBL	-
8	1	十	四	(四)		5A004	b													d. Items specially designed and limited to jail-breaking or rooting.		GBL SGBL	SGBL	SGBL	-

3. Category 5 - Tech As of 14/April,2021 Please check SGBL (Renesas Electronics Corporation has it)

外為令 (External order)	省令(Ministerial ordinance)			EU規制番号 (EU regulation number) 5桁(5 digits)	EU規制文言(英文)(EU regulatory wording)	EU規制の差異 注意(要) (Attention to differences in EU regulations (required))	I Area Group A	Ri Area Group B	To(2) Area Group C	Chi Area Group D
	条 (articles)	項 (sections)	号 (issues)							
9	21	1	一	5.E.1	"Technology" according to the General Technology Note for the "development", "production" of equipment, functions or features specified by 5.A.1. or "software" specified by 5.D.1.a. or 5.D.1.e.;		GBL SGBL	SGBL	SGBL	-
9(1)	21	1	一	5.E.1	"Technology" "required" for the "development" or "production" of equipment, functions or features specified by 5.A.1.b.3		GBL SGBL	SGBL	SBL	-
9(1)	21	1	二	5.E.1	"Technology" "required" for the "use" (excluding operation) of equipment, functions or features specified by 5.A.1.a~j/ 5.A.2.a~e./ 5.A.3./ 5.A.4. / 5.B.2/		GBL SGBL	SGBL	SGBL	-
9(1)	21	1	三	5.E.1	"Technology" "required" for the "use" (excluding operation) of equipment, functions or features specified by 5.A.2.a~e./ 5.A.3./ 5.A.4. / 5.B.2/		GBL SGBL	SGBL	SGBL	-
	21	1	四	5.E.1	"Technology" "required" for the "use" (excluding operation) of equipment, functions or features specified by 5.A.1.a~j		GBL SGBL	SGBL	SGBL	-
	21	1	五	5.D.1	"Program" required for the "development", "production" of equipment, functions or features specified by 5.A.1.b.3		GBL SGBL	SGBL	SBL	-
9(1)	21	1	六	5.D.1	"Program" required for the "development", "production" of equipment, functions or features specified by 5.A.1.a~d		GBL SGBL	SGBL	SGBL	-
9(1)	21	1	七	5.D.1	"Program" required for the "use" of equipment, functions or features specified by 5.A.2./5.A.3./5.A.4.		GBL SGBL	SGBL	SGBL	-
	21	1	八	5.D.1	"Program" required for the "development", "production" of equipment, functions or features specified by 5.A.1.		GBL SGBL	SGBL	SGBL	-

	21	1	八号二	5.D.1	"Program" required for the "use" of equipment, functions or features specified by 5.A.1./5.A.2/5.A.3/5.A.4		GBL SGBL		SGBL	-
9(1)	21	1	九	Note 5.D.2.c	"Program" having the characteristics of, or performing or simulating the functions of, any of the following: 1. Equipment specified by 5.A.2.a., 5.A.2.c., 5.A.2.d. or 5.A.2.e.; Note 5.D.2.c.1. does not apply to "software" limited to the tasks of "OAM" implementing only published or commercial cryptographic standards.		GBL SGBL	SGBL	SGBL	-
9(1)	21	1	十一	5.E.1	"Program" required for the "development", "production", "use" of equipment, functions or features specified by 5.A.1. d		GBL SGBL	SGBL	SBL	-
9(1)	21	1	十二	5.E.1	"Program" required for the "development", "production", "use" of equipment, functions or features specified by 5.B.1.a./b/5.a.2		GBL SGBL	SGBL	SGBL	-
9(1)	21	1	十三	5.E.1	"Program" required for the "development", "production" "use" of equipment, functions or features specified by 5.B.1.a./b		GBL SGBL	SGBL	SGBL	-
9(1)	21	1	十六	-	"Technology" that has the function specified by 5.A.2, activates the encryption function of the program and the product.	要 (Required)	GBL SGBL	SGBL	SGBL	-
9(1)	21	1	十七	-	"Program" that has the function specified by 5.A.2, activates the encryption function of the program and the product.	要 (Required)	GBL SGBL	SGBL	SGBL	-