

Integrated Device Technology, Inc. 6024 Silver Creek Valley Road, San Jose, CA - 95138

# PRODUCT/PROCESS CHANGE NOTICE (PCN)

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PCN #: <b>A1808-02</b>	DATE: 5-Sep	-2018	MEANS OF DISTIN	NGUISHING CH	ANGED DEVICES:
Product Affected: 4RCD0229KB1ATG			☐ Product Mark		
4RCD0229KB1ATG	3		Back Mark	Assembly lot#	will have a suffix "E"
4RCD0229KB1ATG	M		☐ Date Code		
4RCD0229KB1ATG	3/M		□ Other		
4RCD0229KB1ATG	8/B				
Date Effective: 5-Dec-2018					
Contact: IDT PCN DESK			Attachment:	Yes	☐ No
E-mail: pcndesk@idt.com				ntact your local sa quest & availabili	ales representative for ity.
DESCRIPTION AND PURPOSE OF CH	ANGE:				
<ul> <li>□ Die Technology</li> <li>□ Wafer Fabrication Process</li> <li>□ Assembly Process</li> <li>□ Equipment</li> <li>■ Material</li> <li>□ Testing</li> <li>□ Manufacturing Site</li> <li>□ Data Sheet</li> <li>□ Other</li> </ul>	substrate materal There is no characteristics.	rial, ETS ange to th	our customers that II for the select product ne moisture performate qualification data for	ets.	lly qualified an alternate
RELIABILITY/QUALIFICATION SUM. Refer to qualification data shown in attach					
CUSTOMER ACKNOWLEDGMENT O	F RECEIPT:				
IDT records indicate that you require writte to grant approval or request additional info it will be assumed that this change is accep IDT reserves the right to ship either version on the earlier version has been depleted.	rmation. If IDT table.	does not	receive acknowledge	ement within 30 da	ays of this notice
Customer:			Approval for s	shipments prid	or to effective date.
Name/Date:		E-1	Mail Address:		
Title:		Ph	one# /Fax# :		
CUSTOMER COMMENTS:					
IDT ACKNOWLEDGMENT OF RECE	PT:				
RECD. BY:			DATE:		

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#### **ATTACHMENT I - PCN # : A1808-02**

**PCN Type:** Adding Alternate Materials

**Data Sheet Change:** None

No change in moisture sensitivity level (MSL)

### **Detail Of Change:**

This notice is to advise our customers that IDT has successfully qualified an alternate substrate material, ETS for the select products. Refer Table 1 for comparizon of the existing substrate and the new alternate substrate.

There is no change to the moisture performance of this package.

Table 1: Substrate Material Comparizon

Description	Current Substrate	Alternate Substrate		
Substrate Structure	6 Layers MSAP	4 Layers ETS		
Bottom Solder Mask Layer	AUS320	SR1		
Layer 1-2	GHPL830NS	GHPL830NS		
Core Layer	HL832NS	No Core		
Layer 3-4	GHPL830NS	GHPL830NS		
Bottom Solder Mask Layer	AUS320	SR1		



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#### **ATTACHMENT I - PCN # : A1808-02**

### **Qualification Information and Qualification Data:**

**Affected Packages:** FCCSP-253

**Qual Plan & Results:** Tests are in accordance with JEDEC47 recommended tests.

**Qualification Vehicle:** FCCSP-253 (3 lots)

		Test Results (Rej / SS)			
Test Description	Test Method	Lot 1	Lot 2	Lot 3	
* Temperature Cycling (-55°C to 125°C, 700 cycles)	JESD22-A104	0/25	0/25	0/25	
* HAST - biased (130 °C/85% RH, 96 Hrs)	JESD22-A110	0/25	0/25	0/25	
High Temperature Storage Bake (150°C, 1000 Hrs)	JESD22-A103	0/25	0/25	0/25	
Solder Ball Shear Test	JESD22-B117	0/5	0/5	0/5	
Moisture Sensitivity Level, MSL	J-STD-20 / MSL 3, 260 °C	0/25	0/25	0/25	

<sup>\*</sup> Tests were subjected to Preconditioning per JESD22-A113 prior to stress test