To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

HANNEL MOS FIELD EFFECT POWER TRANSISTOR

Phase-out/Discontinued

2SK855

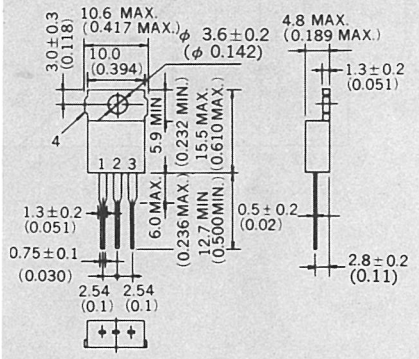
DESCRIPTION The 2SK855 is N-Channel MOS Field Effect Power Transistor designed for switching power supplies and DC–DC converters.

FEATURES

- High V_{DSS} 500 V
- Low $R_{DS(on)}$
- No Secondary Breakdown

PACKAGE DIMENSIONS

in millimeters (inches)



1. Gate
2. Drain
3. Source
4. Fin (Drain)

JEDEC: TO-220AB

ABSOLUTE MAXIMUM RATINGS

Maximum Temperatures

- Storage Temperature . . . . . –55 to +150 °C
- Channel Temperature . . . . . 150 °C Maximum

Maximum Power Dissipation

- Total Power Dissipation (T_C = 25 °C) . . . 50 W

Maximum Voltages and Currents (T_a = 25 °C)

V_{DSS}	Drain to Source Voltage	500	V
V_{GSS}	Gate to Source Voltage	±20	V
$I_{D(DC)}$	Drain Current (DC)*	±5	A
$I_{D(pulse)}$	Drain Current (pulse)**	±20	A

* T_C = 25 °C

** PW ≦ 100 μs, Duty Cycle ≦ 2 %

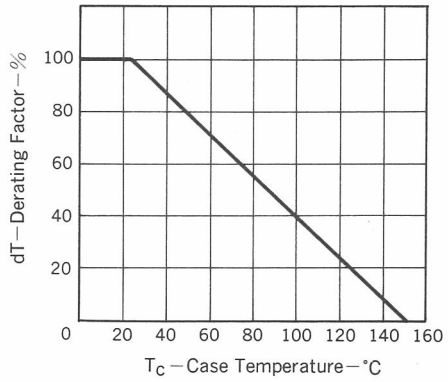
ELECTRICAL CHARACTERISTICS (T_a = 25 °C)

SYMBOL	CHARACTERISTIC	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
$R_{DS(on)}$	Drain to Source On-State Resistance		1.2	1.5	Ω	V_{GS} = 10 V, I_D = 2.5 A
$V_{GS(off)}$	Gate to Source Cutoff Voltage	1.5		3.5	V	V_{DS} = 10 V, I_D = 1 mA
$\lvert y_{fs} \rvert$	Forward Transfer Admittance	2.5	3.0		S	V_{DS} = 10 V, I_D = 2.5 A
I_{DSS}	Drain Leakage Current			100	μA	V_{DS} = 500 V, V_{GS} = 0
I_{GSS}	Gate to Source Leakage Current			±100	nA	V_{GS} = ±20 V, V_{DS} = 0
C_{iss}	Input Capacitance		700		pF	V_{DS} = 10 V V_{GS} = 0 f = 1 MHz
C_{oss}	Output Capacitance		220		pF	
C_{rss}	Reverse Transfer Capacitance		75		pF	
$t_{d(on)}$	Turn-On Delay Time		6		ns	I_D = 2.5 A, V_{DD} ≒ 150 V R_L = 60 Ω, $V_{GS(on)}$ = 10 V R_{in} = 10 Ω See Test Circuit
t_r	Rise Time		15		ns	
$t_{d(off)}$	Turn-Off Delay Time		30		ns	
t_f	Fall Time		7		ns	

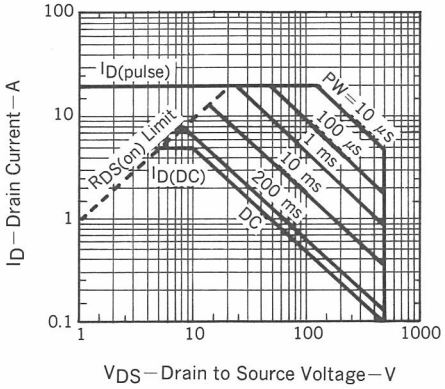
Phase-out/Discontinued

TYPICAL CHARACTERISTICS (T_a = 25 °C)

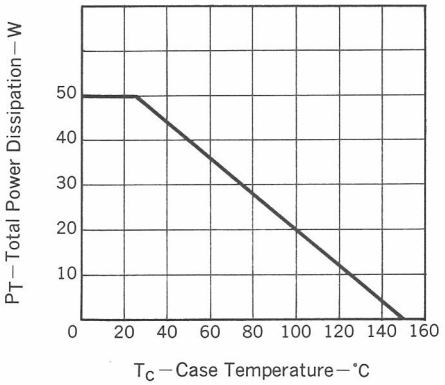
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



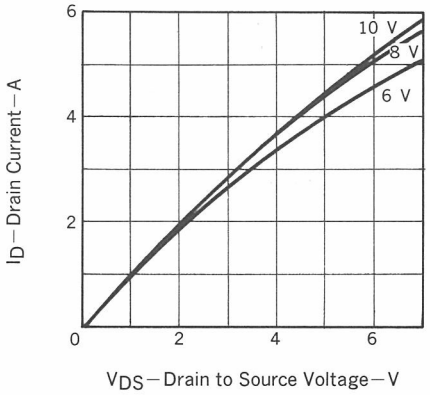
FORWARD BIAS SAFE OPERATING AREA



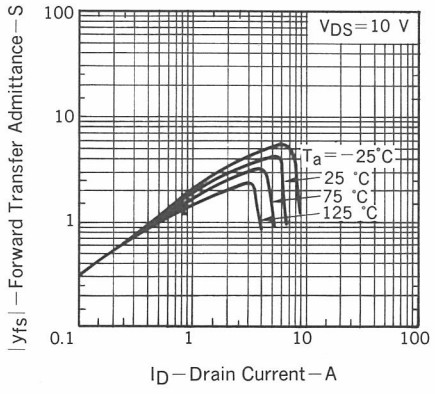
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



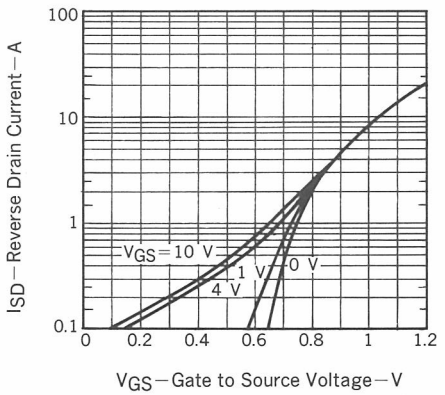
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



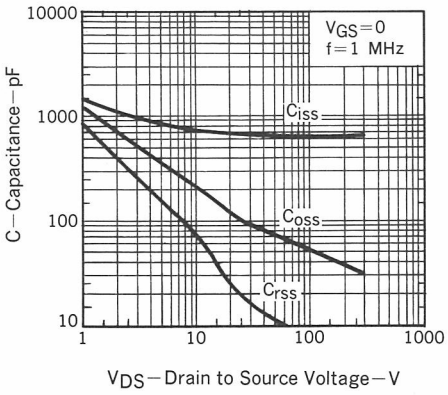
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



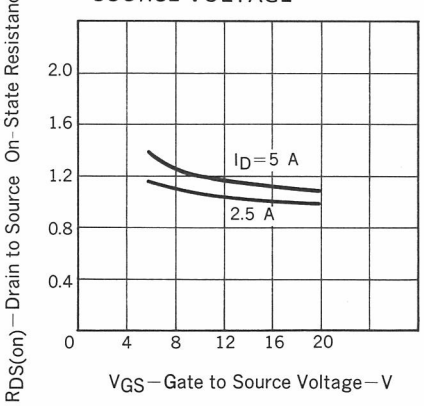
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



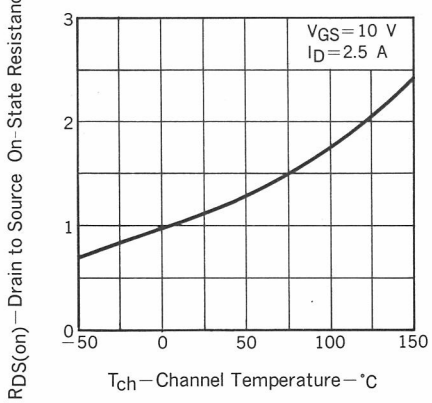
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE

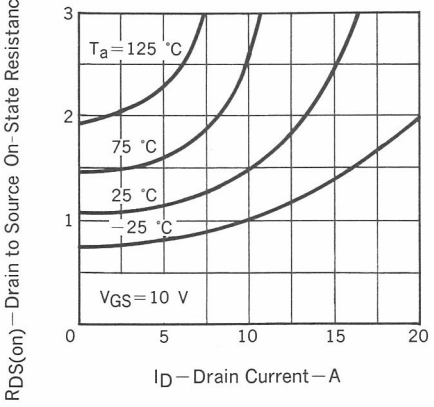


DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE

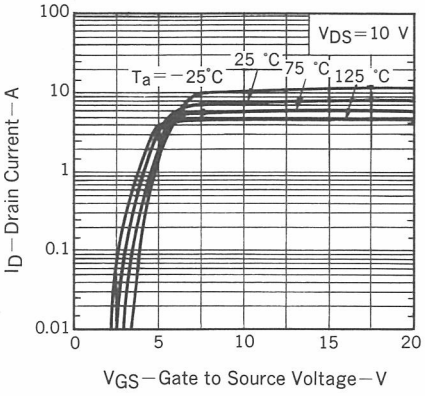


Phase-out/Discontinued

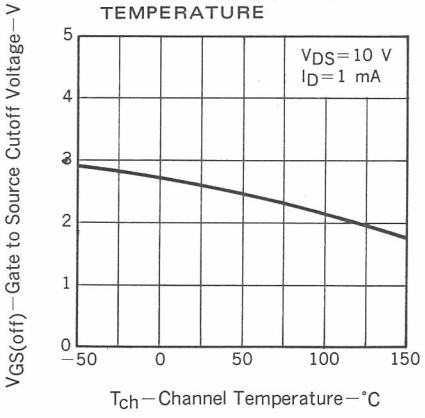
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



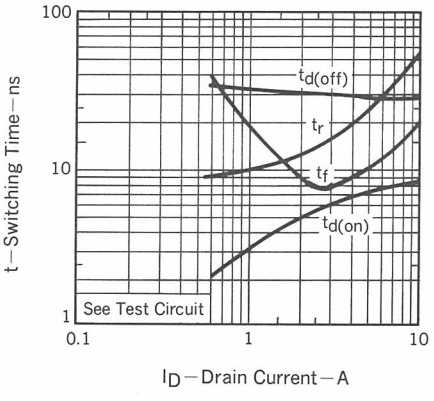
TRANSFER CHARACTERISTICS



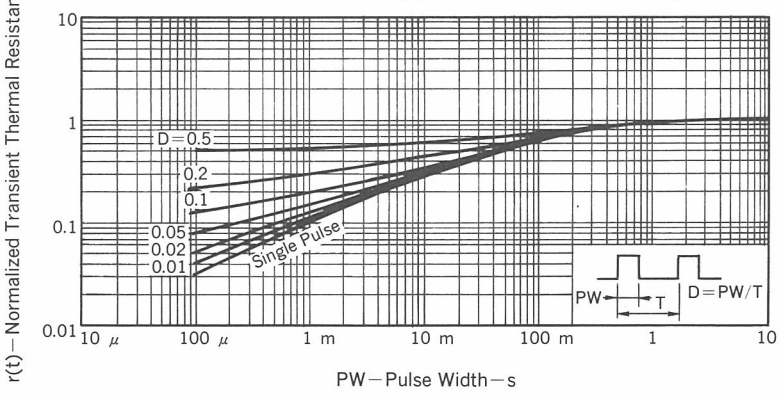
GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE



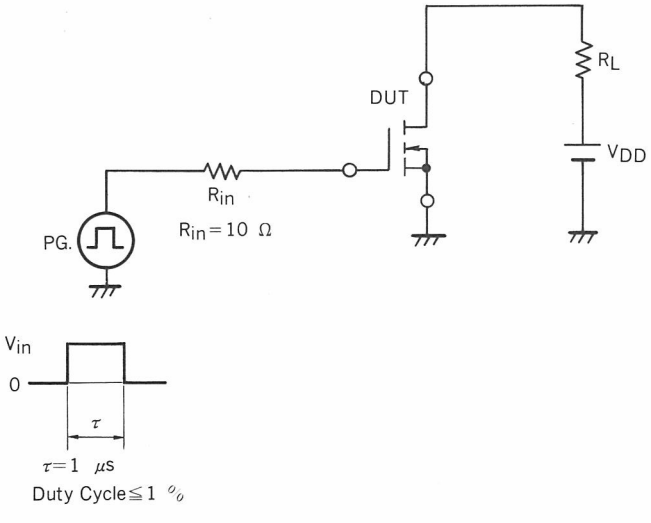
SWITCHING TIME vs. DRAIN CURRENT

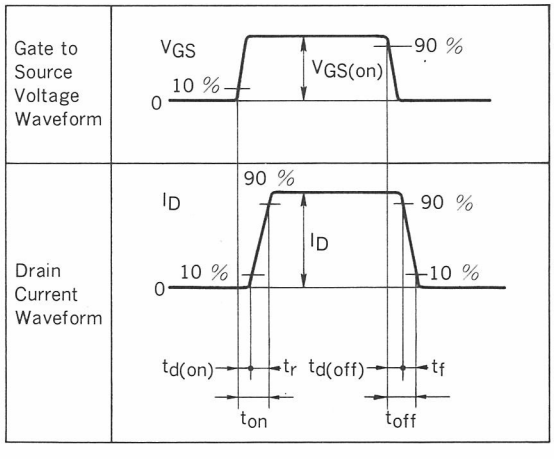


NORMALIZED TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



SWITCHING TIME TEST CIRCUIT





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